MAX4480–MAX4483

Single/Dual/Quad, Low-Cost, Single-Supply, Rail-to-Rail Op Amps with Shutdown

General Description

The MAX4480–MAX4483 low-cost, general-purpose op amps offer rail-to-rail outputs, draw only 50µA of quiescent current, and operate from a single +2.5V to +5.5V supply. For additional power conservation, the MAX4481 offers a low-power shutdown mode that reduces supply current to 0.5µA (max) and puts the amplifier's output in a high-impedance state. These devices are unity-gain stable with capacitive loads up to 400pF. The MAX4480–MAX4483 are specified to +125°C, making them suitable for use in a variety of harsh environments.

The MAX4480 is a single amplifier offered in a tiny 5-pin SC70 package. The MAX4481 is a single amplifier with a low-power shutdown mode that reduces supply current to < 0.5µA and comes in a 6-pin SC70 package. The MAX4482 is a dual amplifier and comes in the space-saving 8-pin SOT23 or µMAX® package. The MAX4483 is a quad amplifier and comes in a 14-pin TSSOP package. All devices are specified for operation across the -40°C to +125°C automotive temperature range.

Applications

- Single-Supply Zero-Crossing Detectors
- Instruments and Terminals
- Portable Communications
- Electronic Ignition Modules
- Infrared Receivers
- Sensor Signal Detection

Selector Guide

PART	NO. OF AMPLIFIERS PER PACKAGE	SHUTDOWN MODE
MAX4480	1	No
MAX4481	1	Yes
MAX4482	2	No
MAX4483	4	No

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Features

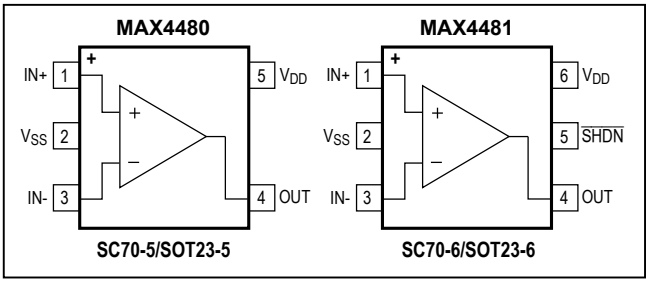
- Single +2.5V to +5.5V Supply Voltage Range
- 50µA Quiescent Current per Amplifier
- 0.5µA (max) Shutdown Mode (MAX4481)
- Available in Space-Saving Packages
  - 5-Pin SC70 (MAX4480)
  - 6-Pin SC70 (MAX4481)
  - 8-Pin SOT23 (MAX4482)
- 105dB AV_{OL} with 5kΩ Load
- 0.005% THD with 100kΩ Load
- Rail-to-Rail Output Voltage Swing
- 3.0mA of Sink and Source Load Current
- Unity-Gain Stable up to C_{LOAD} = 400pF

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4480AXK+T	-40°C to +125°C	5 SC70	ABU
MAX4480AUK+T	-40°C to +125°C	5 SOT23	ADPJ
MAX4481AXT+T	-40°C to +125°C	6 SC70	AAN
MAX4481AUT+T	-40°C to +125°C	6 SOT23	AAOS
MAX4481MXT/PR3+	-55°C to +110°C	6 SC70	AEE
MAX4482AKA+T	-40°C to +125°C	8 SOT23	AAEJ
MAX4482ASA+	-40°C to +125°C	8 SO	—
MAX4482AUA+	-40°C to +125°C	8 FMAX	—
MAX4483ASD+	-40°C to +125°C	14 SO	—
MAX4483AUD+	-40°C to +125°C	14 TSSOP	—

+Denotes a lead(Pb)-free/RoHS-compliant package.
T = Tape and reel.

Pin Configurations



Pin Configurations continued at end of data sheet.

maxim integrated™

Absolute Maximum Ratings

Power-Supply Voltage (V_{DD} to V_{SS}).....-0.3V to +6V
All Other Pins.....(V_{SS} - 0.3V) to (V_{DD} + 0.3V)
Output Short-Circuit Duration
($\overline{OUT}$ shorted to V_{SS} or V_{DD}).....Continuous
Continuous Power Dissipation (T_A = +70°C)
5-Pin SC70 (derate 3.1mW/°C above +70°C)..... 247mW
6-Pin SC70 (derate 3.1mW/°C above +70°C).....245mW
5-Pin SOT23 (derate 3.1mW/°C above +70°C).....247mW
6-Pin SOT23 (derate 8.7mW/°C above +70°C).....696mW
8-Pin SOT23 (derate 5.1mW/°C above +70°C).....408mW
8-Pin µMAX (derate 4.5mW/°C above +70°C).....362mW
8-Pin SO (derate 5.9mW/°C above +70°C).....471mW
14-Pin TSSOP (derate 9.1mW/°C above +70°C).....727mW
14-Pin SO (derate 8.33mW/°C above +70°C).....667mW
Operating Temperature Range .....-40°C to +125°C
Military Operating Temperature Range.....-55°C to +110°C
Junction Temperature.....150°C
Storage Temperature Range.....-65°C to +150°C
Lead Temperature (soldering, 10s).....+3000°C
Soldering Temperature (reflow).....+240°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L ≥ 1MΩ connected to V_{DD}/2, $\overline{SHDN}$ = V_{DD} (MAX4481 only), **T_A = +25°C**, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	V_{DD}	Inferred from PSRR test		2.5		5.5	V
Supply Current per Amplifier	I_{DD}	V_{DD} = 2.5V			45		µA
		V_{DD} = 5.0V			50	100	
Supply Current in Shutdown	I_{SHDN}	$\overline{SHDN}$ = V_{SS} (MAX4481 only)			0.05	0.5	µA
Input Offset Voltage	V_{OS}				±1	±5.5	mV
Input Bias Current	I_B	(Note 1)			±0.1	±100	pA
Input Offset Current	I_{OS}	(Note 1)			±0.1	±100	pA
Input Resistance	R_{IN}	Differential or common mode			1000		MΩ
Input Common-Mode Voltage Range	V_{CM}	Inferred from CMRR test		V_{SS}		V_{DD} - 1.3	V
Common-Mode Rejection Ratio	CMRR	V_{SS} ≤ V_{CM} ≤ V_{DD} - 1.3V		71	86		dB
Power-Supply Rejection Ratio	PSRR	2.5V ≤ V_{DD} ≤ 5.5V		82	92		dB
Large-Signal Voltage Gain	A_{VOL}	V_{SS} + 0.02V ≤ V_{OUT} ≤ V_{DD} - 0.03V	R_L = 100kΩ		110		dB
		V_{SS} + 0.10V ≤ V_{OUT} ≤ V_{DD} - 0.20V	R_L = 5kΩ	94	105		
Output Voltage High	V_{OH}	Specified as V_{DD} - V_{OUT}	R_L = 100kΩ		4		mV
			R_L = 5kΩ		80	150	
Output Voltage Low	V_{OL}	Specified as V_{OUT} - V_{SS}	R_L = 100kΩ		1		mV
			R_L = 5kΩ		8	30	
Output Short-Circuit Current	I_{SC}	Sourcing			3		mA
		Sinking			17		
Shutdown Mode Output Leakage	$I_{OUTSHDN}$	Device in shutdown mode, $\overline{SHDN}$ = V_{SS}, V_{SS} < V_{OUT} < V_{CC} (MAX4481 only)			±0.01	±0.1	µA
$\overline{SHDN}$ Logic Low	V_{IL}	MAX4481 only				0.3 x V_{DD}	V
$\overline{SHDN}$ Logic High	V_{IH}	MAX4481 only		0.7 x V_{DD}			V
$\overline{SHDN}$ Input Current	I_{IL}, I_{IH}	$\overline{SHDN}$ = V_{DD} or V_{SS} (MAX4481 only)		±0.001	±1	±500	nA
Gain-Bandwidth Product	GBW				140		kHz

Electrical Characteristics (continued)

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L ≥ 1MΩ connected to V_{DD}/2, $\overline{SHDN}$ = V_{DD} (MAX4481 only), **T_A = +25°C**, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Phase Margin	φM				70		degrees
Gain Margin					30		dB
Slew Rate	SR				80		V/ms
Input Voltage Noise Density	e_n	f = 10kHz			100		nV/√Hz
Input Current Noise Density	i_n	f = 10kHz			1		nV/√Hz
Capacitive-Load Stability	C_{LOAD}	A_V = +1V/V			400		pF
Shutdown Delay Time	t_{SHDN}	MAX4481 only			0.4		µs
Enable Delay Time	t_{EN}	MAX4481 only			12		µs
Power-On Time	t_{ON}				15		µs
Input Capacitance	C_{IN}				2.0		pF
Total Harmonic Distortion	THD	f = 1kHz, V_{OUT} = $2V_{P-P}$, A_V = +1V/V	R_L = 100kΩ		0.005		%
Settling Time to 0.1%	t_S	V_{OUT} = 2V step			50		µs

Electrical Characteristics

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = 0V, V_{OUT} = V_{DD}/2, R_L ≥ 1MΩ connected to V_{DD}/2, $\overline{SHDN}$ = V_{DD} (MAX4481 only), **TA = -40°C to +125°C**, unless otherwise noted.)

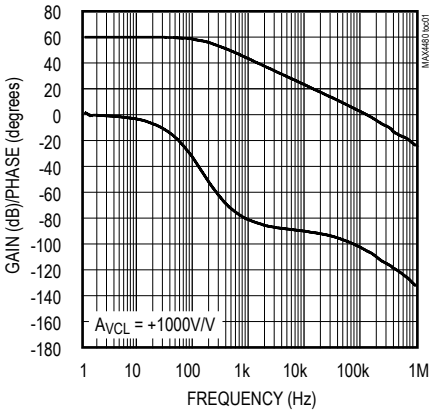
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage Range	V_{DD}	Inferred from PSRR test		2.5		5.5	V
Supply Current per Amplifier	I_{DD}					120	µA
Supply Current in Shutdown	I_{SHDN}	$\overline{SHDN}$ = V_{SS} (MAX4481 only)				1.0	µA
Input Offset Voltage	V_{OS}					9	mV
Input Offset Voltage Drift	TCV_{OS}				±3		µV/°C
Input Bias Current	I_B	(Note 1)				±100	pA
Input Offset Current	I_{OS}	(Note 1)				±100	pA
Input Common-Mode Voltage Range	V_{CM}	Inferred from CMRR test		V_{SS}		V_{DD} - 1.4	V
Common-Mode Rejection Ratio	CMRR	V_{SS} ≤ V_{CM} ≤ V_{DD} - 1.4V		67			dB
Power-Supply Rejection Ratio	PSRR	2.5V ≤ V_{DD} ≤ 5.5V		77			dB
Shutdown Mode Output Leakage	$I_{OUTSHDN}$	Device in shutdown mode, $\overline{SHDN}$ = V_{SS}, V_{SS} < V_{OUT} < V_{CC} (MAX4481 only)	-40°C to +85°C			±0.5	µA
			+85°C to +125°C			±2.5	
$\overline{SHDN}$ Logic Low	V_{IL}	MAX4481 only				0.3 x V_{DD}	V
$\overline{SHDN}$ Logic High	V_{IH}	MAX4481 only		0.7 x V_{DD}			V
$\overline{SHDN}$ Input Current	I_{IL}, I_{IH}	$\overline{SHDN}$ = V_{DD} or V_{SS} (MAX4481 only)				1	µA
Large-Signal Voltage Gain	A_{VOL}	V_{SS} + 0.1V ≤ V_{OUT} ≤ V_{DD} - 0.20V, R_L = 5kΩ		84			dB
Output Voltage High	V_{OH}	Specified as V_{DD} - V_{OUT}, R_L = 5kΩ				200	mV
Output Voltage Low	V_{OL}	Specified as V_{OUT} - VSS, R_L = 5kΩ				50	mV

Note 1: Guaranteed by design.
Note 2: Specifications are 100% tested at T_A = +25°C (exceptions noted). All temperature limits are guaranteed by design.

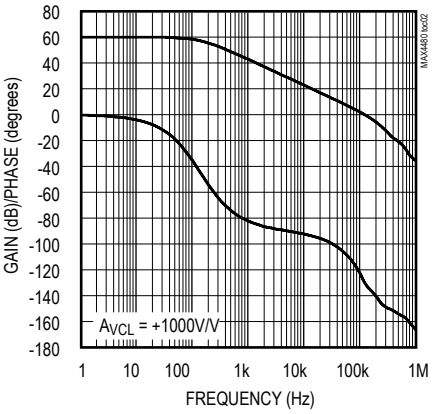
Typical Operating Characteristics

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = V_{DD}/2, $V_{\overline{SHDN}}$ = 5V, R_L ≥ 1MΩ connected to V_{DD}/2, T_A = +25°C, unless otherwise noted.)

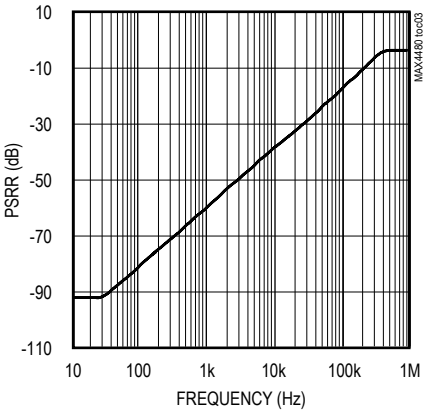
GAIN AND PHASE vs. FREQUENCY (NO LOAD)



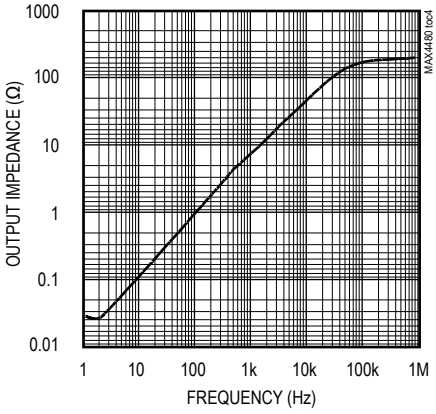
GAIN AND PHASE vs. FREQUENCY (C_L = 400pF)



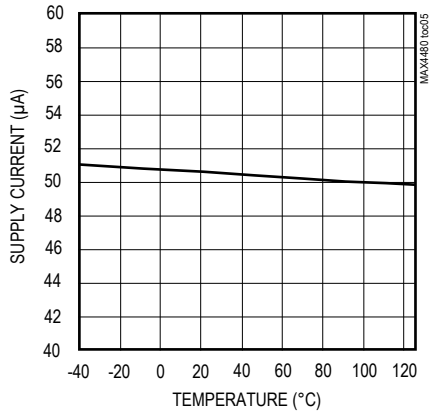
POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



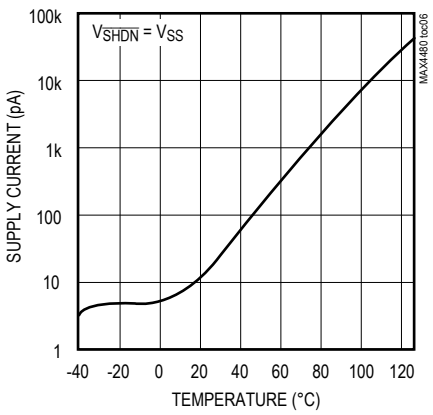
OUTPUT IMPEDANCE vs. FREQUENCY



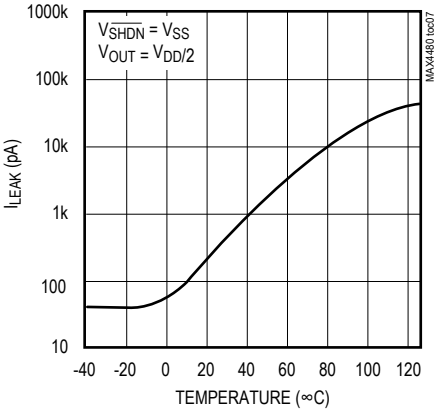
SUPPLY CURRENT vs. TEMPERATURE



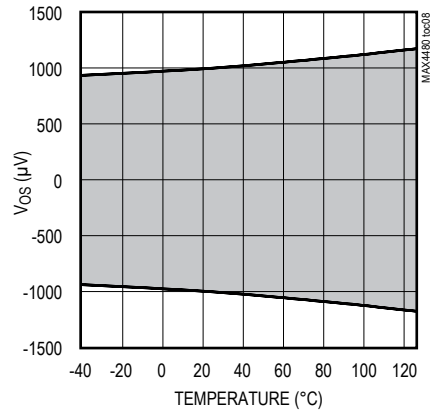
MAX4481 SHUTDOWN SUPPLY CURRENT vs. TEMPERATURE



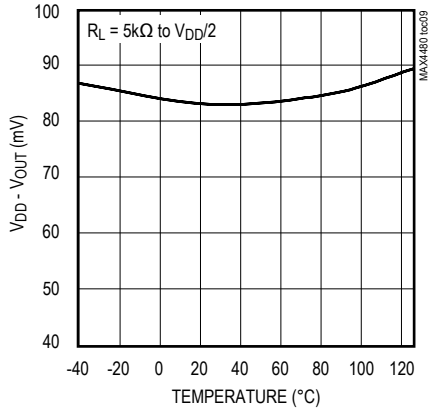
MAX4481 OUTPUT LEAKAGE CURRENT vs. TEMPERATURE



INPUT OFFSET VOLTAGE vs. TEMPERATURE



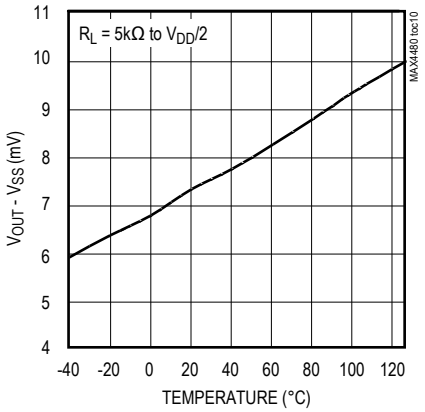
OUTPUT VOLTAGE SWING HIGH vs. TEMPERATURE



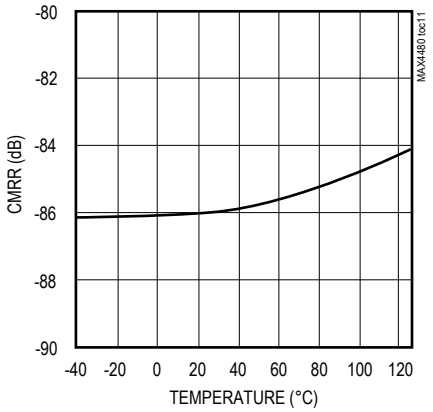
Typical Operating Characteristics (continued)

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = V_{DD}/2, $V_{\overline{SHDN}}$ = 5V, R_L ≥ 1MΩ connected to V_{DD}/2, T_A = +25°C, unless otherwise noted.)

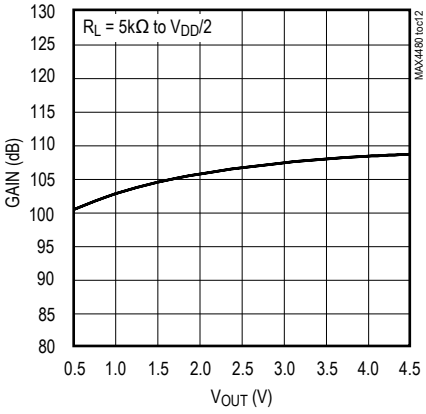
OUTPUT VOLTAGE SWING LOW vs. TEMPERATURE



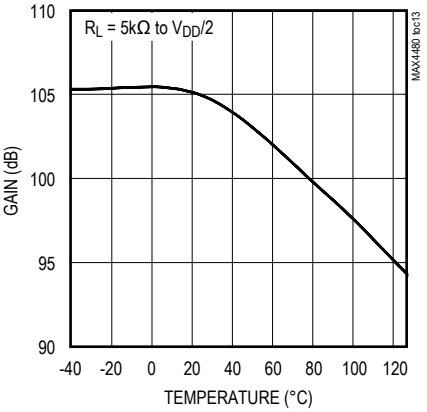
COMMON-MODE REJECTION RATIO vs. TEMPERATURE



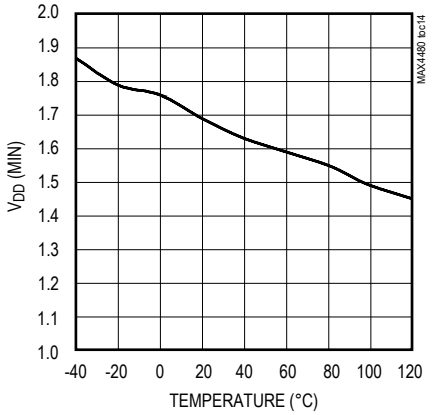
LARGE-SIGNAL GAIN vs. OUTPUT VOLTAGE



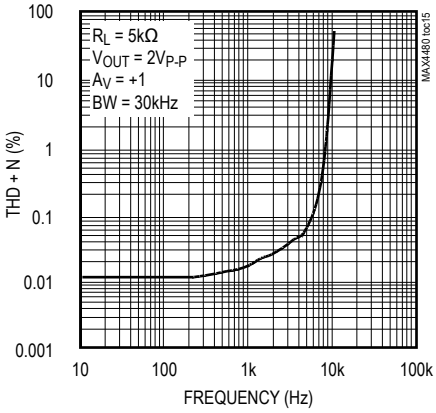
LARGE-SIGNAL GAIN vs. TEMPERATURE



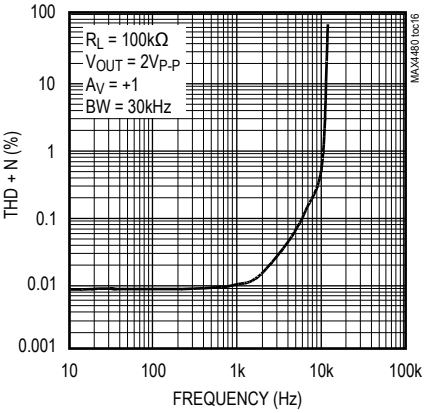
MINIMUM OPERATING VOLTAGE vs. TEMPERATURE



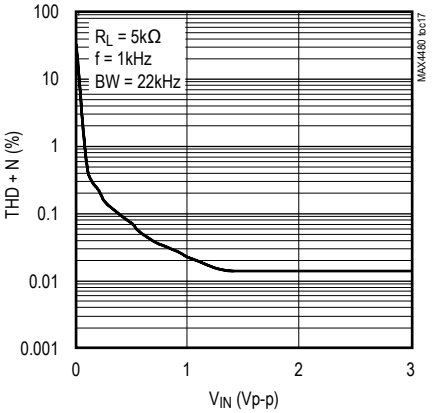
TOTAL HARMONIC DISTORTION PLUS NOISE vs. FREQUENCY



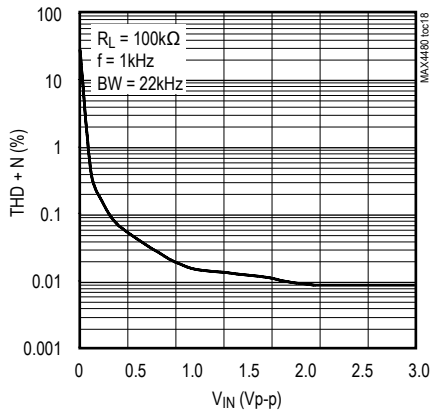
TOTAL HARMONIC DISTORTION PLUS NOISE vs. FREQUENCY



TOTAL HARMONIC DISTORTION PLUS NOISE vs. INPUT AMPLITUDE



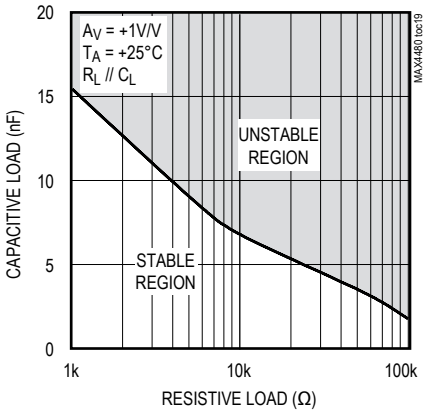
TOTAL HARMONIC DISTORTION PLUS NOISE vs. INPUT AMPLITUDE



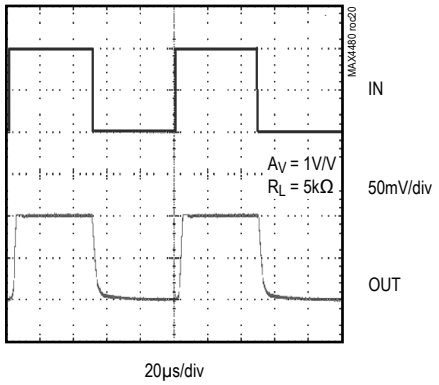
Typical Operating Characteristics (continued)

(V_{DD} = +5V, V_{SS} = 0V, V_{CM} = V_{DD}/2, $V_{\overline{SHDN}}$ = 5V, R_L ≥ 1MΩ connected to V_{DD}/2, T_A = +25°C, unless otherwise noted.)

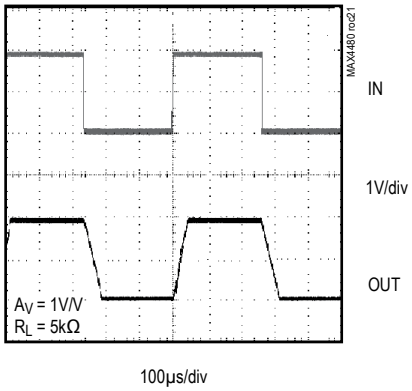
CAPACITIVE-LOAD STABILITY



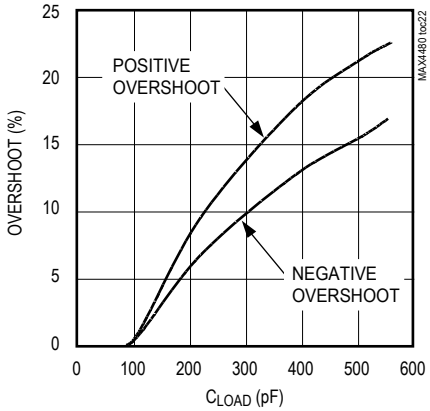
NONINVERTING SMALL-SIGNAL TRANSIENT RESPONSE



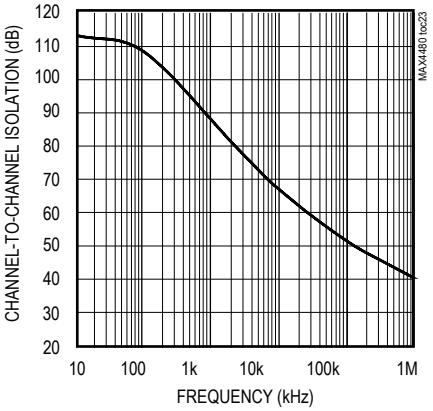
NONINVERTING LARGE-SIGNAL TRANSIENT RESPONSE



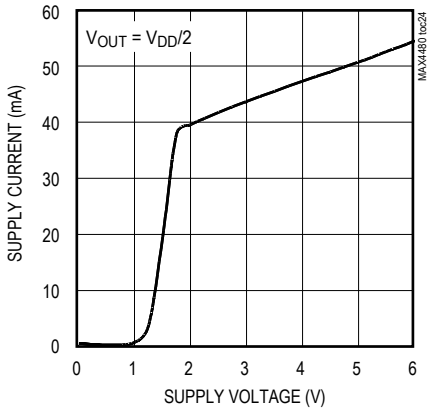
PERCENT OVERSHOOT vs. CAPACITIVE LOAD



CHANNEL-TO-CHANNEL ISOLATION vs. FREQUENCY



SUPPLY CURRENT vs. SUPPLY VOLTAGE



Pin Description

PIN				NAME	FUNCTION
MAX4480	MAX4481	MAX4482	MAX4483		
1	1	—	—	IN+	Noninverting Amplifier Input
—	—	3	3	INA+	Noninverting Amplifier Input A
—	—	5	5	INB+	Noninverting Amplifier Input B
—	—	—	10	INC+	Noninverting Amplifier Input C
—	—	—	12	IND+	Noninverting Amplifier Input D
2	2	4	11	V_{SS}	Negative Supply. Connect to ground for single-supply operation. Use a 0.01µF bypass capacitor to GND.
3	3	—	—	IN-	Inverting Amplifier Input
—	—	2	2	INA-	Inverting Amplifier Input A
—	—	6	6	INB-	Inverting Amplifier Input B
—	—	—	9	INC-	Inverting Amplifier Input C
—	—	—	13	IND-	Inverting Amplifier Input D
4	4	—	—	OUT	Amplifier Output
—	—	1	1	OUTA	Amplifier Output A
—	—	7	7	OUTB	Amplifier Output B
—	—	—	8	OUTC	Amplifier Output C
—	—	—	14	OUTD	Amplifier Output D
5	6	8	4	V_{DD}	Positive Supply. Use a 0.01µF bypass capacitor to GND.
—	5	—	—	$\overline{SHDN}$	Active-Low Shutdown Input. Connect to V_{DD} for normal operation. Do not leave floating.

Detailed Description

Rail-to-Rail Output Stage

The MAX4480–MAX4483 can drive a 5kΩ load and still typically swing within 80mV of the supply rails. Figure 1 shows the MAX4480 output voltage swing configured with A_V = +10V/V.

Driving Capacitive Loads

Driving a capacitive load can cause instability in many op amps, especially those with low quiescent current. The MAX4480–MAX4483 are unity-gain stable for a range of capacitive loads to above 400pF. Figure 2 shows the MAX4480 response with an excessive capacitive load. Adding a series resistor between the output and the load capacitor (Figure 3) improves the circuit's response by isolating the load capacitance from the op amp's output.

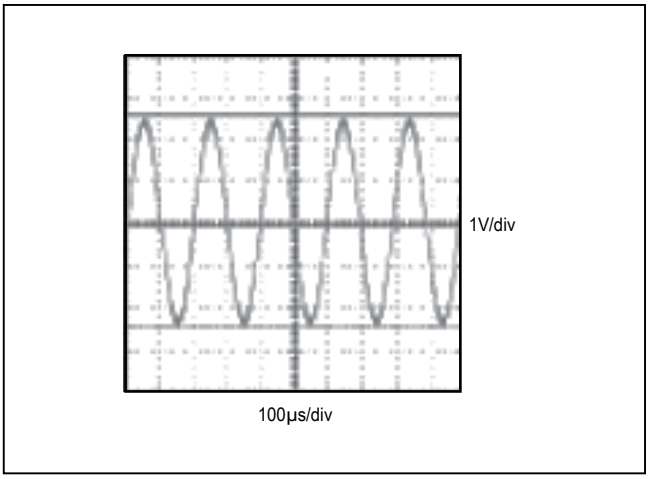


Figure 1. Rail-to-Rail Output Operation

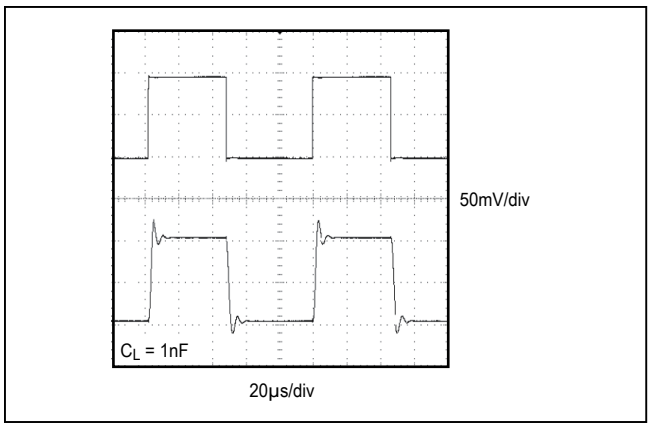


Figure 2. Small-Signal Transient Response with Excessive Capacitive Load

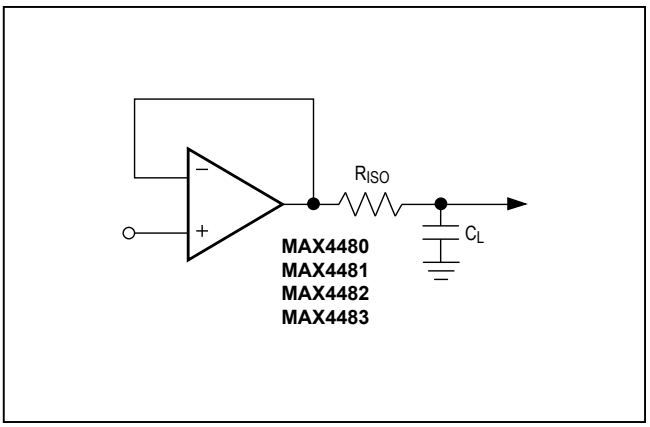


Figure 3. Capacitive-Load-Driving Circuit

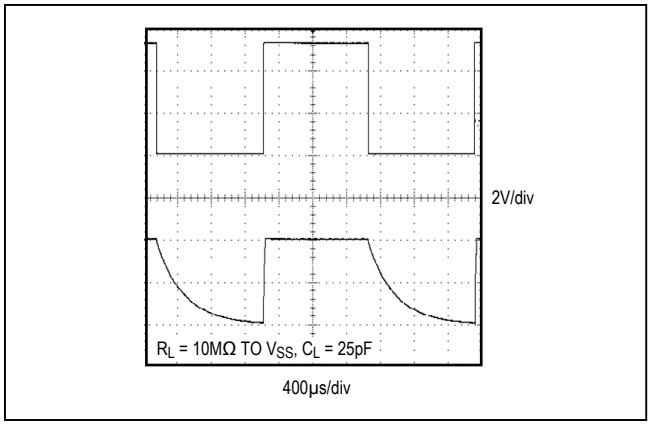


Figure 4. Shutdown Waveform

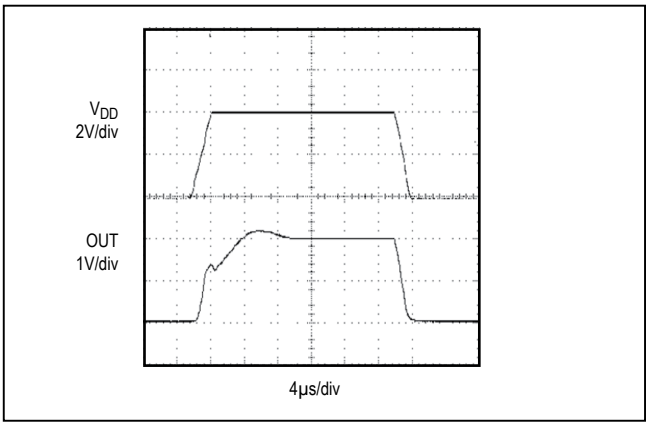


Figure 5. Power-Up/Down Waveform

Applications Information

Shutdown Mode

The MAX4481 features a low-power shutdown mode. When $\overline{SHDN}$ goes low, the supply current drops to 0.05µA (typ) and the output enters a high-impedance state. Pull $\overline{SHDN}$ high to enable the amplifier. Do not leave $\overline{SHDN}$ floating. Figure 4 shows the shutdown waveform.

Power-Up

The MAX4480–MAX4483 outputs typically settle within 50µs after power-up. Figure 5 shows the output voltage on power-up and power-down.

Power Supplies and Layout

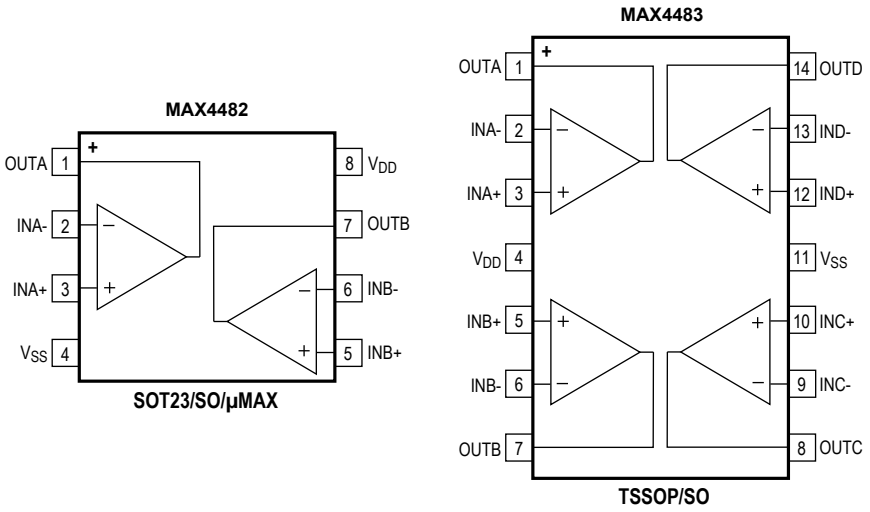
The MAX4480–MAX4483 operate from a single +2.5V to +5.5V power supply. Bypass the power supply with a 0.1µF capacitor to ground.

Good layout techniques optimize performance by decreasing the amount of stray capacitance at the op amp's inputs and outputs. To decrease stray capacitance, minimize trace lengths by placing external components close to the op amp's pins.

Chip Information

PROCESS: BiCMOS

Pin Configurations (continued)



Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
5 SC70	X5+1	21-0076	90-0188
5 SOT23	U5+1	21-0057	90-0174
6 SC70	X6SN+1	21-0077	90-0189
6 SOT23	U6SN+1	21-0058	90-0175
8 SOT23	K8+5	21-0078	90-0176
8 SO	S8+2	21-0041	90-0096
8 µMAX	U8+1	21-0036	90-0092
14 TSSOP	U14+1	21-0066	90-0113
14 SO	S14+1	21-0041	90-0112

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	10/00	Initial release	—
1	1/01	Released MAX4481, revised *Electrical Characteristics* and Figures 2 and 4.	1, 2, 3, 8
2	10/12	Added MAX4481MXT/PR2-W and lead-free notation to *Ordering Information*. Revised *Absolute Maximum Ratings* with military temp range.	1, 2
3	4/13	Removed –W from MAX4481MXT/PR2 in *Ordering Information* and revised *Absolute Maximum Ratings* and updated to MAX4481MXT/PR3+.	1, 2
4	5/14	Removed automotive reference from *General Description*	1

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