## GENERAL DESCRI PTI ON

The SP6685 is a current-regulated charge pump ideal for powering high brightness LEDs for camera flash applications.
The charge pump can be set to regulated two current levels for FLASH and TORCH modes. The SP6685 automatically switches modes between step-up and step-down ensuring that LED current does not depend on the forward voltage. A low current sense reference voltage ( 50 mV ) allows the use of small 0603 current sensing resistors.

The SP6685 is designed to operate from a single cell lithium-ion battery or fixed 3.3 V or 5.0 V power rails and is available in a RoHS compliant, "green"/halogen free space saving 10-pin 3mmx3mm DFN package.

## APPLI CATI ONS

- White LED Torch/ Flash for Cell Phone, DSCs and Camcorders
- White LED Backlighting
- Generic Lighting/ Flash Application
- General Purpose High Current Boost


## FEATURES

- Output Current up to 700 mA
- Up to 94\% Efficiency in Torch Mode
- Minimum External Components: No Inductor
- Adjustable FLASH Mode Current
- 1x and 2x Charge Pump Operation
- 2.4MHz High Frequency Operation
- $\mathrm{I}_{\mathrm{Q}}<1 \mu \mathrm{~A}$ in Shutdown
- Built-In Soft Start Limit Inrush Current
- Output Overvoltage Protection
- Over current/ Temperature Protection
- 10pin 3x3mm DFN Package


## TYPI CAL APPLI CATI ON DI AGRAM



Fig. 1: SP6685 Application Diagram

## ABSOLUTE MAXI MUM RATI NGS

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

Output Current Pulse (FLASH) .................................... 1A
Output Current Continuous (TORCH)......................... 0.4A
$\mathrm{V}_{\text {EN }}$.................................................................. 0 OV to 7 V
Storage Temperature............................. $-65^{\circ} \mathrm{C}$ to $150^{\circ} \mathrm{C}$
Lead Temperature (Soldering, 10 sec ) .................... $260^{\circ} \mathrm{C}$
ESD Rating EN pin (HBM - Human Body Model) .......... 1kV
ESD Rating All Other Pins (HBM) ............................... 2kV

## OPERATI NG RATI NGS

Input Voltage Range $\mathrm{V}_{\text {IN................................2.7V }}$ to 5.5 V
Operating Temperature Range................... $-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$
Thermal Resistance $\theta_{\mathrm{JA}} \ldots . . . . . . . . . . . . . . . . . . . . . . . . . . . . . .57 .1^{\circ} \mathrm{C} / \mathrm{W}$

## ELECTRI CAL SPECI FI CATI ONS

Specifications with standard type are for an Operating Junction Temperature of $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ only; limits applying over the full Operating Junction Temperature range are denoted by a " $\bullet$ ". Minimum and Maximum limits are guaranteed through test, design, or statistical correlation. Typical values represent the most likely parametric norm at $\mathrm{T}_{j}=25^{\circ} \mathrm{C}$, and are provided for reference purposes only. Unless otherwise indicated, $\mathrm{V}_{\mathrm{IN}}=3.6, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{FC}}=\mathrm{C}_{\mathrm{OUT}}=1 \mu \mathrm{~F} . \mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$.

| Parameter | Min. | Typ. | Max. | Units |  | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Input Voltage | 2.7 |  | 5.5 | V | - |  |
| Quiescent Current |  | 0.5 | 3 | mA | - | $\begin{aligned} & \mathrm{V}_{\text {IN }}=2.7-5.5 \mathrm{~V} \text { FLASH }=0 \mathrm{~V} \\ & \mathrm{I}_{\text {LOAD }}=100 \mu \mathrm{~A} \end{aligned}$ |
|  |  | 2 |  |  |  | FLASH $=\mathrm{V}_{\text {IN }}, 2 \times$ Mode |
| Shutdown Current |  |  | 1 | $\mu \mathrm{A}$ |  | $\mathrm{V}_{\mathrm{IN}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{EN}}=0 \mathrm{~V}$ |
| Oscillator Frequency |  | 2.4 |  | MHz |  |  |
| Charge Pump Equivalent Resistance (x2 Mode) |  | 5 |  | $\Omega$ |  | $\mathrm{V}_{\mathrm{FB}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{IN}}=3.6 \mathrm{~V}$ |
| Charge Pump Equivalent Resistance (x1 Mode) |  | 0.6 | 0.8 | $\Omega$ |  | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$ |
| FB Reference Voltage | 138 | 150 | 162 | mV | - | FLASH $=\mathrm{V}_{\text {IN, }}$, $\mathrm{R}_{\text {SET }}=88.7 \mathrm{~K}$ |
| FB Reference Voltage | 45 | 50 | 55 | mV | - | FLASH = GND |
| FB Pin Current |  |  | 0.5 | $\mu \mathrm{A}$ |  | $\mathrm{V}_{\mathrm{FB}}=0.3 \mathrm{~V}$ |
| EN, Flash Logic Low |  |  | 0.4 | V | - |  |
| EN, Flash Logic High | 1.3 |  |  | V | - |  |
| EN, Flash Pin Current |  |  | 0.5 | $\mu \mathrm{A}$ | - |  |
| $\mathrm{V}_{\text {out }}$ Turn-on Time |  | 250 | 500 | $\mu \mathrm{S}$ | - | $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}$, FB within $90 \%$ of regulation |
| Thermal Shutdown Temperature |  | 145 |  | ${ }^{\circ} \mathrm{C}$ |  |  |

## BLOCK DI AGRAM



Fig. 2: SP6685 Block Diagram

PI N ASSI GNEMENT
$\square$
Fig. 3: SP6685 Pin Assignment

## PI N DESCRIPTION

| Name | Pin Number | Description |
| :---: | :---: | :---: |
| VIN | 1 | Input voltage for the charge pump. Decouple with $4.7 \mu \mathrm{~F}$ ceramic capacitor close to the pins of the IC. |
| C1 | 2 | Positive input for the external fly capacitor. Connect a ceramic $1 \mu \mathrm{~F}$ capacitor close to the pins of the IC. |
| C2 | 3 | Negative input for the external fly capacitor. Connect a ceramic $1 \mu \mathrm{~F}$ capacitor close to the pins of the IC. |
| FLASH | 4 | Logic input to toggle between FLASH and TORCH mode. In TORCH Mode FB is regulated to the internal 50 mV reference. In FLASH Mode FB reference voltage can be adjusted by changing the resistor from $\mathrm{R}_{\text {SET }}$ pin to ground. Choose the external current sense Resistor ( $\mathrm{R}_{\text {SENSE }}$ ) based on desired current in TORCH Mode. |
| EN | 5 | Shutdown control input. Connect to $\mathrm{V}_{\text {IN }}$ for normal operation, connect to ground for shutdown. |
| $\mathrm{R}_{\text {SEt }}$ | 6 | Connect a resistor from this pin to ground. When in FLASH Mode (FLASH = High) this resistor sets the current regulation point according to the following: $\mathrm{V}_{\mathrm{FB}}=\left(1.26 \mathrm{~V} / \mathrm{R}_{\mathrm{SET}}\right) * 11.2 \mathrm{~K} \Omega$ |
| FB | 7 | Feedback input for the current control loop. Connect directly to the current sense resistor. |
| $\mathrm{S}_{\text {GND }}$ | 8 | Internal ground pin. Control circuitry returns current to this pin. |
| $\mathrm{P}_{\text {GND }}$ | 9 | Power ground pin. Fly capacitor current returns through this pin. |
| $V_{\text {OUt }}$ | 10 | Charge Pump Output Voltage. Decouple with an external capacitor. At least $1 \mu \mathrm{~F}$ is recommended. Higher capacitor values reduce output ripple. |

## ORDERI NG I NFORMATI ON

| Part Number | Temperature <br> Range | Marking | Package | Packing <br> Quantity | Note 1 | Note 2 |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| SP6685ER-L | $-40^{\circ} \mathrm{C} \leq T_{A} \leq+85^{\circ} \mathrm{C}$ | SP66 <br> 85 ER <br> WWX | DFN-10 | Bulk | RoHS Compliant <br> Halogen Free |  |
| SP6685ER-L/TR | $-40^{\circ} \mathrm{C} \leq T_{A} \leq+85^{\circ} \mathrm{C}$ | SP66 <br> 85 ER <br> WWX | DFN-10 | 3K/Tape \& Reel | RoHS Compliant <br> Halogen Free |  |
| SP6685EB | SP6685 Evaluation Board |  |  |  |  |  |

"WW" = Work Week - "X" = Lot Number

## TYPI CAL PERFORMANCE CHARACTERISTI CS

All data taken at $\mathrm{V}_{\mathrm{IN}}=3.6 \mathrm{~V}$, Typical Application Circuit, $\mathrm{D} 1=$ Luxeon LXCL-PWF1, $\mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$, unless otherwise specified.


Fig. 4: TORCH Mode Output Current


Fig. 6: TORCH Mode Output Efficiency


Fig. 8: Ripple 1 x FLASH Mode $700 \mathrm{~mA}, \mathrm{CH} 1=\mathrm{V}_{\mathrm{IN}}$ $\mathrm{CH} 2=\mathrm{V}_{\text {out }}, \mathrm{V}_{\text {IN }}=4.2 \mathrm{~V}, \mathrm{C}_{\text {IN }}=10 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {out }}=4.7 \mu \mathrm{~F}$


Fig. 5: FLASH Mode Output Current


Fig. 7: FLASH Mode Output Efficiency


Fig. 9: Ripple $2 x$ FLASH Mode 700 mA . $\mathrm{CH} 1=\mathrm{V}_{\mathrm{IN}}$ $\mathrm{CH} 2=\mathrm{V}_{\text {OUT }}, \mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{C}_{\text {IN }}=10 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=4.7 \mu \mathrm{~F}$


Fig. 10: Ripple 1x TORCH Mode 200mA. CH1 = $\mathrm{V}_{\text {IN }}$ $\mathrm{CH} 2=\mathrm{V}_{\text {OUT }}, \mathrm{V}_{\text {IN }}=4.2 \mathrm{~V}, \mathrm{C}_{\text {IN }}=10 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=4.7 \mu \mathrm{~F}$


Fig. 12: $\mathrm{C}_{\text {out }}=4.7 \mu \mathrm{~F}$


Fig. 14: $\mathrm{C}_{\mathrm{IN}}=10 \mu \mathrm{~F}$


Fig. 11: Ripple $2 \times$ TORCH Mode 200 mA . $\mathrm{CH} 1=\mathrm{V}_{\text {IN }}$ $\mathrm{CH} 2=\mathrm{V}_{\text {OUT }}, \mathrm{V}_{\text {IN }}=3.0 \mathrm{~V}, \mathrm{C}_{\text {IN }}=10 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUt }}=4.7 \mu \mathrm{~F}$


Fig. 13: $\mathrm{C}_{\text {out }}=4.7 \mu \mathrm{~F}$


Fig. 15: $\mathrm{C}_{\mathrm{IN}}=10 \mu \mathrm{~F}$


Fig. 16: Output Current vs Supply Voltage


Fig. 18: Battery Current vs Supply Voltage


Fig. 20: D1 = AOT 3228HPW0303B LED, $\mathrm{R}_{\text {SENSE }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=162 \mathrm{~K}, \mathrm{C}_{\text {IN }}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=0.47 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 17: Efficiency ve Supply Voltage


Fig. 18: $\mathrm{D} 1=\mathrm{AOT} 3228 \mathrm{HPW} 0303 \mathrm{~B}$ LED, $\mathrm{R}_{\text {SENSE }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=162 \mathrm{~K}, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=0.47 \mu \mathrm{~F}, \mathrm{C}_{\text {OUt }}=1 \mu \mathrm{~F}$


Fig. 21: D1 $=$ AOT 3228HPW0303B LED, $\mathrm{R}_{\text {Sense }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=162 \mathrm{~K}, \mathrm{C}_{\text {IN }}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=0.47 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 22: $\mathrm{D} 1=\mathrm{AOT} 6060 \mathrm{HPW0305B}$ LED, $\mathrm{R}_{\text {SENSE }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=75 \mathrm{~K}, \mathrm{C}_{\text {IN }}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 19: D1 = AOT 6060HPW0305B LED, $\mathrm{R}_{\text {SENSE }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=75 \mathrm{~K}, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 26: $\mathrm{D} 1=\mathrm{AOT}$ 2015HPW1915B LED, $\mathrm{R}_{\text {SENSE }}=0.22 \Omega$ $\mathrm{R}_{\text {SEt }}=80.6 \mathrm{~K}, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {out }}=1 \mu \mathrm{~F}$


Fig. 23: $\mathrm{D} 1=\mathrm{AOT} 6060 \mathrm{HPW0305B}$ LED, $\mathrm{R}_{\text {SENSE }}=0.33 \Omega$ $\mathrm{R}_{\text {SET }}=75 \mathrm{~K}, \mathrm{C}_{\text {IN }}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 20: D1 = AOT 2015HPW1915B LED, $\mathrm{R}_{\text {SENSE }}=0.22 \Omega$ $\mathrm{R}_{\text {SET }}=80.6 \mathrm{~K}, \mathrm{C}_{\mathrm{IN}}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 27: $\mathrm{D} 1=\mathrm{AOT}$ 2015HPW1915B LED, $\mathrm{R}_{\text {SENSE }}=0.22 \Omega$ $\mathrm{R}_{\text {SET }}=80.6 \mathrm{~K}, \mathrm{C}_{\text {IN }}=4.7 \mu \mathrm{~F}, \mathrm{C}_{\mathrm{F}}=1 \mu \mathrm{~F}, \mathrm{C}_{\text {OUT }}=1 \mu \mathrm{~F}$


Fig. 28: Startup 200mA Torch $\mathrm{V}_{\text {IN }}=3.6 \mathrm{~V}, \mathrm{~V}_{\text {OUT }}=3.2 \mathrm{~V}$


Fig. 30: Torch in 1X to Flash in 1 X Mode, $\mathrm{V}_{\mathrm{IN}}=4.2 \mathrm{~V}$ $\mathrm{CH} 1=\mathrm{FLASH}, \mathrm{CH} 2=\mathrm{V}_{\text {OUT }}, \mathrm{CH} 3=\mathrm{V}_{\mathrm{FB}}, \mathrm{CH} 4=\mathrm{I}_{\text {OUT }} 1 \mathrm{~A} / \mathrm{div}$


Fig. 32: Efficiency
See fig. 34 for Application Circuit


Fig. 29: Startup 700mA Flash

$$
V_{I N}=3.6 \mathrm{~V}, V_{\text {OUT }}=3.6 \mathrm{~V}
$$



Fig. 31: Torch in 1 X to Flash in 2 X Mode, $\mathrm{V}_{\mathrm{IN}}=3.6 \mathrm{~V}$ $\mathrm{CH} 1=\mathrm{FLASH}, \mathrm{CH} 2=\mathrm{V}_{\text {OUT }}, \mathrm{CH} 3=\mathrm{V}_{\mathrm{FB}}, \mathrm{CH} 4=\mathrm{I}_{\text {OUT }} 1 \mathrm{~A} / \mathrm{div}$


Fig. 33: Output Current See fig. 34 for Application Circuit

## APPLI CATI ON I NFORMATI ON

The SP6685 can be used with multiple LEDs in parallel as shown in figure 34. For best performance, the LEDs should be in a single package, preferably from a single die to have better matching for forward voltage Vf for a given forward current If. In practice, if the Vf of one LED is higher than the others, it will consume a larger If, which will raise its
temperature which will then cause its Vf to reduce, correcting the imbalance. The overall current will be the sum of the individual currents, for example Itotal $=4 *$ ILED.


Fig. 34: Multiple LED Flash Circuit

## THEORY OF OPERATION

The SP6685 is a charge pump regulator designed for converting a Li-Ion battery voltage of 2.7 V to 4.2 V to drive a white LED used in digital still camera Flash and Torch applications. The SP6685 has two modes of operation which are pin selectable for either Flash or Torch. Flash mode is usually used with a pulse of about 200 to 300 millisecond to generate a high intensity Flash. Torch can be used continuously at a lower output current than Flash and is often used for several seconds in a digital still camera "movie" mode.

The SP6685 also has two modes of operation to control the output current: the $1 X$ mode and 2 X mode. Operation begins after the enable pin EN receives a logic high, the bandgap reference wakes up after about $200 \mu \mathrm{sec}$, and then SP6685 goes through a soft-start mode designed to reduce inrush current. The SP6685 starts in the 1X mode,
which acts like a linear regulator to control the output current by continuously monitoring the feedback pin FB. In 1X mode, if the SP6685 auto detects a dropout condition, which is when the FB pin is below the regulation point for more than 32 cycles of the internal clock, the SP6685 automatically switches to the 2X mode. The SP6685 remains in the 2 X mode until one of four things happens: 1) the enable pin EN has been toggled, 2) the Flash pin has changed from High to Low, 3) $\mathrm{V}_{\mathrm{IN}}$ is cycled. 4) a thermal fault occurs.

The 2 X mode is the charge pump mode where the output can be pumped as high as two times the input voltage, provided the output does not exceed the maximum voltage for the SP6685, which is internally limited to about 5.5 V . In the 2 X mode, as in the 1 X mode, the output current is regulated by the voltage at the FB pin.

In the Torch mode, (Flash = GND) the Flash pin is set to logic low and the SP6685 IFB pin regulates to 50 mV output:
$V_{\mathrm{FB}}=50 \mathrm{mV}$
(Torch Mode)

When in Flash mode, (Flash $=\mathrm{V}_{\text {IN }}$ ), the FB regulation voltage is set by the resistor $\mathrm{R}_{\text {SET }}$ connected between the $\mathrm{R}_{\mathrm{SET}}$ pin and $\mathrm{S}_{\text {GND }}$ and the equation:
$\mathrm{V}_{\mathrm{FB}}=\left(1.26 \mathrm{~V} / \mathrm{R}_{\mathrm{SET}}\right) * 11.2 \mathrm{~K} \Omega \quad$ (Flash Mode)

Where the 1.26 V is the internal bandgap reference voltage and the $11.2 \mathrm{~K} \Omega$ is an internal resistance used to scale the $\mathrm{R}_{\text {SET }}$ current. Typical values of $R_{\text {SET }}$ are $40 \mathrm{~K} \Omega$ to $180 \mathrm{~K} \Omega$ for a range of $\mathrm{V}_{\mathrm{FB}}=300 \mathrm{mV}$ to 75 mV in Flash mode.

The output current is then set in either Flash or Torch mode by the equation:

$$
I_{\text {OUT }}=V_{\text {FB }} / R_{\text {sense }}
$$

## Overtemperature Protection

When the temperature of the SP6685 rises above $145^{\circ} \mathrm{C}$, the over temperature protection circuitry turns off the output switches to prevent damage to the device. If the temperature drops back down below $135^{\circ} \mathrm{C}$, the part automatically recovers and executes a soft start cycle.

## Overvoltage Protection

The SP6685 has over voltage protection. If the output voltage rises above the 5.5 V threshold, the over voltage protection shuts off all of the output switches to prevent the output voltage from rising further. When the output decreases below 5.5 V , the device resumes normal operation.

## Overcurrent Protection

The over current protection circuitry monitors the average current out of the $\mathrm{V}_{\text {out }}=50 \mathrm{mV}$ (Torch Mode) pin. If the average output
current exceeds approximately 1 Amp, then the over current protection circuitry shuts off the output switches to protect the chip.

## Component Selection

The SP6685 charge pump circuit requires 3 capacitors: $4.7 \mu \mathrm{~F}$ input, $1 \mu \mathrm{~F}$ output and $1 \mu \mathrm{~F}$ fly capacitor are typically recommended. For the input capacitor, a larger value of $10 \mu \mathrm{~F}$ will help reduce input voltage ripple for applications sensitive to ripple on the battery voltage. All the capacitors should be surface mount ceramic for low lead inductance necessary at the 2.4 MHz switching frequency of the SP6685 and to obtain low ESR, which improves bypassing on the input and output and improves output voltage drive by reducing output resistance. Ceramic capacitors with X5R or X7R temperature grade are recommended for most applications. A selection of recommended capacitors is included in Table 1 below.

| Manufacturer | Part Number | Value | Size/ Type |
| :---: | :---: | :---: | :---: |
| muRata | GRM155R60J 105K | $1 \mu \mathrm{~F} / 6.3 \mathrm{~V}$ | $0402 / \mathrm{X} 5 \mathrm{R}$ |
| muRata | GRM188R60J 475K | $4.7 \mu \mathrm{~F} / 6.3 \mathrm{~V}$ | $0603 / \mathrm{X} 5 \mathrm{R}$ |
| muRata | GRM21BR60J 106K | $10 \mu \mathrm{~F} / 6.3 \mathrm{~V}$ | $0805 / \mathrm{X} 5 \mathrm{R}$ |

Table 1: Recommended Capacitors

The input and output capacitors should be located as close to the $\mathrm{V}_{\text {IN }}$ and $\mathrm{V}_{\text {Out }}$ pins as possible to obtain best bypassing, and the returns should be connected directly to the $P_{\text {GND }}$ pin or to the thermal pad ground located under the SP6685. The fly capacitor should be located as close to the C1 and C2 pins as possible. See typical circuit layout (page 13) for details on the recommended layout.

To obtain lower output ripple, the Cout value can be increased from $1 \mu \mathrm{~F}$ to $2.2 \mu \mathrm{~F}$ or $4.7 \mu \mathrm{~F}$ with a corresponding decrease in output ripple as shown in the Typical Performance Characteristic curves. For output currents of 500 mA to 700 mA , the recommended $\mathrm{C}_{\mathrm{FC}}$ fly capacitor value of $1 \mu \mathrm{~F}$ should be used. Output currents in Flash of 100 mA to 400 mA can use a $0.47 \mu \mathrm{~F} \mathrm{C}_{\mathrm{Fc}}$ but a minimum $1 \mu \mathrm{~F} \mathrm{C}_{\text {out }}$ is still needed.

## Resistor Selection

The sense resistor $\mathrm{R}_{\text {sense }}$ is determined by the value needed in the Torch mode for the desired output current by the equation:

$$
R_{\text {SENSE }}=V_{\text {FB }} / I_{\text {out }}
$$

Where $\mathrm{V}_{\mathrm{FB}}=50 \mathrm{mV}$ (Torch Mode)
Once the $R_{\text {Sense }}$ resistor has been selected for Torch mode, the $\mathrm{V}_{\mathrm{FB}}$ voltage can be selected for Flash mode using the following equation:

$$
\mathrm{V}_{\mathrm{FB}}=\mathrm{I}_{\mathrm{OUT}} * \mathrm{R}_{\text {SENSE }} \text { (Flash Mode) }
$$

Where $\mathrm{I}_{\text {out }}$ is for Flash Mode.
Next, the $\mathrm{R}_{\text {SET }}$ resistor can be selected for Flash mode using the following equation:
$R_{\text {SET }}=\left(1.26 \mathrm{~V} / \mathrm{V}_{\text {FB }}\right) * 11.2 \mathrm{~K} \Omega$ (Flash Mode)
For an example of 200 mA Torch mode and 600 mA Flash mode, the values $\mathrm{R}_{\text {SENSE }}=$ $0.25 \Omega$, VFB $=150 \mathrm{mV}$ (Flash Mode), and $\mathrm{R}_{\text {SET }}$ $=94 \mathrm{~K} \Omega$ are calculated. The power obtained in the Flash mode would be:
$P_{\text {FLASH }}=V_{\text {FB }} * I_{\text {OUT }}=150 \mathrm{mV} * 600 \mathrm{~mA}=90 \mathrm{~mW}$.
The typical 0603 surface mount resistor is rated $1 / 10$ Watt continuous power and $1 / 5$ Watt pulsed power, more than enough for this application. For other applications, the $\mathrm{P}_{\text {FLASH }}$ power can be calculated and resistor size selected. The $R_{\text {SENSE }}$ resistor is recommended to be size 0603 for most applications. The
range of typical resistor values and sizes are shown in table 2.

| Part Ref. | Value | Tolerance | Size |
| :---: | :---: | :---: | :---: |
| $\mathrm{R}_{\text {SET }}$ | $68 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $75 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $82 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $91 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $100 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $110 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $120 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $130 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $140 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SET }}$ | $150 \mathrm{~K} \Omega$ | $5 \%$ | 0402 |
| $\mathrm{R}_{\text {SENSE }}$ | $0.22 \Omega$ | $5 \%$ | 0603 |
| $\mathrm{R}_{\text {SENSE }}$ | $0.27 \Omega$ | $5 \%$ | 0603 |
| $\mathrm{R}_{\text {SENSE }}$ | $0.33 \Omega$ | $5 \%$ | 0603 |
| $\mathrm{R}_{\text {SENSE }}$ | $0.39 \Omega$ | $5 \%$ | 0603 |
| $\mathrm{R}_{\text {SENSE }}$ | $0.47 \Omega$ | $5 \%$ | 0603 |

Table 2: Resistor Value and Sizes
Evaluation board circuit layout


## PACKAGE SPECI FICATI ON

## 10-PIN DFN



## REVISI ON HISTORY

| Revision | Date | Description |
| :---: | :---: | :--- |
| 2.0 .0 | $06 / 18 / 2009$ | Reformatted to corporate standard <br> Updated ESD level for EN pin. |
|  |  |  |
|  |  |  |

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