MXD8015HC

Low Noise Amplifier for LTE Mid-High Band

General Description

MXD8015HC high gain, low noise amplifier (LNA) is dedicated to LTE middle band and high band receive using advanced RFCMOS process. The high linearity performance and low noise figure makes the device an ideal choice for LTE receiving Applications.

MXD8015HC works under a 1.6V to 3.3V single power supply while consumes 6 mA current in low noise mode, in power down mode, the power consumption will be reduced to less than 1uA.

MXD8015HC uses a small 1.1mm × 0.7mm × 0.45mm DFN 6-pin package.

Applications

- LTE high-mid band receiving

Features

- Broadband frequency range: 1.8 to 2.7 GHz
- High Gain
  - 15.0dB gain at 2.8V 1.8GHz to 2.2GHz
  - 13.5dB gain at 2.8V 2.3GHz to 2.7GHz
  - 14.0dB gain at 1.8V 1.8GHz to 2.2GHz
  - 12.0dB gain at 1.8V 2.3GHz to 2.7GHz
- Low noise figure
  - 0.8dB noise figure at 2.8V 1.8GHz to 2.2GHz
  - 1.0dB noise figure at 2.8V 2.3GHz to 2.7GHz
  - 1.0dB noise figure at 1.8V 1.8GHz to 2.2GHz
  - 1.2dB noise figure at 1.8V 2.3GHz to 2.7GHz
- Operation current 6mA
- Small, DFN (6-pin, 1.1mm x 0.7mm x 0.45mm) package ，MSL1
- No DC blocking capacitors required.

Pin Configuration/Application Diagram (Top view)

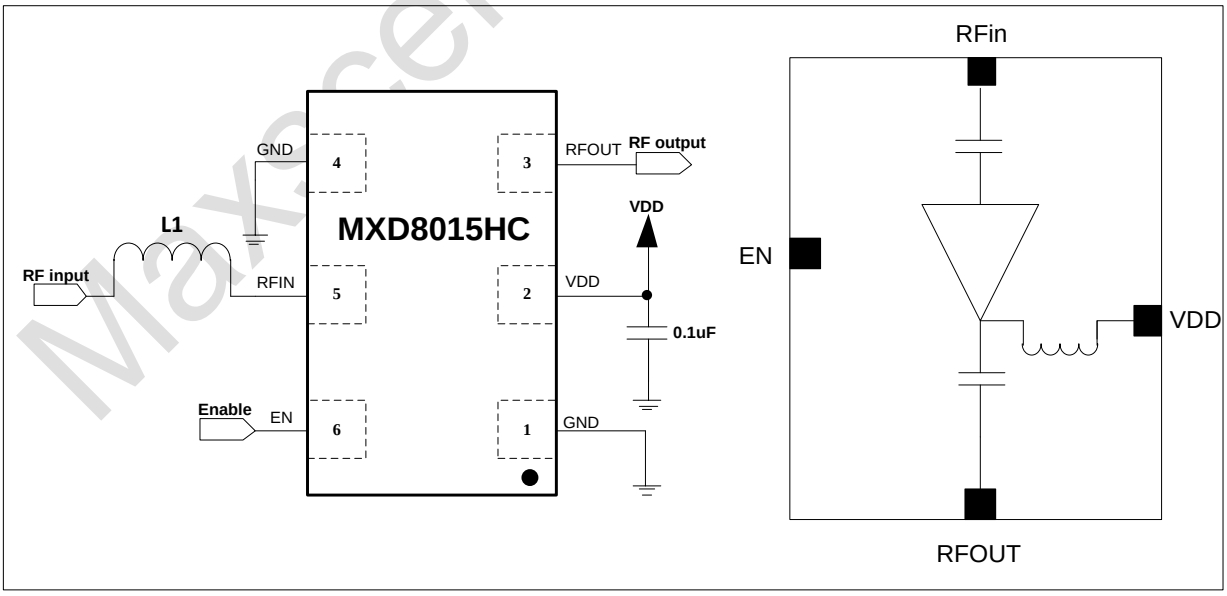


Figure 1 MXD8015HC application circuit

Pin Descriptions & Input matching inductance

Table 1

Pin	Pin Name	I/O	Pin Description
1	GND	AG	Analog VSS
2	VDD	AP	Power supply
3	RFOUT	AO	LNA output
4	GND	AG	Analog VSS
5	RFIN	AI	LNA input from antenna
6	EN	DI	Pull high into low noise mode, pull low into power down mode

Note: *DI* (digital input), *DO* (digital output), *DIO* (digital bidirectional), *AI* (analog input), *AO* (analog output), *AIO* (analog bidirectional), *AP* (analog power), *AG* (analog ground),

Table 2 Input matching inductance

Component	Matching Band	Vendor	Type	Part Number & value
L1	1800MHz – 2200MHz	Murata	Wired inductor, high Q	LQW15AN6N8, 6.8nH
		various	Ceramic inductor, low Q	6.2nH
	2300MHz – 2700MHz	Murata	Wired inductor, high Q	LQW15AN4N3, 4.3nH
		various	Ceramic inductor, low Q	3.9nH

Recommended Operation Range

Table 3

Parameters	Symbol	Min	Typ	Max	Units
Operation Frequency	f1	1800	-	2700	MHz
Power supply	V_{DD}	1.6	2.8	3.3	V
Control Voltage High	V_{CTL_H}	1.0	1.8	VDD	V
Control Voltage Low	V_{CTL_L}	0	0	0.3	V

Absolute Maximum Ratings

Table 4 Maximum ratings

Parameters	Symbol	Minimum	Maximum	Units
Supply voltage	V_{DD}	-0.3	+3.6	V
Digital control voltage	V_{CTL}	-0.3	VDD+0.3, Max: 3.6	V
RF input power	P_{IN}	-	+20	dBm
Operating temperature	T_{OP}	-35	+90	℃
Storage temperature	T_{STG}	-55	+150	℃
Electrostatic Discharge Human body model (HBM), Class 1C[Note1]	ESD_HBM	-	1500	V
Machine Model (MM), Class A[Note2]	ESD_MM		150	
Charged device model (CDM), Class III[Note3]	ESD_CDM		500	

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.
Note1: According to ESDA/JEDECJS-001-2014
Note2: According to JESD22-A115C
Note3: According to ESDA/JEDECJS-002-2014

Specifications

Typically T_A=25℃ VDD=2.8V, All data measured on Maxscend's EVB, unless otherwise noted

Table 5 High Gain mode Electrical Specifications

Parameter	Symbol	Specification			Units	Test Condition
		Min.	Typical	Max.		
DC Specifications						
Supply voltage	V_{DD}	1.6	2.8	3.3	V	
Supply current	I_{DD}	4.5 0	6 0.05	9 1	mA μA	VDD = 2.8V, VEN=high VDD = 2.8V, VEN=low
RF Specifications						
Power gain	G	13.0 11.5	15.0 13.5	17.0 15.5	dB dB	1800-2200MHz 2300-2700MHz
Noise figure	NF	-	0.8 1.0	1.3 1.5	dB dB	1800-2200MHz 2300-2700MHz
Input Return loss	|S11|	-	-10	-5	dB	1800 to 2700MHz
Output Return loss	|S22|	-	-10	-6	dB	1800 to 2700MHz
Stability factor	Kf	1.2	-	-	-	
Input 1 dB compression point	P1dB	-10 -8	-6 -4	-	dBm dBm	1800 to 2200MHz 2300 to 2700MHz
Input IP3	IIP3	-3 -2	1 2	-	dBm dBm	Note1 Note2
Startup time		-	-	1	μs	Shutdown state to power on state

Note1: Pin=Pin2=-25dBm, F1=2100MHz, F2=2101MHz

Note2: Pin=Pin2=-25dBm, F1=2600MHz, F2=2601MHz

Specifications

Typically T_A=25℃ VDD=1.8V, All data measured on Maxscend's EVB, unless otherwise noted

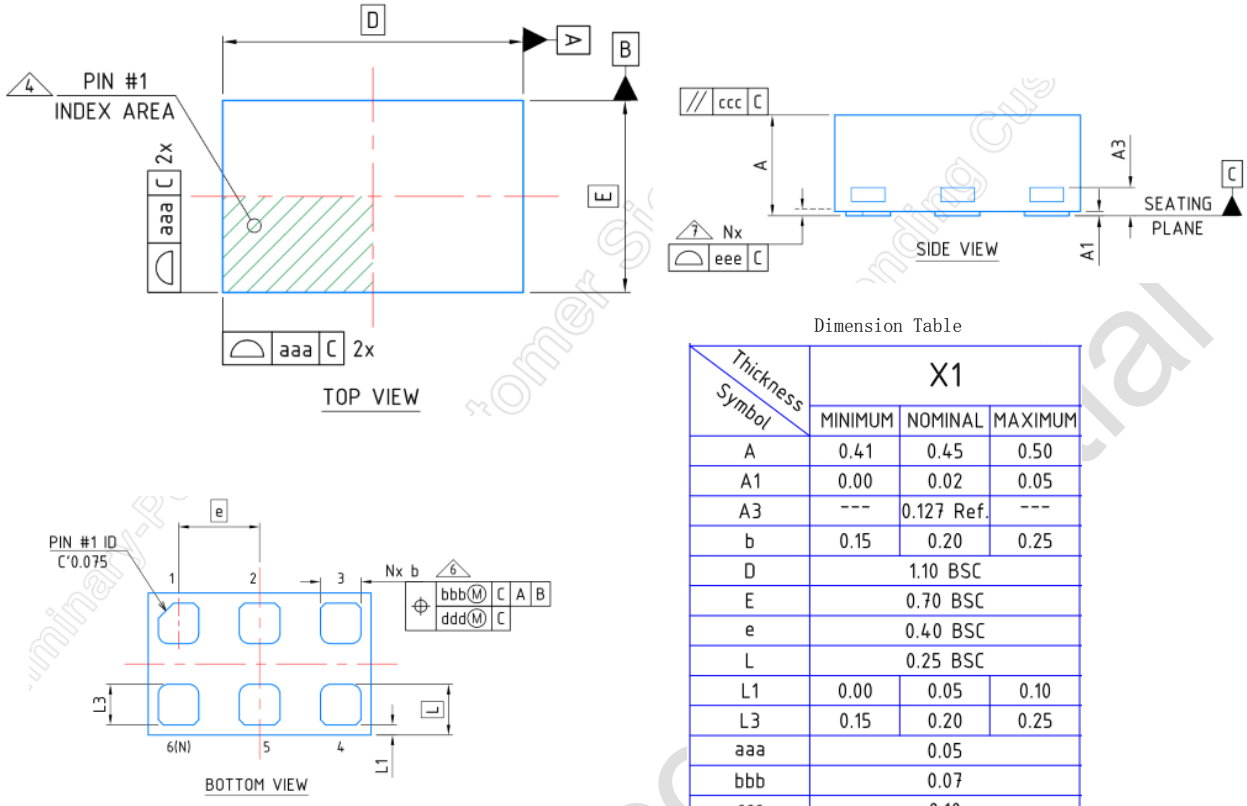
Table 6 High Gain mode Electrical Specifications

Parameter	Symbol	Specification			Units	Test Condition
		Min.	Typical	Max.		
DC Specifications						
Supply voltage	V_{DD}	1.6	1.8	3.3	V	
Supply current	I_{DD}	3 0	4 0.05	6 1	mA μA	VDD = 1.8V, VEN=high VDD = 1.8V, VEN=low
RF Specifications						
Power gain	G	12.0 10.0	14.0 12.0	15.5 13.5	dB dB	1800-2200MHz 2300-2700MHz
Noise figure	NF	-	1.0 1.2	1.5 1.7	dB dB	1800-2200MHz 2300-2700MHz
Input Return loss	|S11|	-	-10	-5	dB	1800 to 2700MHz
Output Return loss	|S22|	-	-10	-6	dB	1800 to 2700MHz
Stability factor	Kf	1.2	-	-	-	
Input 1 dB compression point	P1dB	-12 -9	-8 -5	-	dBm dBm	1800 to 2200MHz 2300 to 2700MHz
Input IP3	IIP3	-3 -3	1 1	-	dBm dBm	Note1 Note2
Startup time		-	-	1	μs	Shutdown state to power on state

Note1: Pin=Pin2=-25dBm, F1=2100MHz, F2=2101MHz

Note2: Pin=Pin2=-25dBm, F1=2600MHz, F2=2601MHz

Package Outline Dimensions



Dimension Table

Thickness / Symbol	X1		
	MINIMUM	NOMINAL	MAXIMUM
A	0.41	0.45	0.50
A1	0.00	0.02	0.05
A3	---	0.127 Ref.	---
b	0.15	0.20	0.25
D		1.10 BSC	
E		0.70 BSC	
e		0.40 BSC	
L		0.25 BSC	
L1	0.00	0.05	0.10
L3	0.15	0.20	0.25
aaa		0.05	
bbb		0.07	
ccc		0.10	
ddd		0.05	
eee		0.08	
N		6	
ND		3	

Figure 2 MXD8015HC outline dimension

Marking Specification

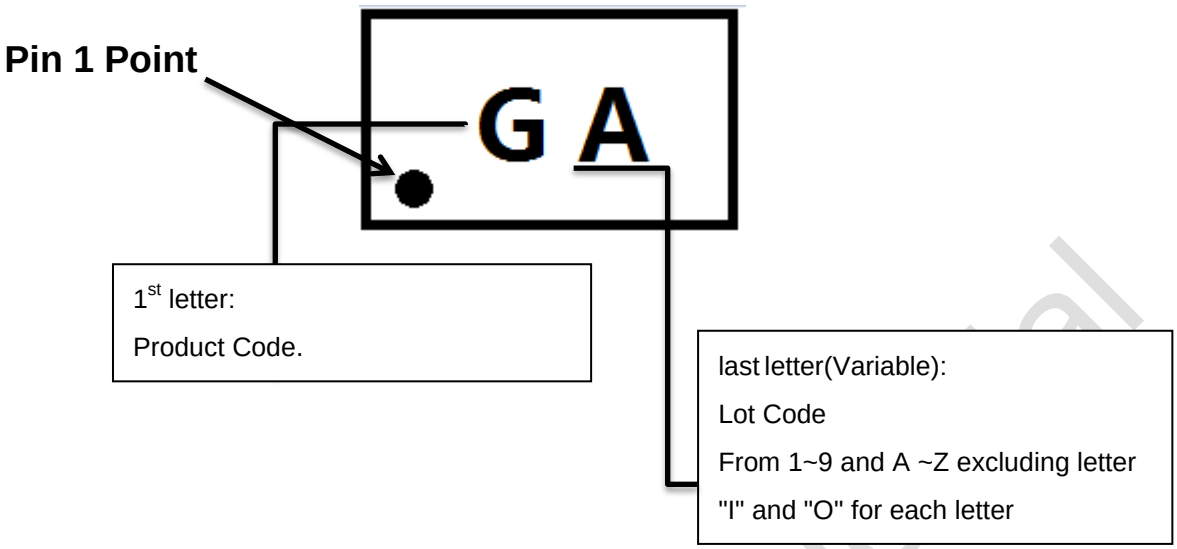
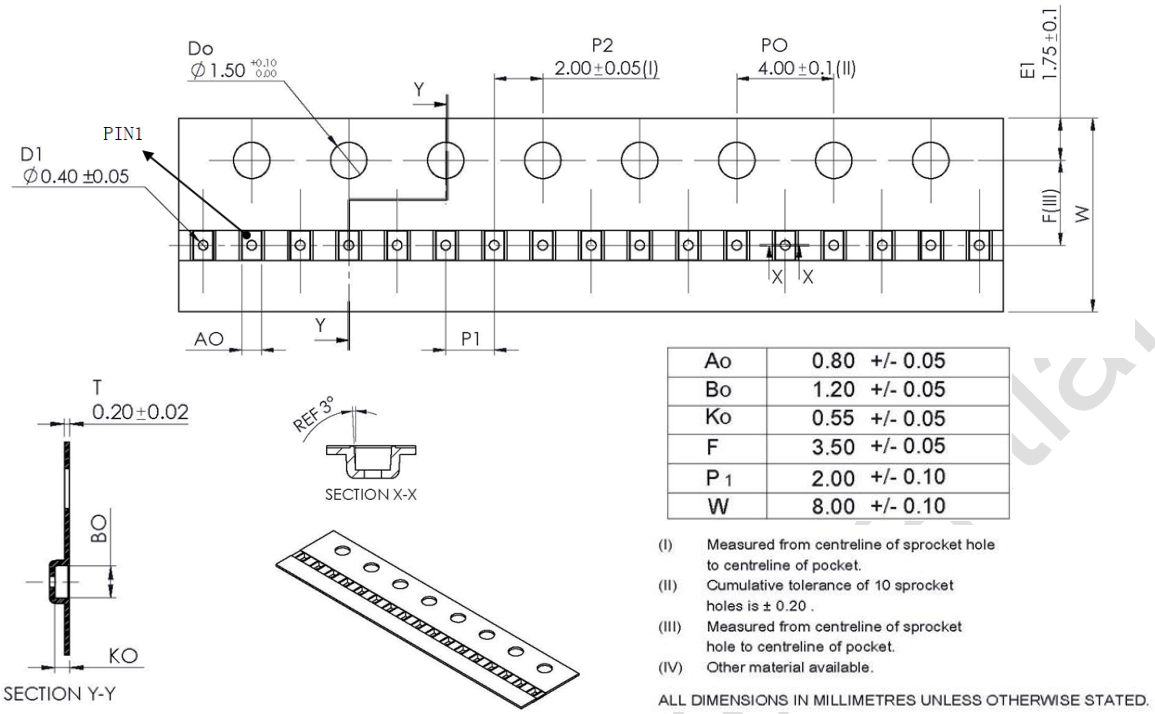


Figure 3 Marking specification (Top View)

Tape and Reel Dimensions



Ao	0.80 +/- 0.05
Bo	1.20 +/- 0.05
Ko	0.55 +/- 0.05
F	3.50 +/- 0.05
P_1	2.00 +/- 0.10
W	8.00 +/- 0.10

(I) Measured from centreline of sprocket hole to centreline of pocket.
(II) Cumulative tolerance of 10 sprocket holes is ± 0.20 .
(III) Measured from centreline of sprocket hole to centreline of pocket.
(IV) Other material available.

ALL DIMENSIONS IN MILLIMETRES UNLESS OTHERWISE STATED.

Figure 4 Tape and reel dimensions

Reflow Chart

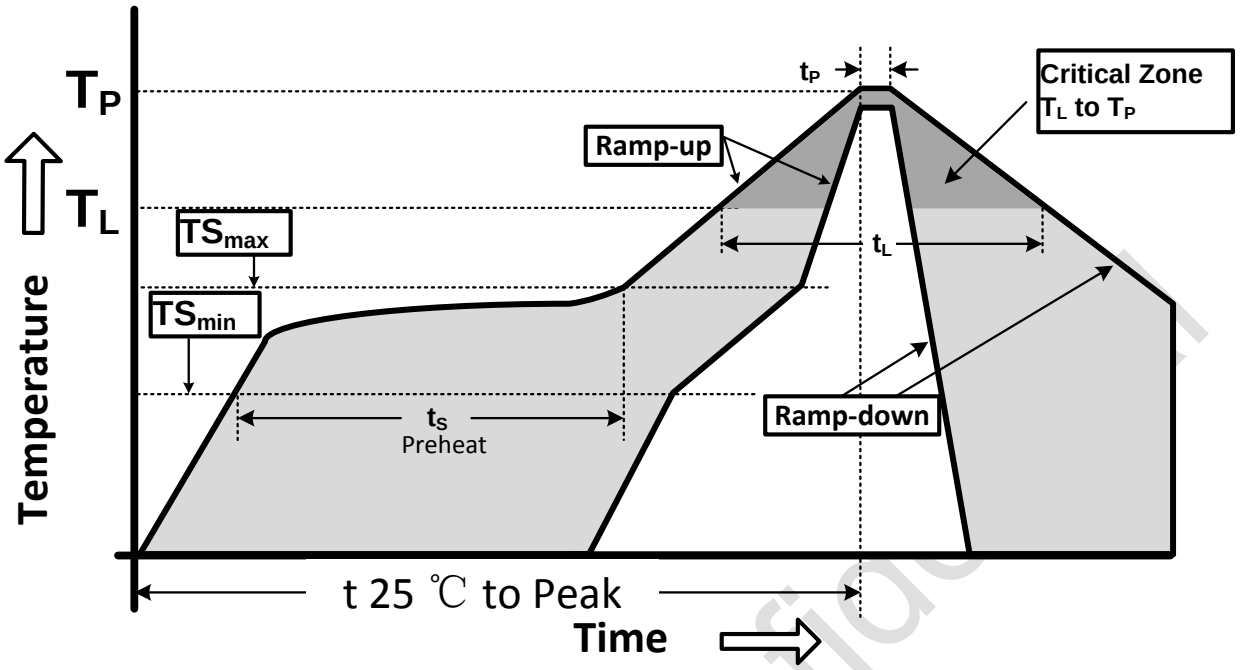


Figure 5 Recommended Lead-Free Reflow Profile

Table 7 Reflow condition

Profile Parameter	Lead-Free Assembly, Convection, IR/Convection
Ramp-up rate（TS_{max} to T_p）	3℃/second max.
Preheat temperature（TS_{min} to TS_{max}）	150℃ to 200℃
Preheat time（t_s）	60 - 180 seconds
Time above TL , 217℃（t_L）	60 - 150 seconds
Peak temperature（T_p）	260℃
Time within 5℃ of peak temperature(t_p)	20 - 40 seconds
Ramp-down rate	6℃/second max.
Time 25℃ to peak temperature	8 minutes max.

ESD Sensitivity

Integrated circuits are ESD sensitive and can be damaged by static electric charge. Proper ESD protection techniques should be used when handling these devices.

RoHS Compliant

This product does not contain lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB) and polybrominated diphenyl ethers (PBDE), and are considered RoHS compliant.

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