

# **MXD8638C**

0.1-3.0GHz SP3T Switch

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#### **General Description**

The MXD8638C is a CMOS silicon-on-insulator (SOI), single-pole, triple-throw (SP3T) transmitting and receiving switch. The high linearity performance and low insertion loss makes the device an ideal choice for GSM/WCDMA/LTE handset and data card applications.

The MXD8638C SP3T switch is provided in a compact QFN 9-pin 1.15mm x 1.15mm x 0.45mm package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

#### **Applications**

- GSM/WCDMA/LTE receive and transmit
- 802.11b/g/n WLANs

#### Features

- Broadband frequency range: 0.1 to 3.0 GHz
- Low insertion: 0.45 dB @ 2.7 GHz •
- High isolation: 25 dB up to 2.7 GHz
- P0.1dB of 36dBm
- No external DC blocking capacitors required
- Positive low voltage control: VC = 1.6 to 3.00
  V, VDD = 2.5 to 3.0 V
- Small, QFN (9-pin, 1.15mm x 1.15mm x 0.45mm) package

#### **Functional Block Diagram and Pin Function**

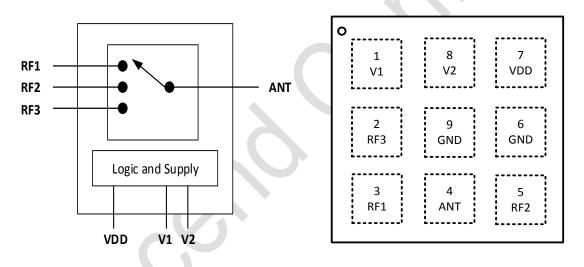
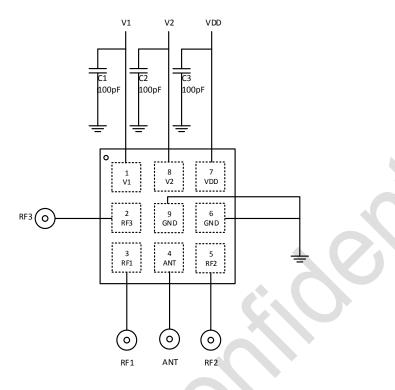


Figure 1 Functional Block Diagram

Figure 2 Pin-out (Top View)



## **Application Circuit**



#### Figure 3 MXD8638C Application Circuit

#### **Table 1. Pin Description**

Pin No.	Name	Description	Pin No.	Name	Description
1	V1	Control Pin 1	6	GND	Ground
2	RF3	RF-Port 3	7	VDD	Power Supply
3	RF1	RF-Port 1	8	V2	Control Pin 2
4	ANT	Antenna	9	GND	Ground
5	RF2	RF-Port 2			

#### Truth Table

Table 2.

V1	V2	Active Path
0	0	OFF
1	0	ANT to RF1
0	1	ANT to RF2
1	1	ANT to RF3

**Note:** "1" = 1.0 V to 3.00 V. "0" = 0 V to +0.3 V.

## **Recommended Operation Range**

#### Table 3.

Parameters	Symbol	Min	Тур	Max	Units
Operation Frequency	f1	0.1	-	3.0	GHz
Power supply	Vdd	2.5	2.8	3.0	V
Switch Control Voltage High	Vctl_h	1.6	1.8	3.0	V
Switch Control Voltage Low	Vctl_l	0	0	0.3	V



#### **Specifications**

#### **Table 4.Electrical Specifications**

Devementer	Symbol	Specification		Unite	Test Condition	
Parameter		Min.	Typical	Max.	Units	Test Condition
DC Specification	IS					
Control voltage:						
Low	Vctl_l	0	0	0.3	V	
High	V <sub>CTL_H</sub>	1.6	+1.8	3.0	V	
Supply voltage	V <sub>DD</sub>	2.5	2.8	3.0	V	
Supply current	IDD		35	60	μA	VDD = 2.8 V
Control current	Ість		1		μA	VC= 1.8 V
<b>RF Specification</b>	S					
			0.30	0.35	dB	0.1 to 1.0 GHz
Insertion loss	IL		0.37	0.40	dB	1.0 to 2.2 GHz
			0.45	0.50	dB	2.2 to 3.0 GHz
		30	35		dB	0.1 to 1.0 GHz
Isolation	ISO	25	30		dB	1.0 to 2.2 GHz
		20	25		dB	2.2 to 3.0 GHz
Return loss	S11		15		dB	0.1 to 3.0 GHz
Ord Order Inset						0.1 to 3.0 GHz,
3rd Order Input	IIP3	+60	+70		dBm	$\Delta F = 1 MHz,$
Intercept Point						PIN = +20 dBm/tone
Input 0.1 dB						
compression	P0.1dB		+36		dBm	0.1 to 3.0 GHz,
point						ANT to RF1,RF2 and RF3
2nd harmonic	2fo		+90		dBc	0.1~3GHz,
	210		100		uD0	PIN = +26 dBm
3rd harmonic	3fo		+90		dBc	0.1~3GHz,
	010		100		480	PIN = +26 dBm
Switching on			1		μs	50% VCTL to 10/90% RF
time					~~	
Switching off					μs	50% VCTL to 90/10% RF
time					<u>мо</u>	
Startup time			5		μs	Shutdown state to any RF
					μο	switch state

## **Absolute Maximum Ratings**

#### Table 5. Maximum ratings

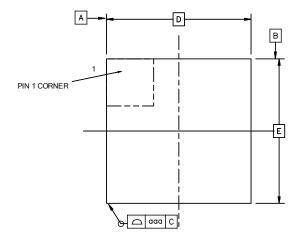
Parameters	Symbol	Minimum	Maximum	Units
Supply voltage	V <sub>DD</sub>	+2.5	+3.3	V
Digital control voltage	VCTL	0	+3.3	V
RF input power	P <sub>IN</sub>		+36.5	dBm
Operating temperature	Тор	-30	+85	°C
Storage temperature	T <sub>STG</sub>	-55	+150	°C
Electrostatic discharge: Human Body Model (HBM), Class 1C Machine Model (MM), Class A	ESD		1000 100	V V

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

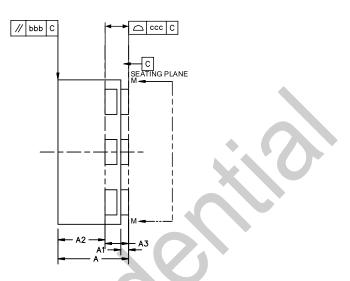


#### MXD8638C – 0.1-3.0GHz SP3T Switch

## Package Outline Dimension



TOP VIEW



	SYMBOL	MILLIMETER			
DESCRIPTION		MIN	NOM	MAX	
TOTAL THICKNESS	A	0.40	0.45	0.50	
STAND OFF	A1	0.00		0.05	
MOLD THICKNESS		A2	0.25	0.30	0.35
L/F THICKNESS	A3	0.150 REF			
LEAD WIDTH	b	0.10	0.20	0.30	
BODY SIZE	Х	D	1.10	1.15	1.20
BODY SIZE	Y	E	1.10	1.15	1.20
LEAD PITCH	е	0.40 BSC			
LEAD LENGTH	L	0.10	0.20	0.30	
PACKAGE EDGE TOLER/	aaa		0.1		
MOLD FLATNESS	bbb	0.1			
COPLANARITY	CCC	0.08			
LEAD OFFSET	ddd	0.1			
EXPOSED PAD OFFSET	eee	0.1			

Figure 4. Package outline dimension



**Reflow Chart** 

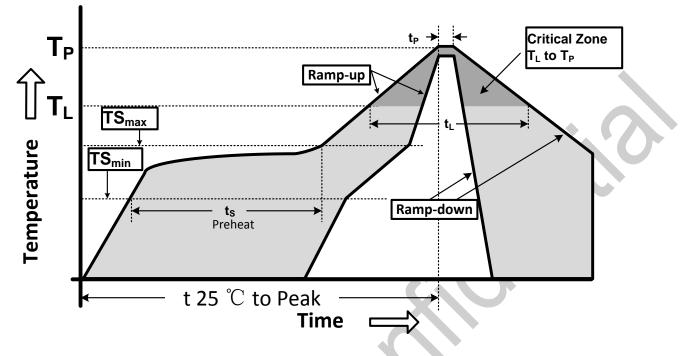


Figure 5. Recommended Lead-Free Reflow Profile

#### **Table 6 Reflow condition**

Profile Parameter	Lead-Free Assembly, Convection, IR/Convection			
Ramp-up rate (TS <sub>max</sub> to T <sub>p</sub> )	3℃/second max.			
Preheat temperature (TS <sub>min</sub> to TS <sub>max</sub> )	150°C to 200°C			
Preheat time (t <sub>s</sub> )	60 - 180 seconds			
Time above TL , 217 $^{\circ}$ C $(t_L)$	60 - 150 seconds			
Peak temperature (T <sub>p</sub> )	<b>260</b> ℃			
Time within 5 $^\circ \! \mathbb{C}$ of peak temperature(t <sub>P</sub> )	20 - 40 seconds			
Ramp-down rate	6℃/second max.			
Time 25 $^{\circ}$ C to peak temperature	8 minutes max.			

## **ESD Sensitivity**

Integrated circuits are ESD sensitive and can be damaged by static electric charge. Proper ESD protection techniques should be used when handling these devices.

## **RoHS Compliant**

This product does not contain lead, mercury, cadmium, hexavalent chromium, polybrominated biphenyls (PBB) and polybrominated diphenyl ethers (PBDE), and are considered RoHS compliant.

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