## FEATURES

- 850ps Data Input to Output

■ Extended 100E Vee range of -4.2 V to -5.5 V

- Differential output

■ Fully compatible with industry standard 10 KH , 100K ECL levels
■ Internal $75 \mathrm{~K} \Omega$ input pull-down resistors
■ Fully compatible with Motorola MC10E/100E164

- Available in 28-pin PLCC package


## BLOCK DIAGRAM



## DESCRIPTION

The SY10/100E164 are 16:1 multiplexers with a differential output. The select inputs (SEL0,1,2,3) control which one of the sixteen data inputs ( $\mathrm{A} 0-\mathrm{A}_{15}$ ) is propagated to the output.

Special attention to the design layout results in a typical skew between the 16 inputs of only 50 ps.

## PIN NAMES

| Pin | Function |
| :--- | :--- |
| $\mathrm{A}_{0}-\mathrm{A} 15$ | Data Inputs |
| SEL[0:3] | Select Inputs |
| $\overline{\mathrm{Q}, \mathrm{Q}}$ | Outputs |
| Vcco | Vcc to Output |

## PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information ${ }^{(1)}$

| Part Number | Package <br> Type | Operating <br> Range | Package <br> Marking | Lead <br> Finish |
| :--- | :---: | :---: | :---: | :---: |
| SY10E164JC | J28-1 | Commercial | SY10E164JC | Sn-Pb |
| SY10E164JCTR $^{(2)}$ | J28-1 | Commercial | SY10E164JC | Sn-Pb |
| SY100E164JC | J28-1 | Commercial | SY100E164JC | Sn-Pb |
| SY100E164JCTR(2) | J28-1 | Commercial | SY100E164JC | Sn-Pb |
| SY10E164JZ ${ }^{(3)}$ | J28-1 | Commercial | SY10E164JZ with <br> Pb-Free bar-line indicator | Matte-Sn |
| SY10E164JZTR ${ }^{(2,3)}$ | J28-1 | Commercial | SY10E164JZ with <br> Pb-Free bar-line indicator | Matte-Sn |
| SY100E164JZ ${ }^{(3)}$ | J28-1 | Commercial | SY100E164JZ with <br> Pb-Free bar-line indicator | Matte-Sn |
| SY100E164JZTR ${ }^{(2,3)}$ | J28-1 | Commercial | SY100E164JZ with <br> Pb-Free bar-line indicator | Matte-Sn |

Notes:

1. Contact factory for die availability. Dice are guaranteed at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$, DC Electricals only.
2. Tape and Reel.
3. Pb -Free package is recommended for new designs.

## TRUTH TABLE

| SEL3 | SEL2 | SEL1 | SEL0 | Data |
| :---: | :---: | :---: | :---: | :---: |
| L | L | L | L | A 0 |
| L | L | L | H | A 1 |
| L | L | H | L | A 2 |
| L | L | H | H | A 3 |
| L | H | L | L | A 4 |
| L | H | L | H | A 5 |
| L | H | H | L | A 6 |
| L | H | H | H | A 7 |


| SEL3 | SEL2 | SEL1 | SELo | Data |
| :---: | :---: | :---: | :---: | :---: |
| H | L | L | L | A8 |
| H | L | L | H | A9 |
| H | L | H | L | A10 |
| H | L | H | H | A11 |
| H | H | L | L | A12 |
| H | H | L | H | A13 |
| H | H | H | L | A14 |
| H | H | H | H | A15 |

## DC ELECTRICAL CHARACTERISTICS

$\mathrm{VEE}=\mathrm{VEe}$ (Min.) to Vee (Max.); Vcc = Vcco = GND

| Symbol | Parameter | $\mathrm{TA}=0^{\circ} \mathrm{C}$ |  |  | $\mathrm{TA}=+25^{\circ} \mathrm{C}$ |  |  | $\mathrm{TA}=+85^{\circ} \mathrm{C}$ |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. | Min. | Typ. | Max. | Min. | Typ. | Max. |  |  |
| IIH | Input HIGH Current | - | - | 150 | - | - | 150 | - | - | 150 | $\mu \mathrm{A}$ | - |
| IEE | Power Supply Current $\begin{array}{r} 10 \mathrm{E} \\ 100 \mathrm{E} \end{array}$ | - | $\begin{aligned} & 59 \\ & 59 \end{aligned}$ | $\begin{aligned} & 71 \\ & 71 \end{aligned}$ | - | $\begin{aligned} & 59 \\ & 59 \end{aligned}$ | $\begin{aligned} & 71 \\ & 71 \end{aligned}$ | - | $\begin{aligned} & 59 \\ & 68 \end{aligned}$ | $\begin{aligned} & 71 \\ & 81 \end{aligned}$ | mA | - |

## AC ELECTRICAL CHARACTERISTICS

$\mathrm{VEE}=\mathrm{VEE}$ (Min.) to Vee (Max.); Vcc = Vcco = GND

| Symbol | Parameter | $\mathrm{TA}=0^{\circ} \mathrm{C}$ |  |  | $\mathrm{TA}^{\prime}=+25^{\circ} \mathrm{C}$ |  |  | $\mathrm{TA}=+85^{\circ} \mathrm{C}$ |  |  | Unit | Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. | Min. | Typ. | Max. | Min. | Typ. | Max. |  |  |
| tPD | Propagation Delay to Output |  |  |  |  |  |  |  |  |  | ps | - |
|  | A Input | 350 | 600 | 850 | 350 | 600 | 850 | 350 | 600 | 850 |  |  |
|  | SELo | 500 | 700 | 900 | 500 | 700 | 900 | 500 | 700 | 900 |  |  |
|  | SEL1 | 400 | 675 | 900 | 400 | 675 | 900 | 400 | 675 | 900 |  |  |
|  | SEL2 | 400 | 675 | 900 | 400 | 675 | 900 | 400 | 675 | 900 |  |  |
|  | SEL3 | 400 | 550 | 700 | 400 | 550 | 700 | 400 | 550 | 700 |  |  |
| tskew | Within-Device Skew | - | 50 | - | - | 50 | - | - | 50 | - | ps | 1 |
| tr tf | Rise/Fall Times 20-80\% | 275 | 400 | 550 | 275 | 400 | 550 | 275 | 400 | 550 | ps | - |

Note:

1. Within-device skew is defined as the difference in the $A$ to $Q$ delay between the 16 different $A$ inputs.

## 28-PIN PLCC (J28-1)



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