

6-BIT D REGISTER

SY10E151 SY100E151

## **FEATURES**

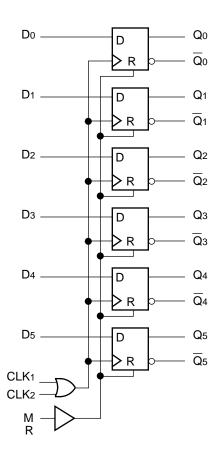
- 1100MHz toggle frequency
- Extended 100E VEE range of -4.2V to -5.46V
- **■** Differential outputs
- Asynchronous Master Reset
- Dual clocks
- Fully compatible with industry standard 10KH, 100K ECL levels
- Internal 75K $\Omega$  input pulldown resistors
- Fully compatible with Motorola MC10E/100E151
- Available in 28-pin PLCC package

## **DESCRIPTION**

The SY10/100E151 offer 6 edge-triggered, high-speed, master-slave D-type flip-flops with differential outputs, designed for use in new, high-performance ECL systems. The two external clock signals (CLK1, CLK2) are gated through a logical OR operation before use as clocking control for the flip-flops. Data is clocked into the flip-flops on the rising edge of either CLK1 or CLK2 (or both). When both CLK1 and CLK2 are at a logic LOW, data enters the master and is transferred to the slave when either CLK1 or CLK2 (or both) go HIGH.

The MR (Master Reset) signal operates asynchronously to make all Q outputs go to a logic LOW.

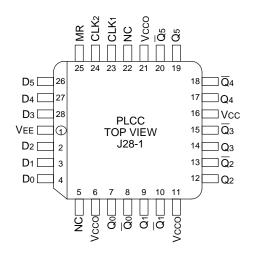
## **BLOCK DIAGRAM**



## **PIN NAMES**

Pin	Function
D0-D5	Data Inputs
CLK1, CLK2	Clock Inputs
MR	Master Reset
Q0-Q5	True Outputs
$\overline{\overline{Q}}_0$ – $\overline{\overline{Q}}_5$	Inverting Outputs
Vcco	Vcc to Output

## **PACKAGE/ORDERING INFORMATION**



28-Pin PLCC (J28-1)

# Ordering Information<sup>(1)</sup>

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY10E151JI	J28-1	Industrial	SY10E151JI	Sn-Pb
SY10E151JITR <sup>(2)</sup>	J28-1	Industrial	SY10E151JI	Sn-Pb
SY10E151JC	J28-1	Commercial	SY10E151JC	Sn-Pb
SY10E151JCTR <sup>(2)</sup>	J28-1	Commercial	SY10E151JC	Sn-Pb
SY100E151JC	J28-1	Commercial	SY100E151JC	Sn-Pb
SY100E151JCTR <sup>(2)</sup>	J28-1	Commercial	SY100E151JC	Sn-Pb
SY10E151JY <sup>(3)</sup>	J28-1	Industrial	SY10E151JY with Pb-Free bar-line indicator	Matte-Sn
SY10E151JYTR <sup>(2, 3)</sup>	J28-1	Industrial	SY10E151JY with Pb-Free bar-line indicator	Matte-Sn
SY100E151JZ <sup>(3)</sup>	J28-1	Commercial	SY100E151JZ with Pb-Free bar-line indicator	Matte-Sn
SY100E151JZTR <sup>(2, 3)</sup>	J28-1	Commercial	SY100E151JZ with Pb-Free bar-line indicator	Matte-Sn

### Notes:

- 1. Contact factory for die availability. Dice are guaranteed at  $T_A$  = 25 $^{\circ}$ C, DC Electricals only.
- 2. Tape and Reel.
- 3. Pb-Free package is recommended for new designs.

# TRUTH TABLES<sup>(1)</sup>

## **Asynchronous Operation**

	Output			
Dn	CLK <sub>1</sub>	CLK <sub>2</sub>	Qn(t + 1)	
Х	Х	Х	Н	L

#### Note:

1. H = HIGH Voltage Level

L = LOW Voltage Level

X = Don't Care

 $t = Time\ before\ positive\ CLK\ transition$ 

t+1 = Time after positive CLK transition

u = LOW-to-HIGH transition

## **Synchronous Operation**

	Inputs								
Dn	CLK1	CLK <sub>2</sub>	MR	Qn(t + 1)					
L	u	L	L	L					
Н	u	L	L	Н					
L	L	u	L	L					
Н	L	u	L	Н					
Х	Н	u	L	Qn(t)					
Х	u	Н	Ĺ	Qn(t)					
Х	L	L	L	Qn(t)					

# DC ELECTRICAL CHARACTERISTICS<sup>(1)</sup>

VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C		TA = +25°C			TA = +85°C					
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
Iн	Input HIGH Current	_	_	150		_	150			150	μΑ	
IEE	Power Supply Current										mA	_
	10E	l —	65	78	_	65	78	_	65	78		
	100E	_	65	78	_	65	78	_	75	90		

### Note:

1. Specification for packaged product only.

# AC ELECTRICAL CHARACTERISTICS<sup>(2)</sup>

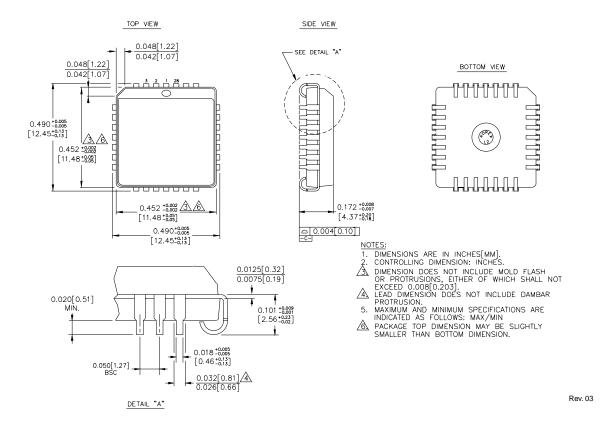
VEE = VEE (Min.) to VEE (Max.); VCC = VCCO = GND

		TA = 0°C		TA = +25°C			TA = +85°C					
Symbol	Parameter	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Unit	Condition
fMAX	Max. Toggle Frequency	1100	1400	_	1100	1400	_	1100	1400	_	MHz	_
tPD	Propagation Delay to Output CLK MR	475 475	650 650	800 850	475 475	650 650	800 850	475 475	650 650	800 850	ps	_
ts	Set-up Time, D	0	-175	_	0	-175	_	0	-175	_	ps	_
tн	Hold Time, D	350	175	_	350	175	_	350	175	_	ps	_
trr	Reset Recovery Time	750	550	_	750	550	_	750	550	_	ps	_
tPW	Minimum Pulse Width CLK, MR	400	_	_	400	_	_	400	_	_	ps	_
tskew	Within-Device Skew	_	65	_	_	65	_	_	65	_	ps	1
tr tf	Rise/Fall Time 20% to 80%	300	450	700	300	450	700	300	450	700	ps	_

### Note:

- 1. Within-device skew is defined as identical transitions on similar paths through a device.
- 2. Specification for packaged product only.

## 28-PIN PLCC (J28-1)



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