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## Memory PICtail™ Plus and PIM Modules

#### Overview

The Memory PICtail™ Plus and PIM modules are a series of boards designed around Microchip Serial EEPROM devices. The boards are designed to interface with the PICtail Plus connector as well as the MPLAB® Memory Starter Kit and the PICkit™ 2.

#### **Key Features of the Board**

- Follows the published recommended usage for each bus, including decoupling capacitors, pull-up/down resistors
- PICtail Plus connector and PICkit 2 connector
- Test points for oscilloscope connections for firmware debugging. (I<sup>2</sup>C™ and UNI/O® only)
- High density, wide voltage range devices mounted to the board

### **Getting Started**

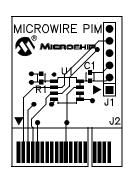
The memory PIM modules allow you to get started right out of the box. By using one of the PIM modules with either the MPLAB Memory Starter Kit or the PICkit 2, you can read and write the Serial EEPROM directly. Many of the application notes written for the PIC® microcontroller families will work directly with the corresponding PIM module, by installing the PIM into the PICtail Plus connector on the Explorer 16 Board. The following devices are mounted on the boards.

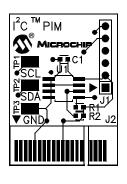
- 11AA160 UNI/O®
- 24AA512 I<sup>2</sup>C™
- 25AA512 SPI
- 93AA86B Microwire

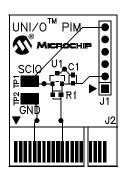
# Memory PICtail™ Plus and PIM Modules

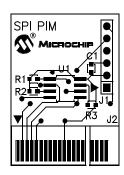
## **Board Configurations**

The four boards that make up the kit are shown below:









## **Signal Interface**

The table below lists the pinouts for the connectors for each of the boards.

## **Table of Pins**

SIGNAL	PlCtail™ Plus Connector				PICkit™ 2 Header			
	UNI/O®	I <sup>2</sup> C™	SPI	MW	UNI/O <sup>®</sup>	I <sup>2</sup> C™	SPI	MW
Vcc	21+22	21+22	21+22	21+22	2	2	2	2
Vss	9+10	9+10	9+10	9+10	3	3	3	3
SCIO	8	_	_	_	6	_	_	_
SDA	_	8	_	_	_	6	_	_
SCL	_	6	_	_	_	5	_	_
CS	_	_	2	2	_	_	1	1
MISO	_	_	5	5	_	_	4	4
MOSI	_	_	7	7	_	_	6	6
SCK	_	_	3	3	_	_	5	5

## Other

More information can be found visiting the Microchip web site at http://www.microchip.com/memory

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