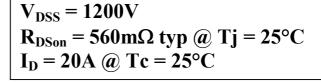
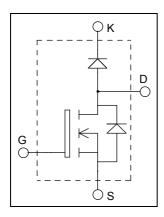


ISOTOP® Boost chopper MOSFET + SiC chopper diode Power module





Application

- AC and DC motor control
- Switched Mode Power Supplies
- Power Factor Correction
- Brake switch

Features

• Power MOS 8TM MOSFET

- Low R_{DSon}
- Low input and Miller capacitance
- Low gate charge
- Avalanche energy rated

• SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- ISOTOP® Package (SOT-227)
- Very low stray inductance
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Stable temperature behavior
- Very rugged
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of VCEsat
- RoHS Compliant

Absolute maximum ratings

ISOTOP®

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Breakdown Voltage		1200	V
т	Continuous Drain Current	$T_c = 25$ °C	20	
I_{D}	Continuous Drain Current	$T_c = 80$ °C	15	A
I_{DM}	Pulsed Drain current			
V_{GS}	Gate - Source Voltage		±30	V
R _{DSon}	Drain - Source ON Resistance		672	mΩ
P_{D}	Maximum Power Dissipation	$T_c = 25^{\circ}C$	543	W
I_{AR}	Avalanche current (repetitive and non repetitive)		14	A

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



All ratings @ $T_j = 25$ °C unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 1200V$	$T_j = 25$ °C			100	^
		$V_{GS} = 0V$	$T_j = 125$ °C			500	μΑ
R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 14A$			560	672	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.5 \text{mA}$		3	4	5	V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}$	•			±100	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$		7736		
C_{oss}	Output Capacitance	$V_{\rm DS} = 25V$		715		pF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz		92		
Q_{g}	Total gate Charge	$V_{GS} = 10V$		300		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 600V$ $I_{\text{D}} = 14A$		50		nC
Q_{gd}	Gate – Drain Charge			140		
$T_{d(on)}$	Turn-on Delay Time	Resistive switching @ 25°C		50		
$T_{\rm r}$	Rise Time	$\begin{aligned} V_{GS} &= 15V \\ V_{Bus} &= 800V \\ I_D &= 14A \end{aligned}$		31		
$T_{d(off)}$	Turn-off Delay Time			170		ns
T_{f}	Fall Time	$R_G = 2.2\Omega$		48		

SiC chopper diode ratings and characteristics

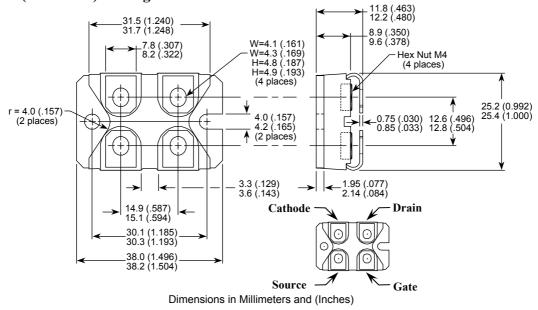
Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			1200			V
T	Maximum Dayarga Lagkaga Current	V -1200V	$T_j = 25^{\circ}C$		32	200	^
I_{RM} Maximum Reverse Leakage Current V_R =1200V $\boxed{}$	$T_j = 175$ °C		56	1000	μΑ		
I_F	DC Forward Current		Tc = 100°C		10		A
$V_{\rm F}$	Diode Forward Voltage	$1_{r_0} = 10\Delta$	$T_j = 25^{\circ}C$		1.6	1.8	V
v _F			$T_j = 175$ °C		2.3	3	v
Qc	Total Capacitive Charge	$I_F = 10A, V_R = 600V$ di/dt = 500A/ μ s			80		nC
С	Total Capacitance	$f = 1MHz, V_R =$	= 200V	96			mE.
		$f = 1MHz, V_R =$	= 400V		69		pF

Thermal and package characteristics

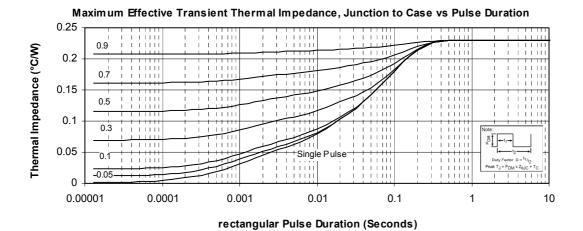
	Characteristic		Min	Тур	Max	Unit
P. 170	Junction to Case Thermal Resistance	Mosfet			0.23	°C/W
R_{thJC}		SiC Diode			1.65	
R_{thJA}	Junction to Ambient (IGBT & Diode)				20	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t = 1 min, 50/60Hz		2500			V
T_{J}, T_{STG}	Storage Temperature Range		-40		150	°C
$T_{ m L}$	Max Lead Temp for Soldering:0.063" from case for 10 sec				300	
Torque	Mounting torque (Mounting = 8-32 or 4mm Machine and terminals = 4mm Machine)				1.5	N.m
Wt	Package Weight			29.2		g



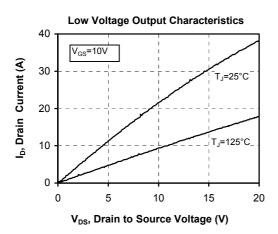
SOT-227 (ISOTOP®) Package Outline

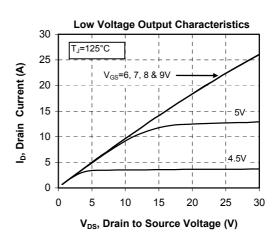


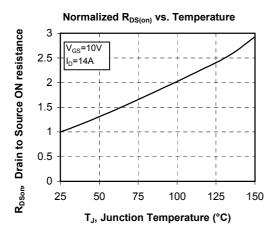
Typical Mosfet Performance Curve



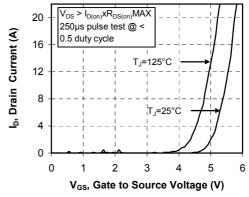


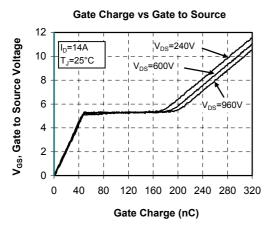


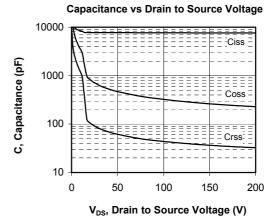






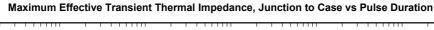


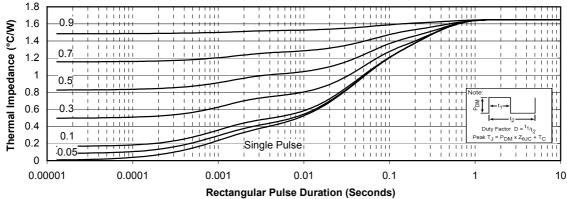


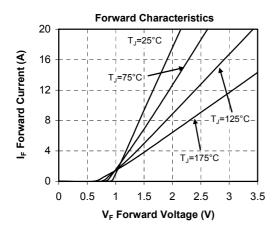


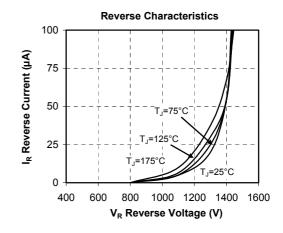


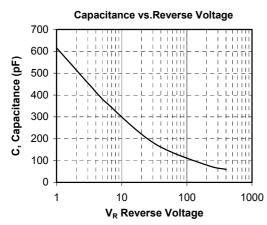
Typical SiC Diode Performance Curve











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