

650V, 95A,  $V_{CE(on)}$ = 1.9V Typical

# Ultra Fast NPT - IGBT®

The Ultra Fast 650V NPT-IGBT® family of products is the newest generation of IGBTs optimized for outstanding ruggedness and best trade-off between conduction and switching losses.

#### **Features**

- · Low Saturation Voltage
- Low Tail Current
- RoHS Compliant

- Short Circuit Withstand Rated
- High Frequency Switching
- Ultra Low Leakage Current





All Ratings: T<sub>c</sub> = 25°C unless otherwise specified.

Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).

#### **MAXIMUM RATINGS**

Symbol	Parameter	Ratings	Unit
V <sub>CES</sub>	Collector Emitter Voltage	650	V
$V_{GE}$	Gate-Emitter Voltage	±30	V
I <sub>C1</sub>	Continuous Collector Current @ T <sub>C</sub> = 25°C	208	
I <sub>C2</sub>	Continuous Collector Current @ T <sub>C</sub> = 110°C	100	Α
I <sub>CM</sub>	Pulsed Collector Current ①	400	
SCWT	Short Circuit Withstand Time: V <sub>CE</sub> = 325V, V <sub>GE</sub> = 15V, T <sub>C</sub> =125°C	10	μs
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C	892	W
T <sub>J</sub> ,T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 to 150	°C
T <sub>L</sub>	Max. Lead Temp. for Soldering: 0.063" from Case for 10 Sec.	300	°C

### STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage (V <sub>GE</sub> = 0V, I <sub>C</sub> = 250uA)	650			
V <sub>GE(TH)</sub>	Gate Threshold Voltage $(V_{CE} = V_{GE}, I_{C} = 2.5 \text{mA}, T_{j} = 25 ^{\circ}\text{C})$	3.5	5.0	6.5	Volts
V <sub>CE(ON)</sub>	Collector-Emitter On Voltage ( $V_{GE} = 15V$ , $I_C = 95A$ , $T_j = 25^{\circ}C$ )	ĺ	1.9	2.4	
	Collector-Emitter On Voltage $(V_{GE} = 15V, I_C = 95A, T_j = 125^{\circ}C)$		2.4		
	Collector-Emitter On Voltage (V <sub>GE</sub> = 15V, I <sub>C</sub> = 190A, T <sub>j</sub> = 25°C)		2.6		
I <sub>CES</sub>	Collector Cut-off Current (V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V, T <sub>j</sub> = 25°C) ②		10	250	μA
	Collector Cut-off Current (V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V, T <sub>j</sub> = 125°C) (2)		100		ĺ
I <sub>GES</sub>	Gate-Emitter Leakage Current (V <sub>GE</sub> = ±20V)			±250	nA

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C <sub>ies</sub>	Input Capacitance	Capacitance		5910		
C <sub>oes</sub>	Output Capacitance	$V_{GE} = 0V, V_{CE} = 25V$		1150		pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz		565	ĺ	
$V_{GEP}$	Gate to Emitter Plateau Voltage	Gate Charge		7.5		V
Q <sup>®</sup>	Total Gate Charge	V <sub>GE</sub> = 15V		312	420	
Q <sub>ge</sub>	Gate-Emitter Charge	V <sub>CE</sub> = 325V		42	55	nC
$Q_{gc}$	Gate- Collector Charge	I <sub>C</sub> = 95A		154	210	
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (25°C)		29		
t <sub>r</sub>	Current Rise Time	V <sub>cc</sub> = 433V		76		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V		226		
t,	Current Fall Time	I <sub>C</sub> = 95A		84		
E <sub>on2</sub> ⑤	Turn-On Switching Energy	$R_{G} = 4.3\Omega^{4}$		3120	4680	1
E <sub>off</sub>	Turn-Off Switching Energy	T <sub>J</sub> = +25°C		2550	3830	μJ
t <sub>d(on)</sub>	Turn-On Delay Time	Inductive Switching (125°C)		29		
t <sub>r</sub>	Current Rise Time	V <sub>cc</sub> = 433V		76		ns
t <sub>d(off)</sub>	Turn-Off Delay Time	V <sub>GE</sub> = 15V		246		
t <sub>f</sub>	Current Fall Time	I <sub>C</sub> = 95A		90		
E <sub>on2</sub>	Turn-On Switching Energy	$R_{G} = 4.3\Omega^{(4)}$		3155	4730	1
E <sub>off</sub>	Turn-Off Switching Energy	T <sub>J</sub> = +125°C		2745	4120	μJ

#### THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	Min	Тур	Max	Unit
$R_{\theta JC}$	Junction to Case Thermal Resistance			.14	°C/W
$R_{\theta JA}$	Junction to Ambient Thermal Resistance			40	
W <sub>T</sub>	Package Weight		.22		oz
			6.2		g

- 1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- 2 Pulse test: Pulse Width <  $380\mu s$ , duty cycle < 2%.
- 3 See Mil-Std-750 Method 3471.
- $4~R_{_{
  m G}}$  is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)
- 5  $E_{on2}$  is the energy loss at turn-on and includes the charge stored in the freewheeling diode.
- 6 E<sub>off</sub> is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

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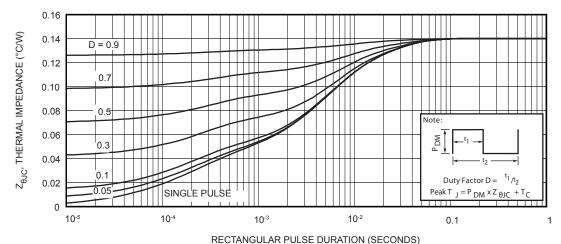
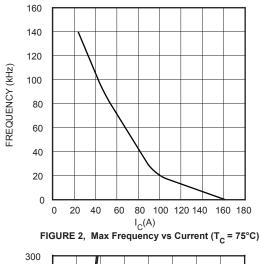
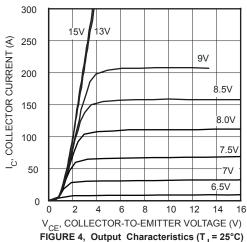
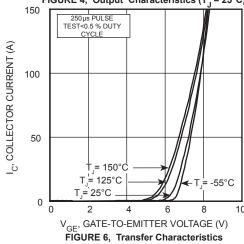


Figure 1, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration







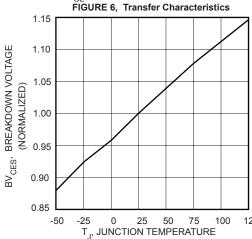
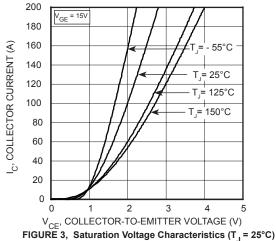


FIGURE 8, Breakdown Voltage vs Junction Temperature



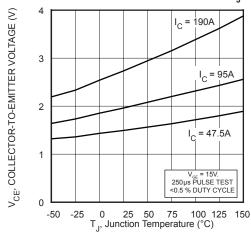
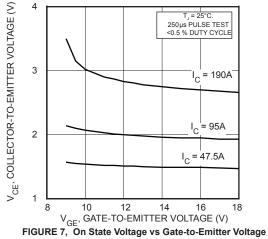


FIGURE 5, On State Voltage vs Junction Temperature



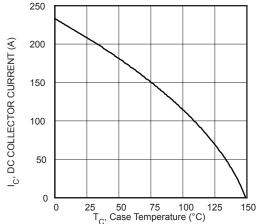
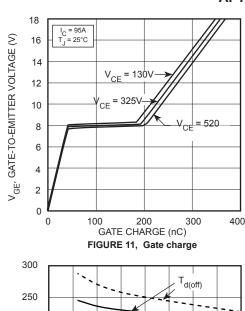
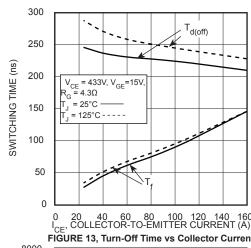
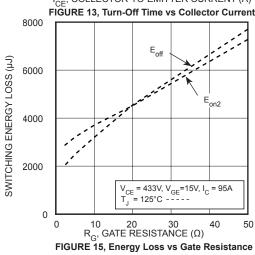


FIGURE 9, DC Collector Current vs Case Temperature

FIGURE 16, Swiitching Energy vs Junction Temperature







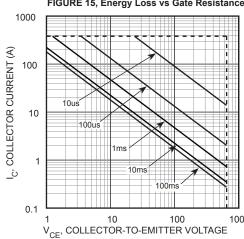
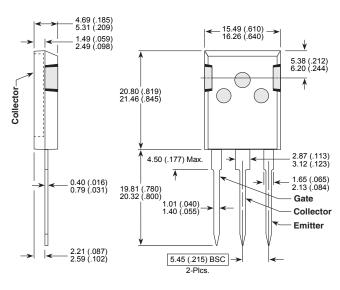


FIGURE 17, Minimum Switching Safe Operating Area

## T-MAX™ (B2) Package Outline



These dimensions are equal to the TO-247 without the mounting hole.

Dimensions in Millimeters and (Inches)

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