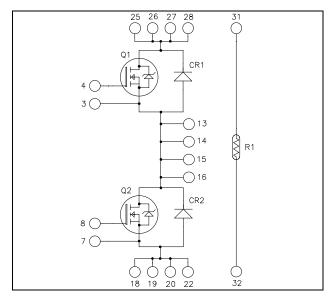


Phase leg SiC MOSFET Power Module

$$\begin{split} V_{DSS} &= 1200V \\ R_{DSon} &= 9m\Omega \ max \ @ \ Tj = 25^{\circ}C \\ I_D &= 295A^* \ @ \ Tc = 25^{\circ}C \end{split}$$



Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

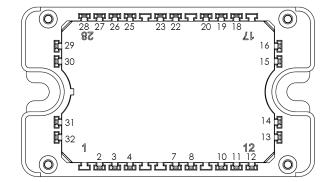
- SiC Power MOSFET
 - High speed switching
 - Low R_{DS(on)}
 - Ultra low loss

SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Very low stray inductance
- Kelvin source for easy drive
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance



- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- **RoHS Compliant**



Pins 25 to 28 must be shorted together Pins 13 to 16 must be shorted together Pins 16/18/19/20 must be shorted together

All ratings @ $T_i = 25$ °C unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Absolute maximum ratings (per SiC MOSFET)

Symbol	Parameter		Max ratings	Unit
$V_{ m DSS}$	Drain - Source Voltage		1200	V
T	Continuous Drain Current	$T_c = 25^{\circ}C$	295*	
I_{D}	Continuous Drain Current	$T_c = 80$ °C	220*	Α
I_{DM}	Pulsed Drain current		590	
V_{GS}	Gate - Source Voltage		-10/25V	V
R_{DSon}	Drain - Source ON Resistance		9	mΩ
P_{D}	Maximum Power Dissipation	$T_c = 25^{\circ}C$	1250	W

^{*} Specification of device but current must be limited due to size of pins.

Electrical Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V$, $V_{DS} = 1200V$				400	μA
D	Drain Cayras an Dagistanas	$V_{GS} = 20V$	$T_j = 25^{\circ}C$		6.25	9	
$R_{DS(on)}$	Drain – Source on Resistance	$I_{\rm D} = 200 A$	$T_j = 150$ °C		11	16	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 40 \text{mA}$			2.4		V
I_{GSS}	Gate – Source Leakage Current	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			2.4	μA	

Dynamic Characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{GS} = 0V$			11		
C_{oss}	Output Capacitance	$V_{DS} = 1000V$			0.88		nF
C_{rss}	Reverse Transfer Capacitance	f = 1MHz			0.06		
Q_{g}	Total gate Charge	$V_{GS} = -5/+20V$			644		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 800V$			184		nC
Q_{gd}	Gate – Drain Charge	$I_{\rm D} = 200 A$			200		
$T_{d(on)}$	Turn-on Delay Time	$V_{GS} = -5/+20V$			35		
T_{r}	Rise Time	$V_{\text{Bus}} = 800 \text{V}$			40		
$T_{d(off)}$	Turn-off Delay Time	$I_D = 200A$; $T_J = 150^{\circ}$			150		ns
T_{f}	Fall Time	$R_L = 4\Omega$; $R_{Gext} = 5\Omega$	$R_L = 4\Omega$; $R_{Gext} = 5\Omega$		70		
Eon	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$	$T_j = 150^{\circ}C$		4.4		mJ
E_{off}	Turn off Energy	$I_{D} = 200A$ $R_{Gext} = 5\Omega$	$T_{j} = 150^{\circ}C$		2.4		1113
R_{Gint}	Internal gate resistance				1.5		Ω
R_{thJC}	Junction to Case Thermal Resistan	ce				0.1	°C/W

Source - Drain diode ratings and characteristics (per SiC MOSFET)

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V	Diode Forward Voltage	$V_{GS} = -5V, I_{SD} = 100A$		3.3		V
V_{SD}	Diode Forward Voltage	$V_{GS} = -2V, I_{SD} = 100A$		3.1		v
t _{rr}	Reverse Recovery Time	$I_{SD} = 200A$; $V_{GS} = -5V$ $V_{R} = 800V$; $di_{F}/dt = 4000A/\mu s$		45		ns
Q _{rr}	Reverse Recovery Charge			1.62		μC
I_{rr}	Reverse Recovery Current	γ _R 300γ, αιμαί 4000Α/μ3		54		A

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SiC schottky diode ratings and characteristics (per SiC diode)

Symbol	Characteristic	Test Conditions			Typ	Max	Unit
V_{RRM}	Peak Repetitive Reverse Voltage					1200	V
T	D	$V_{R}=1200V$	$T_j = 25$ °C		140	800	^
I_{RRM}	Reverse Leakage Current	V _R -1200 V	$T_j = 175$ °C		260	1600	μA
I_F	DC Forward Current		Tc = 125°C		80		A
V_{F}	Diode Forward Voltage	$I_F = 80A$	$T_i = 25$ °C		1.5	1.8	V
V F	Diode Forward Voltage	1 _F - 60A	$T_i = 175^{\circ}C$		2.2	3	v
Qc	Total Capacitive Charge	$I_F = 80A, V_R = 3$ $di/dt = 2000A/\mu s$		520		nC	
C	Total Capacitance	$f = 1 MHz, V_R = 400 V$			372		рF
	Total Capacitance	$f = 1MHz, V_R = 800V$			268		pF
R_{thJC}	Junction to Case Thermal Resistance					0.28	°C/W

Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic		Min	Typ	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B _{25/85}	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T _C =100°C		4		%

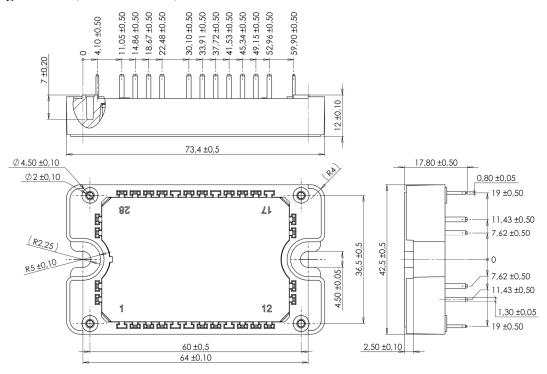
$$R_T = \frac{R_{25}}{\exp \left[B_{25/85} \left(\frac{1}{T_{25}} - \frac{1}{T} \right) \right]} \quad \begin{array}{l} \text{T: Thermistor temperature} \\ R_T: \text{ Thermistor value at T} \end{array}$$

Thermal and package characteristics

Symbol	Characteristic		Min	Max	Unit	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t	=1 min, 50/60H	Z	4000		V
Tı	On creating in partial temporature range SiC MOSFET		SFET	-40	150	
1 ј	Operating junction temperature range SiC dioc			-40	175	
T_{JOP}	Recommended junction temperature under switching conditions				T _J max -25	°C
T_{STG}	Storage Temperature Range				125	
$T_{\rm C}$	Operating Case Temperature	e Temperature				
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight		·		110	g



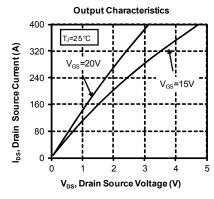
Package outline (dimensions in mm)

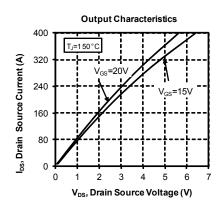


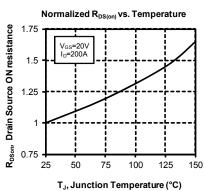
See application note 1906 - Mounting Instructions for SP3F Power Modules on www.microsemi.com

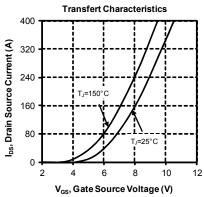


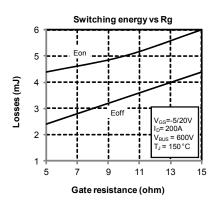
Typical SiC MOSFET Performance Curve

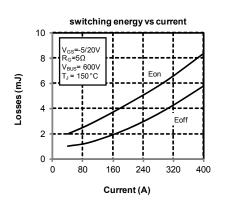


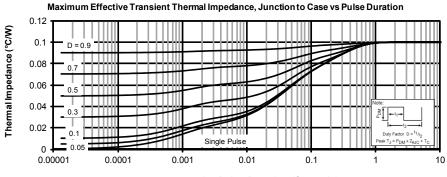






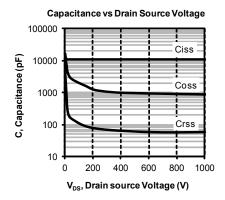


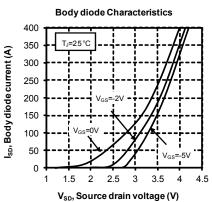


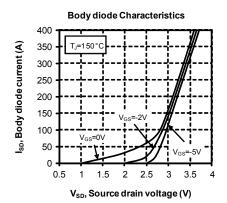


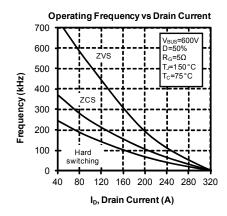
rectangular Pulse Duration (Seconds)

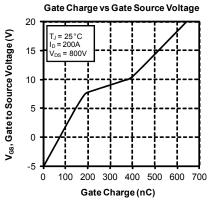


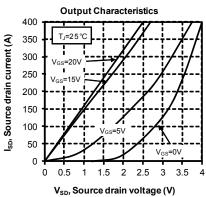


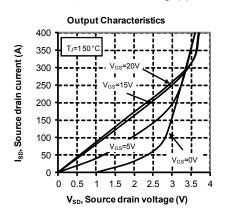










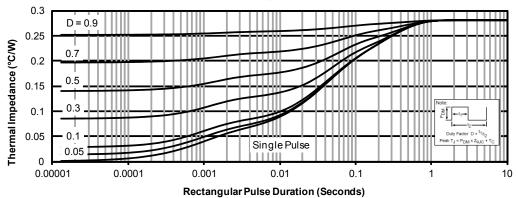


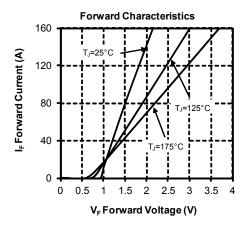
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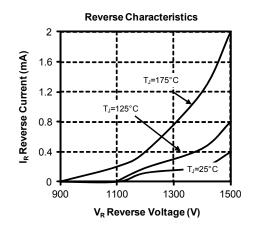


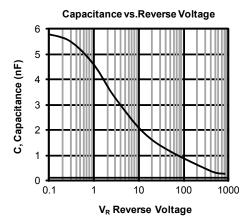
Typical SiC diode Performance Curve

${\bf Maximum\ Effective\ Transient\ Thermal\ Impedance, Junction\ to\ Case\ vs\ Pulse\ Duration}$











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