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ATmega16M1/32M1/32C1/64M1/64C1

Appendix A - Atmel ATmega16M1/ATmega32M1/ ATmega32C1/ATmega64M1/ATmega64C1 Automotive Specification at 150°C

DATASHEET

Description

This document contains information specific to devices operating at temperatures up to 150°C. Only deviations are covered in this appendix, all other information can be found in the complete Automotive datasheet. The complete Automotive datasheet can be found on http://www.atmel.com

1. Electrical Characteristics

1.1 Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Test Conditions	Unit
Operating temperature	–55 to +150	°C
Storage temperature	-65 to +175	°C
Voltage on any pin except RESET with respect to ground	–0.5 to V _{CC} +0.5	V
Voltage on RESET with respect to ground	-0.5 to +13.0	V
Maximum operating voltage	6.0	V
DC current per I/O pin DC current V _{CC} and GND	30 200.0	mA

1.2 DC Characteristics

 $T_A = -40^{\circ}C$ to +150°C, $V_{CC} = 4.5V$ to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Input low voltage, except XTAL1 and RESET pin	V_{CC} = 4.5V to 5.5V	V _{IL}	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, except XTAL1 and RESET pins	$V_{CC} = 4.5V$ to 5.5V	V _{IH}	0.6V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, XTAL1 pin	$V_{CC} = 4.5V$ to 5.5V	V _{IL1}	-0.5		+0.1V _{CC} ⁽¹⁾	V
Input high voltage, XTAL1 pin	V_{CC} = 4.5V to 5.5V	V _{IH1}	0.8V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, RESET pin	$V_{CC} = 4.5V$ to 5.5V	V _{IL2}	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, RESET pin	V_{CC} = 4.5V to 5.5V	V _{IH2}	0.9V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, RESET pin as I/O	V_{CC} = 4.5V to 5.5V	V _{IL3}	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, RESET pin as I/O	$V_{CC} = 4.5V$ to 5.5V	V _{IH3}	0.8V _{CC} ⁽²⁾		V _{CC} + 0.5	V

Notes: 1. "Max" means the highest value where the pin is guaranteed to be read as low

2. "Min" means the lowest value where the pin is guaranteed to be read as high

Although each I/O port can sink more than the test conditions (20mA at V_{CC} = 5V) under steady state conditions (non-transient), the following must be observed:

1] The sum of all IOL, for all ports, should not exceed 400mA.

2] The sum of all IOL, for ports C0 - C5, should not exceed 200mA.

3] The sum of all IOL, for ports C6, D0 - D4, should not exceed 300mA.

4] The sum of all IOL, for ports B0 - B7, D5 - D7, should not exceed 300mA.

If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.

 For temperature range +125°C to +150°C only. For -40°C to +125°C, refer to ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 Automotive datasheet.



1.2 DC Characteristics (Continued)

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Output low voltage ⁽³⁾ , I/O pin except RESET	I_{OL} = 10mA, V_{CC} = 5V	V _{OL}			0.8	V
Output high voltage ⁽⁴⁾ , I/O pin except RESET	$I_{OH} = -10$ mA, $V_{CC} = 5V$	V _{OH}	4.1			V
Output high voltage (reset pin)	$I_{OH} = 0.6 \text{mA}, V_{CC} = 5 \text{V}$	V _{OH3}	3			V
Input leakage current I/O pin	V _{CC} = 5.5V, pin low (absolute value)	I _{IL}			1	μΑ
Input leakage current I/O pin	V _{CC} = 5.5V, pin high (absolute value)	I _{IH}			1	μΑ
Reset pull-up resistor		R _{RST}	30		200	kΩ
I/O pin pull-up resistor		R _{PU}	20		50	kΩ
Power supply current ⁽⁴⁾	Active 16MHz, $V_{CC} = 5V$, External clock, PRR = 0xFF,	I _{CC}		14	30	mA
	ldle 16MHz, V _{CC} = 5V, External clock	I _{CC IDLE}		5.5	15	mA
Power-down mode	WDT enabled, $V_{CC} = 5V$	1		80	330	μA
	WDT disabled, $V_{CC} = 5V$	ICC PWD		70	310	μA
Analog comparator Input leakage current	$V_{CC} = 5V$ $V_{in} = V_{CC}/2$	I _{ACLK}	-200		+200	nA

Notes: 1. "Max" means the highest value where the pin is guaranteed to be read as low

2. "Min" means the lowest value where the pin is guaranteed to be read as high

3. Although each I/O port can sink more than the test conditions (20mA at $V_{CC} = 5V$) under steady state conditions (non-transient), the following must be observed:

1] The sum of all IOL, for all ports, should not exceed 400mA.

2] The sum of all IOL, for ports C0 - C5, should not exceed 200mA.

3] The sum of all IOL, for ports C6, D0 - D4, should not exceed 300mA.

4] The sum of all IOL, for ports B0 - B7, D5 - D7, should not exceed 300mA.

If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.

4. For temperature range +125°C to +150°C only. For -40°C to +125°C, refer to ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 Automotive datasheet.



1.3 ADC Characteristics in Single-ended Mode

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
Resolution	Single ended, Temp = -40°C to 150°C			10		Bit
	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 1MHz	THE		3.2	6.0	LSB
	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 2MHz	TOL		3.2	6.0	
Integral Non Linearity	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 1MHz	INI		0.7	3.0	LSB
Integral Non Linearity	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 2MHz	INC		0.8	3.0	
Differential Non Linearity	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 1MHz	וואס		0.5	1.5	I SB
	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 2MHz	DINE		0.6	1.5	LOD
Gain error	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 1MHz	_	-10.0	-5.0	+0.0	I SB
	$V_{CC} = 5V, V_{Ref} = 2.56V,$ ADC clock = 2MHz		-10.0	-5.0	+0.0	LOD
Offset error	$V_{CC} = 5V$, $V_{Ref} = 2.56V$, ADC clock = 1MHz		-2.0	+2.5	+6.0	I SB
	$V_{CC} = 5V, V_{Ref} = 2.56V,$ ADC clock = 2MHz		-2.0	+2.5	+6.0	200
Reference voltage		V _{REF}	2.56		AVCC	V

 $T_{\rm A}$ = –40°C to +150°C, $V_{\rm CC}$ = 4.5V to 5.5V (unless otherwise noted)

1.4 ADC Characteristics in Differential Mode

 T_{A} = –40°C to +150°C, V_{CC} = 4.5V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
Resolution	Differential conversion, gain = 5x, Temp = -40° C to 150°C			8		
	Differential conversion, gain = 10x, Temp = -40°C to 150°C			8		Pit
	Differential conversion, gain = 20x, Temp = -40°C to 150°C	-		8		Dit
	Differential conversion, gain = 40x, Temp = -40°C to 150°C			8		



ADC Characteristics in Differential Mode (Continued) 1.4

 $T_{\rm A}$ = –40°C to +150°C, $V_{\rm CC}$ = 4.5V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
Absolute accuracy	$\begin{array}{l} \text{Gain} = 5\text{x}, \ 10\text{x}, \ \text{V}_{\text{CC}} = 5\text{V}, \\ \text{V}_{\text{Ref}} = 2.56\text{V}, \\ \text{ADC clock} = 2\text{MHz} \end{array}$			1.5	3.5	
	$\label{eq:eq:constraint} \begin{split} & \text{Gain} = 20\text{x}, \text{V}_{\text{CC}} = 5\text{V}, \\ & \text{V}_{\text{Ref}} = 2.56\text{V}, \\ & \text{ADC clock} = 2\text{MHz} \end{split}$	TUE		1.5	4.0	LSB
				1.5	6.0	
	$\label{eq:Gain} \begin{split} & \text{Gain} = 5\text{x}, \ 10\text{x}, \ \text{V}_{\text{CC}} = 5\text{V}, \\ & \text{V}_{\text{Ref}} = 2.56\text{V}, \\ & \text{ADC clock} = 2\text{MHz} \end{split}$			0.1	1.5	
Integral non linearity	$\label{eq:Gain} \begin{split} & \text{Gain} = 20 \text{x}, \text{V}_{\text{CC}} = 5 \text{V}, \\ & \text{V}_{\text{Ref}} = 2.56 \text{V}, \\ & \text{ADC clock} = 2 \text{MHz} \end{split}$	INL		0.2	2.5	LSB
	$\label{eq:Gain} \begin{array}{l} \mbox{Gain} = 40 x, \ V_{CC} = 5 V, \\ V_{Ref} = 2.56 V, \\ \mbox{ADC clock} = 2 M H z \end{array}$			0.7	4.5	
Differential non linearity	$\label{eq:Gain} \begin{split} & \text{Gain} = 5\text{x}, \ 10\text{x}, \ \text{V}_{\text{CC}} = 5\text{V}, \\ & \text{V}_{\text{Ref}} = 2.56\text{V}, \\ & \text{ADC clock} = 2\text{MHz} \end{split}$			0.1	1.5	
	$\label{eq:Gain} \begin{split} & \text{Gain} = 20 \text{x}, \text{V}_{\text{CC}} = 5 \text{V}, \\ & \text{V}_{\text{Ref}} = 2.56 \text{V}, \\ & \text{ADC clock} = 2 \text{MHz} \end{split}$	DNL		0.2	2.0	LSB
	$\label{eq:Gain} \begin{array}{l} \text{Gain} = 40 \text{x}, \ \text{V}_{\text{CC}} = 5 \text{V}, \\ \text{V}_{\text{Ref}} = 2.56 \text{V}, \\ \text{ADC clock} = 2 \text{MHz} \end{array}$			0.3	4.0	
Gain error			-3.0		+3.0	ISB
			-3.0		+3.0	LOD
Offset error			-3.0		+3.0	I SB
	$ Gain = 20x, 40x, \\ V_{CC} = 5V, V_{Ref} = 2.56V, \\ ADC clock = 2MHz $		-4.0		+4.0	LOD
Reference voltage		V_{REF}	2.56		AVCC - 0.5	V

1.5 **Memory Endurance**

EEPROM endurance: 50,000 Write/Erase cycles. Flash endurance: 10,000 Write/Erase cycles.

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2. Grade 0 Qualification

The ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 has been developed and manufactured according to the most stringent quality assurance requirements of ISO-TS-16949 and verified during product qualification as per AEC-Q100 grade 0.

AEC-Q100 qualification relies on temperature accelerated stress testing. High temperature field usage however may result in less significant stress test acceleration. In order to prevent the risk that

ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 lifetime would not satisfy the application end-of-life reliability requirements, Atmel[®] has extended the testing, whenever applicable (high temperature operating life test, high temperature storage life, data retention, thermal cycles), far beyond the AEC-Q100 requirements. Thereby, Atmel verified the ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 has a long safe lifetime period after the grade 0 qualification acceptance limits.

The valid domain calculation depends on the activation energy of the potential failure mechanism that is considered. Therefore any temperature mission profile which could exceed the AEC-Q100 equivalence domain shall be submitted to Atmel for a thorough reliability analysis



Figure 2-1. AEC-Q100 Lifetime Equivalence

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3. Ordering Information

Table 3-1. ATmega16/32/64/M1/C1 Ordering Code

Speed is 16MHz and Power Supply is 4.5V to 5.5V for all devices. Operating range is the same for all devices too Extended $(-40^{\circ}C \text{ to } +150^{\circ}C)$.

Speed (MHz)	Power Supply	Ordering Code	Package ⁽¹⁾	Operation Range
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1-15AD	MA	Extended (–40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATA8888-15MD	PV	Extended (–40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATA8888-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-15AD	МА	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-15AD	MA	Extended (–40°C to +150°C)

Notes: 1. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also halide free and fully green.

2. For speed versus $V_{cc}\!,$ see complete datasheet.

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4. Package Information

Table 4-1.Package Types

	Package Type
PV	32-lead, 7.0mm \times 7.0mm body, 0.65mm pitch, quad flat no lead package (QFN)
MA	MA, 32 - lead, 7mm \times 7mm body size, 1.0mm body thickness 0.8mm lead pitch, thin profile plastic quad flat package (TQFP)

Figure 4-1. PV



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Figure 4-2. MA



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5. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
7781F-AVR-07/14	Put datasheet in the latest template
7781E-AVR-03/12	Package MA updated
7781D-AVR-01/10	Section 1.2 "DC Characteristics" on page 3 changed
	DC characteristics updated
7781C-A\/R-04/09	ADC characteristics updated
	Memory endurance added
	Ordering code added
7781B-AVR-05/08	Added AEC-Q100 lifetime equivalence graph, page 4
7781A-AVR-03/08	Document creation



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