

ATmega16M1/32M1/32C1/64M1/64C1

Appendix A - Atmel ATmega16M1/ATmega32M1/ ATmega32C1/ATmega64M1/ATmega64C1 Automotive Specification at 150°C

DATASHEET

Description

This document contains information specific to devices operating at temperatures up to 150°C. Only deviations are covered in this appendix, all other information can be found in the complete Automotive datasheet. The complete Automotive datasheet can be found on http://www.atmel.com

1. **Electrical Characteristics**

1.1 **Absolute Maximum Ratings**

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Parameters	Test Conditions	Unit
Operating temperature	-55 to +150	°C
Storage temperature	-65 to +175	°C
Voltage on any pin except RESET with respect to ground	-0.5 to $V_{\rm CC}$ +0.5	V
Voltage on RESET with respect to ground	-0.5 to +13.0	V
Maximum operating voltage	6.0	V
DC current per I/O pin DC current V _{CC} and GND	30 200.0	mA

1.2 **DC Characteristics**

 $T_A = -40$ °C to +150°C, $V_{CC} = 4.5$ V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Input low voltage, except XTAL1 and RESET pin	V _{CC} = 4.5V to 5.5V	V _{IL}	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, except XTAL1 and RESET pins	V _{CC} = 4.5V to 5.5V	V _{IH}	0.6V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, XTAL1 pin	V _{CC} = 4.5V to 5.5V	V_{IL1}	-0.5		+0.1V _{CC} ⁽¹⁾	V
Input high voltage, XTAL1 pin	V _{CC} = 4.5V to 5.5V	$V_{\rm IH1}$	0.8V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, RESET pin	V _{CC} = 4.5V to 5.5V	$V_{\rm IL2}$	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, RESET pin	V _{CC} = 4.5V to 5.5V	$V_{\rm IH2}$	0.9V _{CC} ⁽²⁾		V _{CC} + 0.5	V
Input low voltage, RESET pin as I/O	V _{CC} = 4.5V to 5.5V	V_{IL3}	-0.5		+0.2V _{CC} ⁽¹⁾	V
Input high voltage, RESET pin as I/O	V _{CC} = 4.5V to 5.5V	V_{IH3}	0.8V _{CC} ⁽²⁾		V _{CC} + 0.5	V

Notes: "Max" means the highest value where the pin is guaranteed to be read as low

- "Min" means the lowest value where the pin is guaranteed to be read as high
- 3. Although each I/O port can sink more than the test conditions (20mA at V_{CC} = 5V) under steady state conditions (nontransient), the following must be observed:
 - 1] The sum of all IOL, for all ports, should not exceed 400mA.
 - 2] The sum of all IOL, for ports C0 C5, should not exceed 200mA.
 - 3] The sum of all IOL, for ports C6, D0 D4, should not exceed 300mA.
 - 4] The sum of all IOL, for ports B0 B7, D5 D7, should not exceed 300mA.
 - If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.
- 4. For temperature range +125°C to +150°C only. For –40°C to +125°C, refer to ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 Automotive datasheet.



1.2 DC Characteristics (Continued)

 $T_A = -40$ °C to +150°C, $V_{CC} = 4.5$ V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min.	Тур.	Max.	Unit
Output low voltage ⁽³⁾ , I/O pin except RESET	I _{OL} = 10mA, V _{CC} = 5V	V _{OL}			0.8	V
Output high voltage ⁽⁴⁾ , I/O pin except RESET	$I_{OH} = -10 \text{mA}, V_{CC} = 5 \text{V}$	V _{OH}	4.1			V
Output high voltage (reset pin)	$I_{OH} = 0.6 \text{mA}, V_{CC} = 5 \text{V}$	V_{OH3}	3			V
Input leakage current I/O pin	V _{CC} = 5.5V, pin low (absolute value)	I _{IL}			1	μΑ
Input leakage current I/O pin	V _{CC} = 5.5V, pin high (absolute value)	I _{IH}			1	μΑ
Reset pull-up resistor		R _{RST}	30		200	kΩ
I/O pin pull-up resistor		R_{PU}	20		50	kΩ
Dower cumply current(4)	Active 16MHz, V_{CC} = 5V, External clock, PRR = 0xFF,	I _{cc}		14	30	mA
Power supply current ⁽⁴⁾	Idle 16MHz, V _{CC} = 5V, External clock	I _{CC IDLE}		5.5	15	mA
Power-down mode	WDT enabled, V_{CC} = 5V	ı		80	330	μΑ
	WDT disabled, V_{CC} = 5V	I _{CC PWD}		70	310	μΑ
Analog comparator Input leakage current	$V_{CC} = 5V$ $V_{in} = V_{CC}/2$	I _{ACLK}	-200		+200	nA

Notes: 1. "Max" means the highest value where the pin is guaranteed to be read as low

- 2. "Min" means the lowest value where the pin is guaranteed to be read as high
- 3. Although each I/O port can sink more than the test conditions (20mA at V_{CC} = 5V) under steady state conditions (non-transient), the following must be observed:
 - 1] The sum of all IOL, for all ports, should not exceed 400mA.
 - 2] The sum of all IOL, for ports C0 C5, should not exceed 200mA.
 - 3] The sum of all IOL, for ports C6, D0 D4, should not exceed 300mA.
 - 4] The sum of all IOL, for ports B0 B7, D5 D7, should not exceed 300mA.
 - If IOL exceeds the test condition, VOL may exceed the related specification. Pins are not guaranteed to sink current greater than the listed test condition.
- 4. For temperature range +125°C to +150°C only. For –40°C to +125°C, refer to ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 Automotive datasheet.



1.3 ADC Characteristics in Single-ended Mode

 $T_A = -40$ °C to +150°C, $V_{CC} = 4.5$ V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
Resolution	Single ended, Temp = -40°C to 150°C			10		Bit
Absolute accuracy	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 1MHz	TUE		3.2	6.0	LSB
Absolute decuracy	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz	TOL		3.2	6.0	200
Integral Non Linearity	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 1MHz	INL		0.7	3.0	LSB
integral Non Linearity	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz	IIVE		0.8	3.0	
Differential Non Linearity	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 1MHz	DNL		0.5	1.5	LSB
	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz			0.6	1.5	LOD
Gain error	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 1MHz		-10.0	-5.0	+0.0	LSB
	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz		-10.0	-5.0	+0.0	200
Offset error	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 1MHz		-2.0	+2.5	+6.0	LSB
	V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz		-2.0	+2.5	+6.0	LOD
Reference voltage		V_{REF}	2.56		AVCC	V

1.4 ADC Characteristics in Differential Mode

 $\rm T_A$ = $-40^{\circ} C$ to +150°C, $\rm V_{CC}$ = 4.5V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
gain = 8 Temp = Differen gain = 1 Temp = Differen gain = 2 Temp = Differen gain = 2 Temp =	Differential conversion, gain = 5x, Temp = -40°C to 150°C			8		
	Differential conversion, gain = 10x, Temp = -40°C to 150°C			8		Bit
	Differential conversion, gain = 20x, Temp = -40°C to 150°C			8		ы
	Differential conversion, gain = 40x, Temp = -40°C to 150°C			8		

ADC Characteristics in Differential Mode (Continued) 1.4

 $T_A = -40$ °C to +150°C, $V_{CC} = 4.5$ V to 5.5V (unless otherwise noted)

Parameters	Test Conditions	Symbol	Min	Тур	Max	Unit
Absolute accuracy	$\begin{aligned} &\text{Gain} = 5\text{x}, \ 10\text{x}, \ \text{V}_{\text{CC}} = 5\text{V}, \\ &\text{V}_{\text{Ref}} = 2.56\text{V}, \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$			1.5	3.5	
	$\begin{aligned} &\text{Gain} = 20\text{x, V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$	TUE		1.5	4.0	LSB
	$\begin{aligned} &\text{Gain} = 40\text{x, V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$			1.5	6.0	
	$\begin{aligned} &\text{Gain} = 5\text{x, } 10\text{x, } \text{V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$			0.1	1.5	
Integral non linearity	$\begin{aligned} &\text{Gain} = 20\text{x, V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$	INL		0.2	2.5	LSB
	$\begin{aligned} &\text{Gain} = 40\text{x, V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$			0.7	4.5	
	$\begin{aligned} &\text{Gain} = 5\text{x, } 10\text{x, } \text{V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$			0.1	1.5	
Differential non linearity	$\begin{aligned} &\text{Gain} = 20\text{x, V}_{\text{CC}} = 5\text{V,} \\ &\text{V}_{\text{Ref}} = 2.56\text{V,} \\ &\text{ADC clock} = 2\text{MHz} \end{aligned}$	DNL		0.2	2.0	LSB
	Gain = 40x, V_{CC} = 5V, V_{Ref} = 2.56V, ADC clock = 2MHz			0.3	4.0	
Coin array	$Gain = 5x, 10x, V_{CC} = 5V,$ $V_{Ref} = 2.56V,$ $ADC clock = 2MHz$		-3.0		+3.0	Len
Gain error	Gain = 20x, 40x, V _{CC} = 5V, V _{Ref} = 2.56V, ADC clock = 2MHz		-3.0		+3.0	LSB
Offset error	$Gain = 5x, 10x, V_{CC} = 5V,$ $V_{Ref} = 2.56V,$ $ADC clock = 2MHz$		-3.0		+3.0	LSB
	Gain = 20x, 40x, V _{CC} = 5V, V _{Ref} = 2.56V, ADC clock = 2MHz		-4.0		+4.0	LOB
Reference voltage		V_{REF}	2.56		AVCC - 0.5	V

1.5 **Memory Endurance**

EEPROM endurance: 50,000 Write/Erase cycles. Flash endurance: 10,000 Write/Erase cycles.



2. Grade 0 Qualification

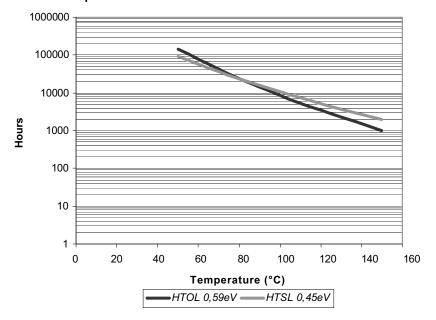
The ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 has been developed and manufactured according to the most stringent quality assurance requirements of ISO-TS-16949 and verified during product qualification as per AEC-Q100 grade 0.

AEC-Q100 qualification relies on temperature accelerated stress testing. High temperature field usage however may result in less significant stress test acceleration. In order to prevent the risk that

ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 lifetime would not satisfy the application end-of-life reliability requirements, Atmel[®] has extended the testing, whenever applicable (high temperature operating life test, high temperature storage life, data retention, thermal cycles), far beyond the AEC-Q100 requirements. Thereby, Atmel verified the ATmega16M1/ATmega16M1/32M1/32C1/64M1/64C1/ATA8888/ATmega64M1/ATmega64C1 has a long safe lifetime period after the grade 0 qualification acceptance limits.

The valid domain calculation depends on the activation energy of the potential failure mechanism that is considered. Therefore any temperature mission profile which could exceed the AEC-Q100 equivalence domain shall be submitted to Atmel for a thorough reliability analysis





Ordering Information 3.

Table 3-1. ATmega16/32/64/M1/C1 Ordering Code

Speed is 16MHz and Power Supply is 4.5V to 5.5V for all devices. Operating range is the same for all devices too Extended (-40°C to +150°C).

Speed (MHz)	Power Supply	Ordering Code	Package ⁽¹⁾	Operation Range
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-15MD	PV	Extended $(-40^{\circ}\text{C to } +150^{\circ}\text{C})$
16 ⁽²⁾	4.5V to 5.5V	ATmega16M1/32M1/32C1/64M 1/64C1-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATA8888-15MD	PV	Extended $(-40^{\circ}\text{C to } +150^{\circ}\text{C})$
16 ⁽²⁾	4.5V to 5.5V	ATA8888-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-ESMD	PV	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-ESAD	MA	Engineering samples
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-15MD	PV	Extended $(-40^{\circ}\text{C to } +150^{\circ}\text{C})$
16 ⁽²⁾	4.5V to 5.5V	ATmega64M1-15AD	MA	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-15MD	PV	Extended (-40°C to +150°C)
16 ⁽²⁾	4.5V to 5.5V	ATmega64C1-15AD	MA	Extended (-40°C to +150°C)



Notes: 1. Pb-free packaging, complies to the European Directive for Restriction of Hazardous Substances (RoHS directive). Also halide free and fully green.

^{2.} For speed versus V_{cc} , see complete datasheet.

4. Package Information

Table 4-1. Package Types

	Package Type
PV	32-lead, 7.0mm \times 7.0mm body, 0.65mm pitch, quad flat no lead package (QFN)
MA	MA, 32 - lead, $7\text{mm} \times 7\text{mm}$ body size, 1.0mm body thickness 0.8mm lead pitch, thin profile plastic quad flat package (TQFP)



Figure 4-1. PV

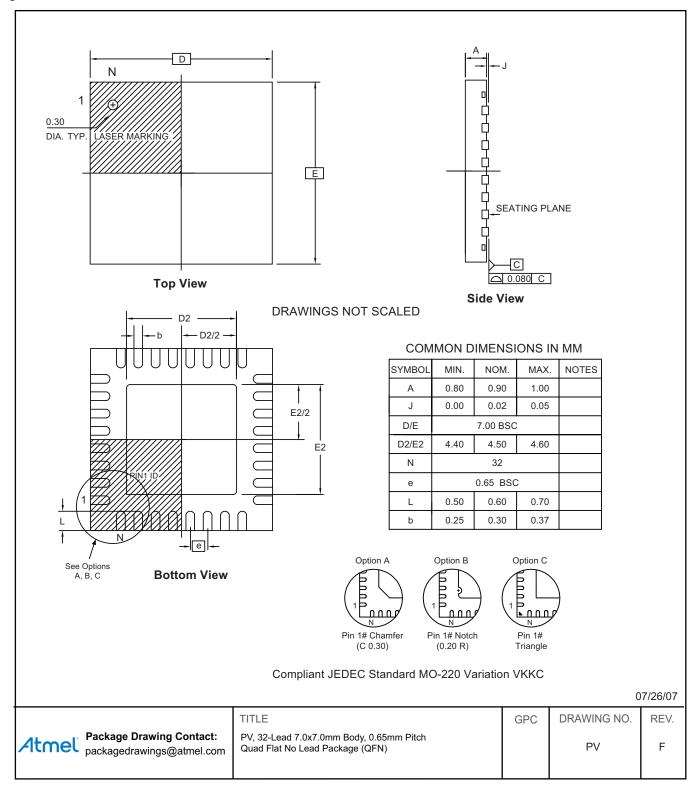
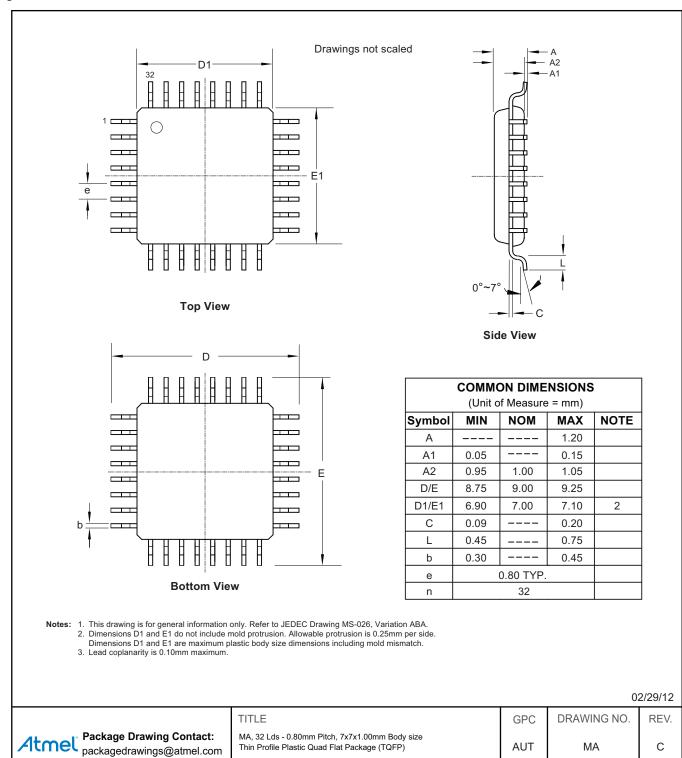




Figure 4-2. MA



5. Revision History

Please note that the following page numbers referred to in this section refer to the specific revision mentioned, not to this document.

Revision No.	History
7781F-AVR-07/14	Put datasheet in the latest template
7781E-AVR-03/12	Package MA updated
7781D-AVR-01/10	 Section 1.2 "DC Characteristics" on page 3 changed
7781C-AVR-04/09	DC characteristics updated
	ADC characteristics updated
	Memory endurance added
	Ordering code added
7781B-AVR-05/08	 Added AEC-Q100 lifetime equivalence graph, page 4
7781A-AVR-03/08	Document creation





1600 Technology Drive, San Jose, CA 95110 USA



F: (+1)(408) 436.4200







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T: (+1)(408) 441.0311

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MB95F634KPMC-G-SNE2 MB95F636KWQN-G-SNE1 MB95F696KPMC-G-SNE2 MB95F698KPMC1-G-SNE2 MB95F698KPMC2-GSNE2 MB95F698KPMC-G-SNE2 MB95F818KPMC1-G-SNE2 MC908JK1ECDWER MC9S08PT60AVLD R5F1076CMSPV0 C8051F389B-GQ C8051F392-A-GMR C8051F580-IQR ISD-ES1600_USB_PROG 901015X SC705C8AE0VFBE STM8TL53G4U6 PIC16F877-04/PB CY8C3MFIDOCK-125 403708R MB95F354EPF-G-SNE2 MB95F564KPFT-G-SNE2 MB95F564KWQN-G-SNE1 MB95F636KP-G-SHSNE2 MB95F636KPMC-G-SNE2 MB95F694KPMC-G-SNE2 MB95F778JPMC1-G-SNE2 MB95F818KPMC-G-SNE2 MC908QY8CDWER
MC9S08PT16AVLD MC9S08PT32AVLH MC9S08PT60AVLC MC9S08PT60AVLH C8051F500-IQR LC87F0G08AUJA-AH CP8361BT
STM8S207C6T3 CG8421AF CYTMA445-48LQI36AA STM8L151G4Y6TR