

maXTouch 1664-node Touchscreen Controller Product Brief

Description

The mXT1664T3 2.0 uses a unique charge-transfer acquisition engine to implement Microchip's patented capacitive sensing method. Coupled with a state-of-the-art CPU, the entire touchscreen sensing solution can measure, classify and track a number of individual finger touches with a high degree of accuracy in the shortest response time. The mXT1664T3 2.0 allows for both mutual and self capacitance measurements, with the self capacitance measurements being used to augment the mutual capacitance measurements to produce reliable touch information.

maXTouch[®] Adaptive Sensing Touchscreen Technology

- Up to 32 X (transmit) lines and 52 Y (receive) lines for use by touchscreen.
- A maximum of 1664 nodes can be allocated to the touchscreen
- Touchscreen size of 15.6 inches (16:10 aspect ratio), assuming a sensor electrode pitch of 6.5 mm. Other sizes are possible with different electrode pitches and appropriate sensor material
- Multiple touch support with up to 16 concurrent touches tracked in real time
- Dual-boot OS support for $\mathsf{Microsoft}^{\texttt{®}}$ $\mathsf{Windows}^{\texttt{®}}$ and $\mathsf{Android}^{^{\mathsf{TM}}}$

Touch Sensor Technology

- Discrete/out-cell support including glass and PET filmbased sensors
- Support for standard (for example, Diamond) and proprietary sensor patterns (review of designs by Microchip or a Microchip-qualified touch sensor module partner is recommended)

Front Panel Material

- Works with PET or glass, including curved profiles (configuration and stack-up to be approved by Microchip or a Microchip-qualified touch sensor module partner)
- Glass 0.4 mm to 4.5 mm (dependent on screen size, touch size, configuration and stack-up)
- Plastic 0.2 mm to 2.2 mm (dependent on screen size, touch size, configuration and stack-up)

Touch Performance

- Moisture/Water Compensation
 - No false touch with condensation or water drop up to 22 mm diameter
 - One-finger tracking with condensation or water drop up to 22 mm diameter

- Glove Support
 - Multiple-finger glove touches up to 1.5 mm thickness (subject to stack-up design)
 - Single-finger glove touch up to 5 mm thickness (subject to stack-up design)
- Mutual capacitance and self capacitance
 measurements supported for robust touch detection
- Noise suppression technology to combat ambient, charger, and power-line noise
 - Up to 240 $V_{\mbox{\scriptsize PP}}$ between 1 Hz and 1 kHz sinusoidal waveform
 - Up to 20 V_{PP} between 1 kHz and 1 MHz sinusoidal waveform
- Stylus Support
 - Supports passive stylus with 1.5 mm contact diameter, subject to configuration, stack-up, and sensor design
- Scan Speed
 - Up to 250 Hz reporting rate for one finger (subject to configuration)
 - Typical report rate for 16 touches ≥100 Hz (subject to configuration)
 - Initial touch latency <10 ms for first touch from idle (subject to configuration)
 - Configurable to allow for power and speed optimization

Enhanced Algorithms

- · Lens bending algorithms to remove display noise
- Touch suppression algorithms to remove unintentional large touches, such as palm
- Palm Recovery Algorithm for quick restoration to normal state

Power Saving

- Programmable timeout for automatic transition from active to idle states
- · Pipelined analog sensing detection and digital processing to optimize system power efficiency

Application Interfaces

- I²C slave with support for Standard mode (up to 100 kHz), Fast mode (up to 400 kHz), Fast-mode Plus (up to 1 MHz), High Speed mode (up to 3.4 MHz)
- USB HID interface for Microsoft Windows 8.x and later versions
- HID-I²C interface for Microsoft Windows 8.x and later versions
- Interrupt to indicate when a message is available
- SPI Debug Interface to read the raw data for tuning and debugging purposes

Power Supply

- Digital (Vdd) 3.3 V nominal
- Digital I/O (VddIO) 3.3 V nominal
- Analog (AVdd) 3.3 V nominal
- High voltage external X line drive (XVdd) up to 10 V

Packages

- 136-ball UFBGA 7 x 7 x 0.6 mm, 0.5 mm pitch
- 162-ball UFBGA 10 × 5 × 0.6 mm, 0.5 mm pitch

Operating Temperature

• −40°C to +85°C

PIN CONFIGURATION

0.1 136-ball UFBGA

_	1	2	3	4	5	6	7	8	9	10	11	12	13
A	О Х31	O X30	O X28	O X25	O X22	О X19	O XVDD		O X12	O X9	О Хб	O X3	O x1
в		O X29	() X27	O X24) X21	О X18	О X16	O X14	O X11	О Х8	() X5	O X2	O x0
с	GND		О _{Х26}) X23	() X20	O X17	O X15	О х13	⊖ x10	O x7	⊖ _{X4}	O XVDD	VREGBOOST
D	AVDD		GND									GND	AVDD
Е	O Y27	О _{У26}	AVDD								AVDD	O Y0	O Y1
F	О Ү30	О У29	О _{У28}			GPIO0	GPIO4	GND			O Y2	О _{Ү3}	О Ү4
G	О _{Y33}	О _{У32}	О _{Y31}					D <u>BG_SS</u> TEST			⊖ ∀5	О Үб	O Y7
н	О Ү36	О У35	О Ү34			NOISE_IN	GPIO3	DBG_DATA XTAL_XOUT		DS0	О Ү8	О Ү9	О Y10
J	О Ү39	О У38	О _{Y37}								O Y11	O Y12	О _{Y13}
к	O Y42	O Y41	O Y40		USBDM	I2CMODE	GND	O PTCXY0	O PTCXY1	O NC	O Y14	O Y15	О У16
L	O Y45	O Y44	O Y43	GND	O SDA		GPIO2	DBG_CLK XTAL_XIN	O PTCXY2	O NC	O Y17	О У18	О _{Y19}
м	O Y48	O Y47	О Y46	GND	O SCL	ADDSEL USBDP	GPI01	GPI05	О	GND	О У20	O Y21	O Y22
N	O Y51	O Y50	О Ү49	AVDD		O VDD		VDDIO	O PTCXY4	AVDD	O Y23	O Y24	⊖ ∀25

Top View

0.2 162-ball UFBGA

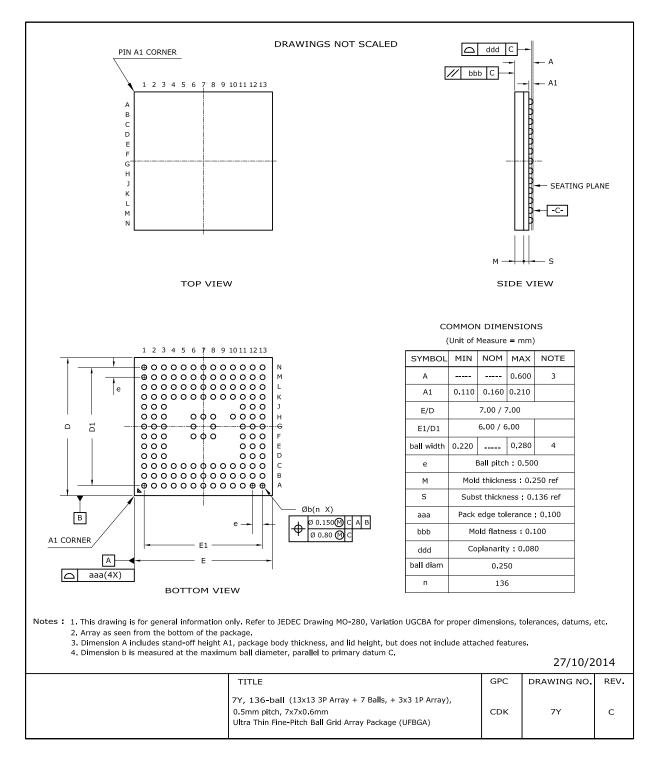
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۲		O sda						GPIO5	DBG_DATA XTAL_XOUT
2			COMMSEL				GPI04		
3									
4		⊖ ⁹	O ^y						C zs
5	O ₽	O ^y	O ^y ₂	O ₽		Y29	Q X	O ₹	Y 32
9	⊖ ^v	O ⁹ ⁄₂	⊖ ^o z	⊖ ^y		Y25	726 726	V27	728 7
7	O S	O S	O ^y	O g		\bigvee_{Y_2}	V 22	Y23	Y24
8		O ₽	O ^g	O ^g		→ [×]			
6		O ^g	O ^y	Y51		Č ž		۲۲ ۲۲	
10	Y50	Y49	\×48	Y47		O ≩	Y12	Č ž	۲ 41 4
11	\ \ \ \ \ 46	Υ ⁴⁵	\bigcirc $\frac{1}{4}$	\ \ \ \ \ \ \ \ \ \ \ \ \ \		⊃ ≿	[∞]		۲ ₀
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14			GND				GND		OND GND
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17	⊖ ^v	O ⁹ ⁄₂	○ ^{sc}	X25 V	X20 X	X16 X	○ ž	\bigcirc »	∑ ×
18	O º	⊖ ⁹	O ž	X26 V	$\bigcirc \frac{x}{2}$	○ ^t x	X12 X	O ☆	○ ×
19	O ^o	O ^g	⊖ ⁹	O ™	\bigcirc $\overset{x_2}{\sim}$	$\bigcirc \overset{\times}{\underset{\varpi}{\mathbb{Z}}}$	⊖ ^x €	$\bigcirc \ $	O ∞́

Top View

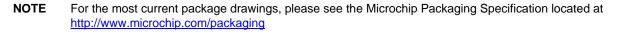
1.0 PACKAGING INFORMATION

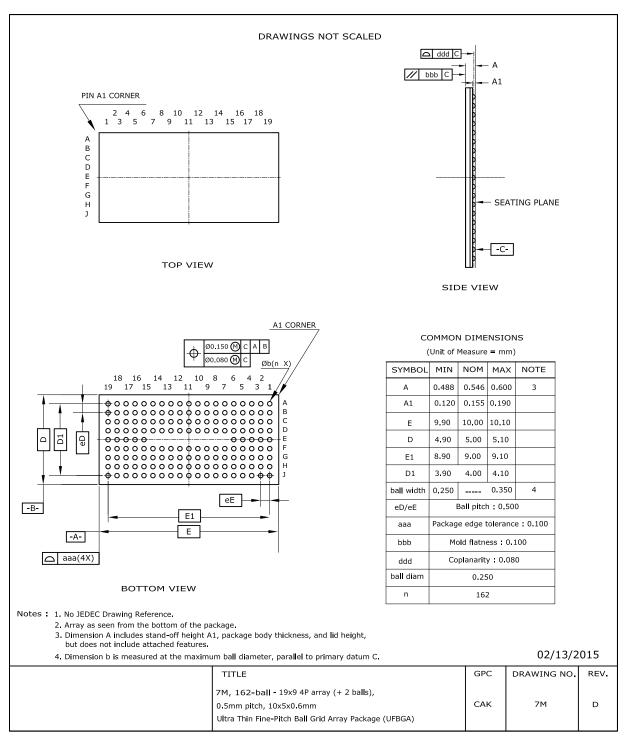
1.1 136-ball UFBGA 7 × 7 × 0.6 mm

NOTE For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging



1.2 162-ball UFBGA 10 × 5 × 0.6 mm





APPENDIX A: REVISION HISTORY

Revision A (August 2018)

Initial edition for firmware revision 2.0 - Release

PRODUCT IDENTIFICATION SYSTEM

The table below gives details on the product identification system for maXTouch devices. See "Orderable Part Numbers" below for example part numbers for the mXT1664T3.

To order or obtain information, for example on pricing or delivery, refer to the factory or the listed sales office.

	Device	Package	Temperat Range		Tape and Reel Option	Pattern			
Device:		Base d	evice name	9					
Package:		А	=	QFP (Plastic	Quad Flatpack))			
		CC	=	UFBGA (Ultra	a Thin Fine-pitc	h Ball Grid Array)			
		C2	=	UFBGA (Ultra	a Thin Fine-pitc	h Ball Grid Array)			
		NH	=	UFBGA (Ultra	a Thin Fine-pitc	h Ball Grid Array)			
		C4	=	X1FBGA (Ext	ra Thin Fine-pit	tch Ball Grid Array)			
		MA	=	XQFN (Super Thin Quad Flat No Lead Sawn)					
		MA5	=	XQFN (Super	[.] Thin Quad Fla	it No Lead Sawn)			
Temperature Range:		U	=	-40°C to +85°	°C (Grade 3)				
		Т	=	-40°C to +85°	°C (Grade 3)				
		В	=	-40°C to +10	5°C (Grade 2)				
Sample Type:		Blank	=	Release Sam	ple				
		ES	=	Pre-release (B	Engineering) Sa	ample			
Tape and Reel Option:		Blank	=	Standard Pac	kaging (Tube o	r Tray)			
		R	=	Tape and Ree	_} (1)				
Pattern:			QTP, Code Otherwise)	e or Special Red	quirements				

check with your Microchip Sales Office for package availability with the Tape and Reel option.

Orderable Part Numbers

Orderable Part Number	Firmware Revision	Description				
ATMXT1664T3-CCU035 (Supplied in trays)	2.0.AA	136-ball UFBGA 7 x 7 x 0.6 mm, RoHS compliant Industrial grade; not suitable for automotive characterization				
ATMXT1664T3-CCUR035 (Supplied in tape and reel)						
ATMXT1664T3-C2U035 (Supplied in trays)	2.0.AA	162-ball UFBGA 10 \times 5 \times 0.6 mm, RoHS compliant Industrial grade; not suitable for automotive characterization				
ATMXT1664T3-C2UR035 (Supplied in tape and reel)						

Atmel SL Code

An SL (QS) code was required on Atmel purchase orders, but is no longer used by Microchip. The SL code has been replaced by the 3-digit QTP code suffix on all Microchip industrial grade orderable part numbers.

The legacy Atmel SL (QS) code for mXT1664T3 2.0.AA is QS976.

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