

# PIC24FJ128GA310 Plug-in Module Manual

## Overview

The PIC24FJ128GA310 Plug-in Module is designed to demonstrate the capabilities of the PIC24FJ128GA310 family using the Explorer 16 Demonstration Board. Most of the pins from the device (PIC24FJ128GA310) are mapped directly to the PIM connector (100-Pin ICE). The exceptions are those pins that are remapped to provide remappable functionality to the pins in the PICtail™ Plus socket. The table below shows the exceptions of the PIC24FJ128GA310 pins that are not directly connected to the ICE connector.

## Pin Mapping Which Does Not Do 1-to-1 Mapping

Pin Function	Device Pin (PIC24FJ128GA310)	PIM Pin (100-Pin Pin U1A)
CN83/RF7	54	69
INT0/CN84/RF6	55	68
RP2/SEG13/RTCC/CN53/RD8	68	55
RP4/SEG14/PMACK2/CN53/RD9	69	54

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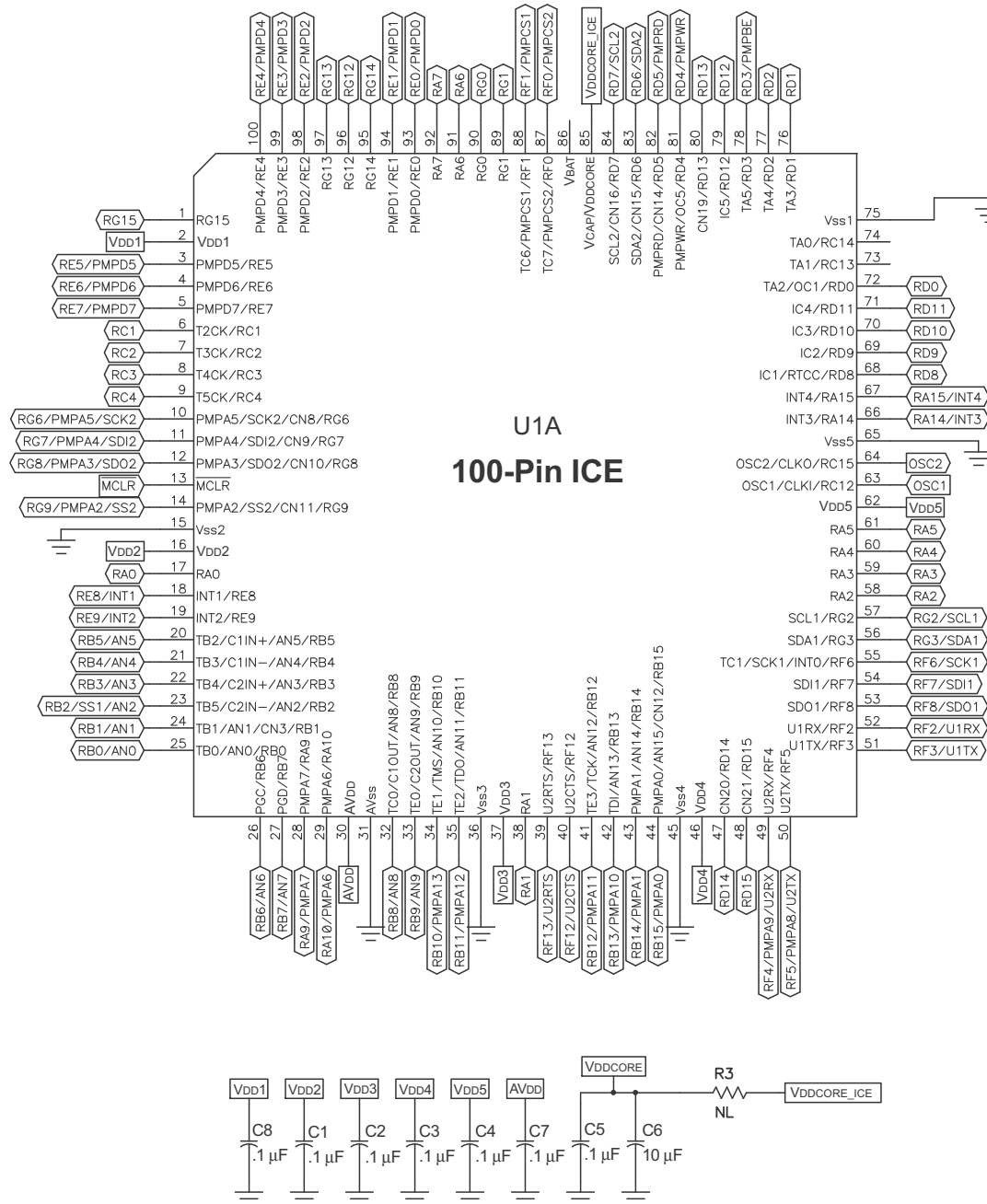
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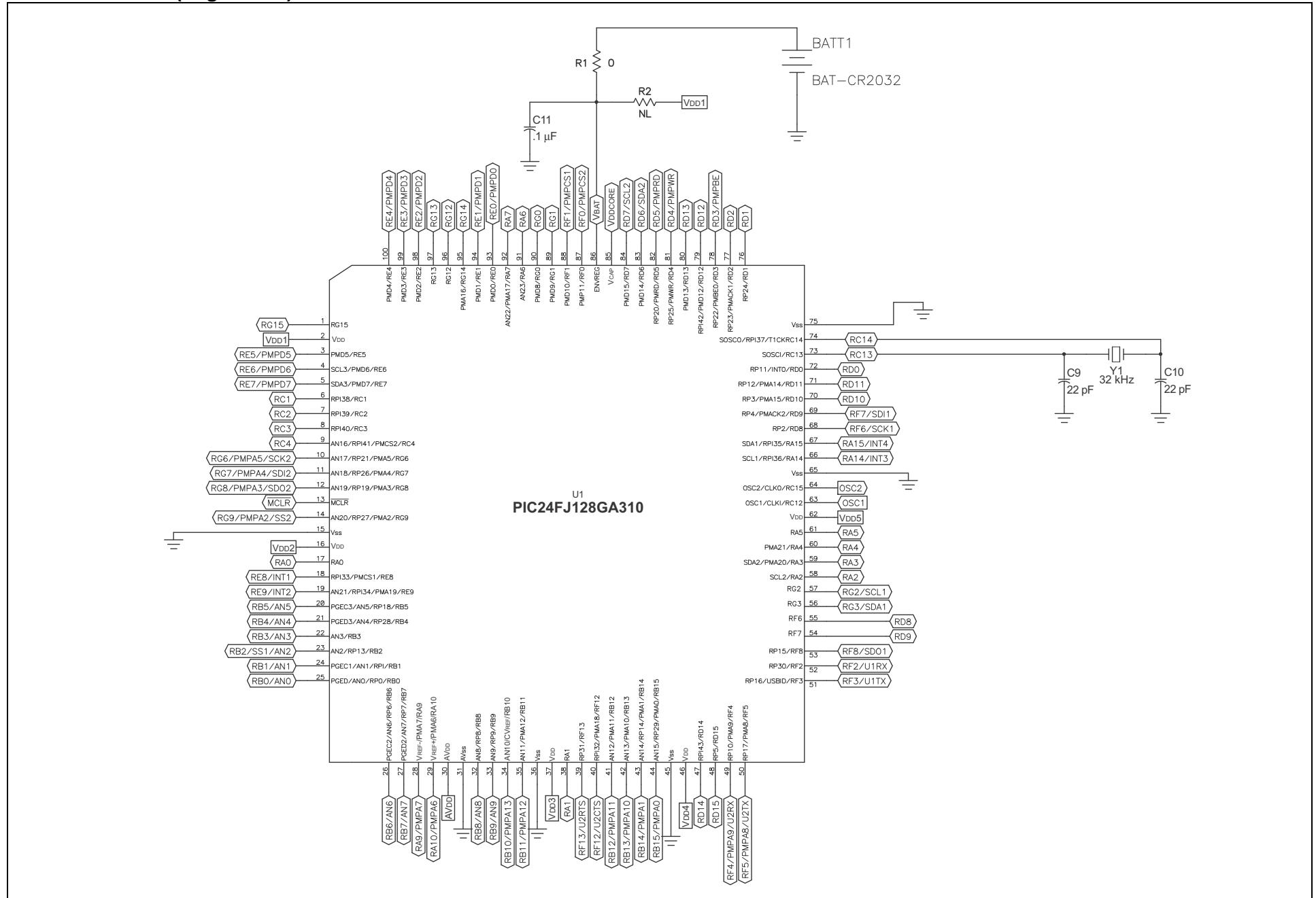
# Introducing the PIC24FJ128GA310 Plug-in Module

## Board Schematic (Page 1 of 2)



# Introducing the PIC24FJ128GA310 Plug-in Module

## Board Schematic (Page 2 of 2)



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