PIC32MX274F256D

PIC32MX274F256D USB/Graphics 44-pin to 100-pin TQFP Plug-In Module (PIM) (MA320021) Information Sheet

The PIC32MX274F256D PIM (MA320021) is designed to demonstrate the capabilities of the PIC32MX1XX/2XX family of devices using development boards, such as the Explorer 16 Development Board which supports 100-pin PIM interfaces.

The PIC32MX1XX/2XX family of devices are high-performance 32-bit microcontrollers in low-pin count packages. Table 1 provides the mapping between the development board functions and the device pins. Figure 1 and Figure 2 provides the 100-pin header and 44-pin device schematics.

44-PIN TO 100-PIN PINOUT

Device Pin #	PIC32MX274F256D	PIM Pin #	Functional Description
1	RPB9/SDA1/CTED4/PMD3/ RB9	99	PMPD3
2	RPC6/PMA1/RC6	43	ALH
3	RPC7/PMA0/RC7	44	ALL
4	RPC8/PMA5/RC8	68	YU
5	RPC9/CTED7/PMA6/RC9	69	XL
6	VSS	15	VSS
7	VCAP	85	VCAP
8	PGED2/RPB10/D+/CTED11/ RB10	90	D+
9	PGEC2/RPB11/D-/RB11	89	D-
10	VUSB	2	VDD
11	AN11/RPB13/CTPLS/PMRD/ RB13	N/C	N/A
12	TMS/PMA10/RA10	8	POWER/ PGED
13	CK/CTED8/PMA7/RA7	26	PGEC
14	CVREF/AN10/C3INB/RPB14/ VBUSON/SCK1/CTED5/RB14	7	RS
15	AN9/C3INA/RPB15/SCK2/ CTED6/PMCS1/RB15	70	CS
16	AVSS	36	AVSS
17	AVDD	30	AVDD
18	$\overline{\text{MCLR}}$	13	$\overline{\text{MCLR}}$
19	PGED3/VREF+/CVREF+/AN0/ C3INC/RPA0/CTED1/PMD7/ RA0	5	PMPD7
20	PGEC3/VREF-/CVREF-/AN1/ RPA1/CTED2/PMD6/RA1	4	PMPD6
21	PGED1/AN2/C1IND/C2INB/ C3IND/RPB0/PMD0/RB0	93	PMPD0
22	PGEC1/AN3/C1INC/C2INA/ RPB1/CTED12/PMD1/RB1	94	PMPD1

Device Pin #	PIC32MX274F256D	PIM Pin #	Functional Description
23	AN4/C1INB/C2IND/RPB2/ SDA2/CTED13/PMD2/CNB2/ RB2	98	PMPD2
24	AN5/C1INA/C2INC/RTCC/ RPB3/SCL2/PMWR/CNB3/ RB3	81	PMPWR
25	AN6/RPC0/RC0	34	YD
26	AN7/RPC1/RC1	35	XR
27	AN8/RPC2/PMA2/RC2	10	EEPROM_SCK
28	VDD	62	VDD
29	VSS	65	VSS
30	OSC1/CLKI/RPA2/RA2	63	OSCI
31	OSC2/CLKO/RPA3/RA3	64	OSCO
32	TDO/RPA8/PMA8/RA8	76	FLASH_CS1
33	SOSCI/RPB4/RB4	72	BUZZER
34	SOSCO/RPA4/T1CK/CTED9/ RA4	6	RST
35	TDI/RPA9/PMA9/RA9	77	WAIT
36	AN12/RPC3/RC3	78	BE
37	RPC4/PMA4/RC4	95	USBID/AN16
38	RPC5/PMA3/RC5	1	AN17/VBUS
39	VSS	31	VSS
40	VDD	2	VDD
41	PB5/USBID/RB5	95	USBID/AN16
42	VBUS	1	AN17/VBUS
43	RPB7/CTED3/PMD5/INT0/ RB7	3	PMPD5
44	RPB8/SCL1/CTED10/PMD4/ RB8	100	PMPD4

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FIGURE 1: 100-PIN HEADER SCHEMATIC

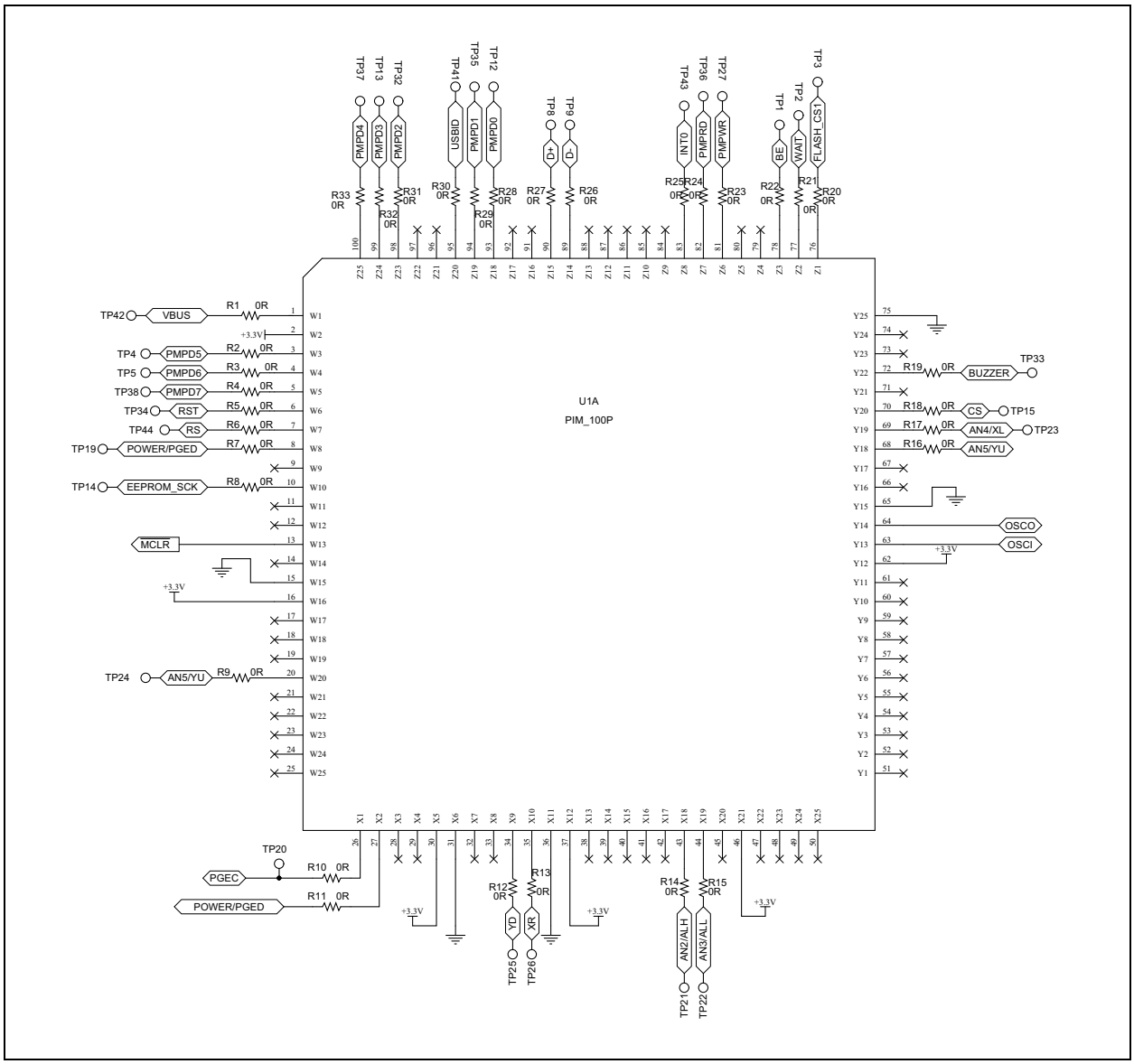

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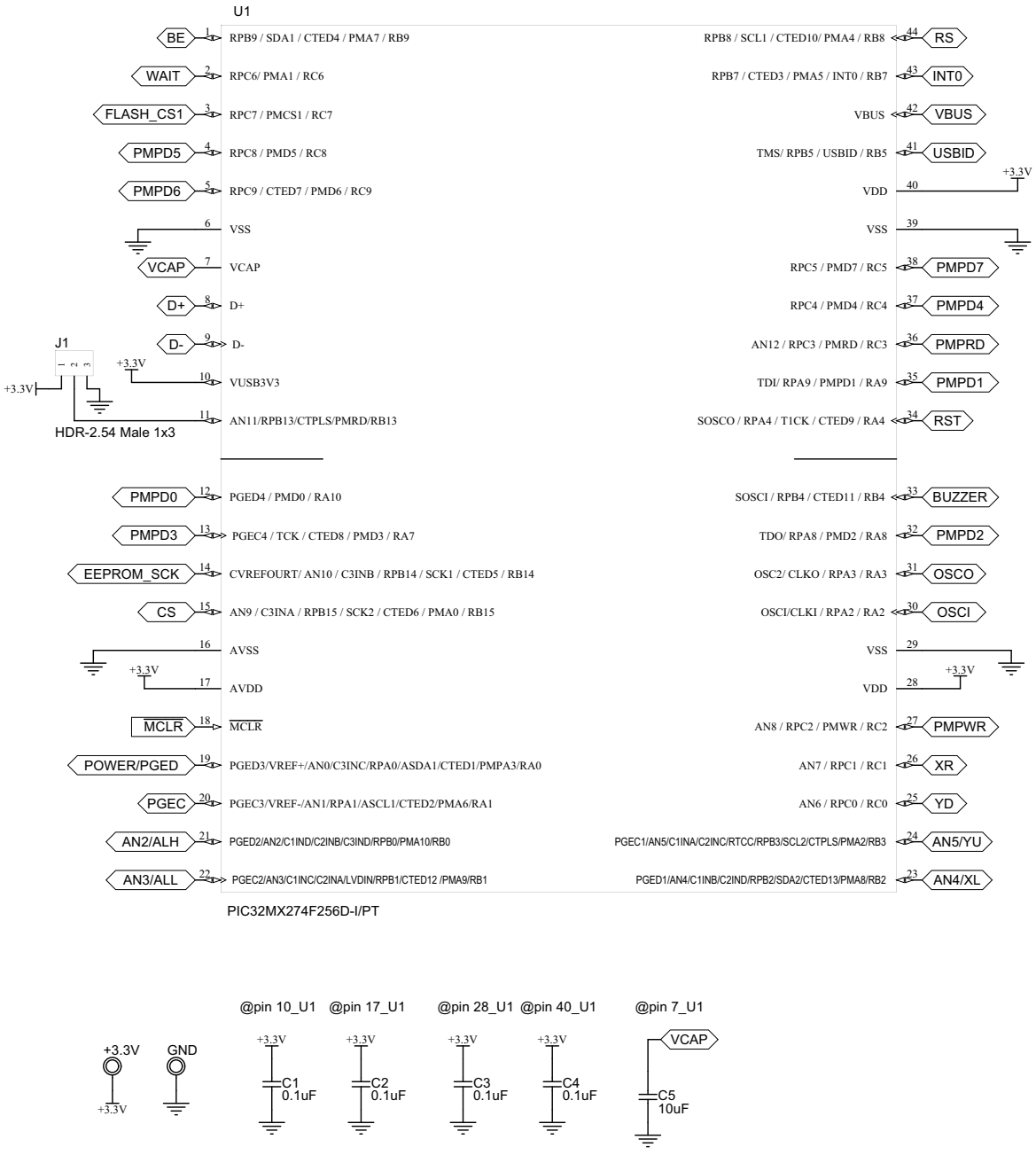
FIGURE 2: 44-PIN DEVICE SCHEMATIC



REVISION HISTORY

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