

High Side Power Switches

General Description

The MIC94064 and MIC94065 are high-side load switches designed for operation between 1.7V to 5.5V. The devices contain a low on-resistance P-channel MOSFET that supports over 2A of continuous current.

The MIC94064/5 provides a slew rate controlled Soft-Start turn-on of 115µs (typical) to prevent in-rush current from glitching supply rails.

The MIC94065 features an active load discharge circuit which insures capacitive loads retain no charge when the main switch is in an OFF state.

An active pull-down on the enable input keeps MIC94064/5 in a default OFF state until the EN pin is pulled to a high level. Built-in level shift circuitry allows low voltage logic signals to switch higher supply voltages, or vice versa; high level logic signals can control low level voltages.

The MIC94064/5's operating voltage range makes them suitable for 1-cell Lithium ion and 2- to 3-cell NiMH/NiCad/Alkaline powered systems, as well as all 5V applications. Their low operating current of 2μ A and low shutdown current of $<1\mu$ A maximize battery life.

Data sheets and support documentation can be found on Micrel's web site at: www.micrel.com.

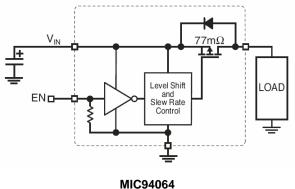
Features

- 1.7V to 5.5V input voltage range
- 2A continuous operating current
- 77mΩ (typ) R_{DSON}
- Built-in level shift for control logic; can be operated by 1.5V logic.
- Low 2µA quiescent current
- Soft-Start: 115µs
- Micro-power shutdown <1µA
- Load discharge circuit: MIC94065
- Space saving 1.2x1.6 mm Thin MLF[®] package

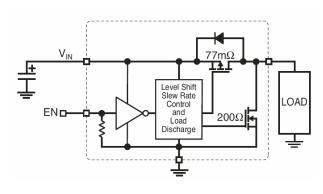
Applications

- Load switch in portable applications:
 - Cellular phones
 - PDAs
 - MP3 players
 - Digital Cameras
 - Portable instrumentation
- Battery switch-over circuits
- Level translator





Load Switch Application



MIC94065 Load Switch with Capacitive Load Discharge

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Ordering Information

Part Number		Part Marking ⁽¹⁾		Soft-Start		Paakaga	
Standard	Pb-Free	Standard	Pb-Free	Son-Start	Load Discharge	Package	
—	MIC94064YC6 ⁽¹⁾	—	64P	•		SC-70-6	
—	MIC94065YC6 ⁽¹⁾	—	6P5	•	•	30-70-0	
	MIC94064YMT	—	P64	•		1.2mm x 1.6mm	
—	MIC94065YMT	—	P65	•	•	Thin MLF [®]	

Notes

1. Underbar symbol on SC-70 Pb-free packages may not be to scale.

Pin Configuration



Pin Description

Pin Number Pin Name		Din Nomo	Pin Function	
SC-70	MLF	Fininallie		
1	1	V _{OUT}	Drain of P-channel MOSFET.	
2,5	2	GND	Ground and the backside pad (MLF only) should both be connected to electrical ground.	
4	3	V _{IN}	Source of P-channel MOSFET.	
3	4	EN	Enable (Input): Active-high CMOS compatible control input for switch A. Do not leave floating.	
6		NIC	No Internal Connection. A signal or voltage applied to this pin will have no effect on device operation.	

Absolute Maximum Ratings⁽¹⁾

Input Voltage (V _{IN})	+6V
Enable Voltage (V _{EN})	
Continuous Drain Current (I _D) ⁽³⁾	
T _A = 25°C	±2A
$T_{A} = 85^{\circ}C$	±1.4A
Pulsed Drain Current (I _{DP}) ⁽⁴⁾	±6A
Continuous Diode Current (I _S) ⁽⁴⁾	–50mA
Storage Temperature (T _s)–55°C to	+150°C
Storage Temperature (T _s) -55° C to EDS Rating – HBM ⁽⁶⁾	4KV

Operating Ratings⁽²⁾

Input Voltage (V _{IN})	
Junction Temperature (T _A)	–40°C to +125°C
Package Thermal Resistar	nce
SC-70-6 (θ _{JA})	
1.2x1.6 MLF [®] (θ _{JA})	
$1.2x1.6 \text{ MLF}^{(8)} (\theta_{JC})^{(3)}$	134°C/W

Electrical Characteristics

 V_{IN} = 5V; T_A = 25°C, bold values indicate –40°C \leq $T_A \leq$ +85°C, unless noted.

Symbol	Parameter	Condition	Min	Тур	Max	Units
$V_{\text{EN_TH}}$	Enable Threshold Voltage	V_{IN} = 1.8V to 4.5V, I_D = -250µA	0.5		1.2	V
		V_{IN} = 1.7V to 4.5V, I_D = -250µA	0.4		1.2	V
I _{EN}	Enable Input Current	$V_{IN} = V_{EN} = 5.5V$		2	4	μA
I _{VIN}	OFF State Leakage Current	V _{IN} = +5.5V, V _{EN} = 0V			1	μA
R _{DS(ON)}	P-Channel Drain to Source ON Resistance	V _{IN} = +4.5V, ID = -100mA, V _{EN} = 1.5V		77	110	mΩ
		V _{IN} = +3.6V, ID = -100mA, V _{EN} = 1.5V		85	115	mΩ
	SC-70 Package	V _{IN} = +2.5V, ID = -100mA, V _{EN} = 1.5V		100	140	mΩ
		V _{IN} = +1.8V, ID = -100mA, V _{EN} = 1.5V		145	200	mΩ
		V _{IN} = +1.7V, ID = -100mA, V _{EN} = 1.5V		155	215	mΩ
R _{DS(ON)}	P-Channel Drain to Source ON Resistance	V _{IN} = +4.5V, ID = -100mA, V _{EN} = 1.5V		85	115	mΩ
		V _{IN} = +3.6V, ID = -100mA, V _{EN} = 1.5V		100	140	mΩ
	MLF Package	V _{IN} = +2.5V, ID = -100mA, V _{EN} = 1.5V		145	200	mΩ
		V _{IN} = +1.8V, ID = -100mA, V _{EN} = 1.5V		155	215	mΩ
		V _{IN} = +1.7V, ID = -100mA, V _{EN} = 1.5V		165	225	mΩ
R _{SHUTDOWN}	Turn-Off Resistance	V_{IN} = +3.6V, I_{TEST} = 1mA, V_{EN} = 0V		200	300	Ω
		MIC94065				

Dynamic

Symbol	Parameter	Condition	Min	Тур	Max	Units
t _{ON_DLY}	Turn-On Delay Time	V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	80	125	170	μs
t _{ON_RISE}	Turn-On Rise Time	V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V	75	115	150	μs
t _{OFF_DLY}	Turn-Off Delay Time	V _{IN} = +3.6V, ID = -100mA, V _{EN} = 1.5V		100	200	ns
t _{OFF_FALL}	Turn-Off Fall Time	V_{IN} = +3.6V, ID = -100mA, V_{EN} = 1.5V		25	100	ns

Notes:

1. Exceeding the absolute maximum rating may damage the device.

2. The device is not guaranteed to function outside its operating rating.

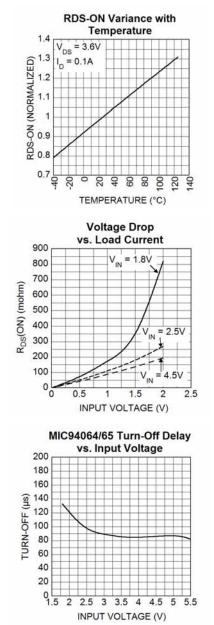
3. With backside thermal contact to PCB.

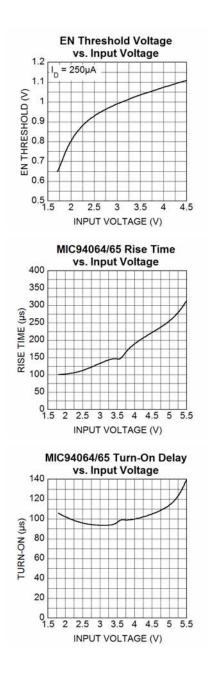
4. Pulse width $<300\mu$ s with < 2% duty cycle.

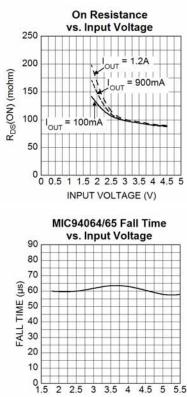
5. Continuous body diode current conduction (reverse conduction, i.e. V_{OUT} to V_{IN}) is not recommended.

6. Devices are ESD sensitive. Handling precautions recommended. HBM (Human body model), 1.5k in series with 100pF.

Typical Characteristics

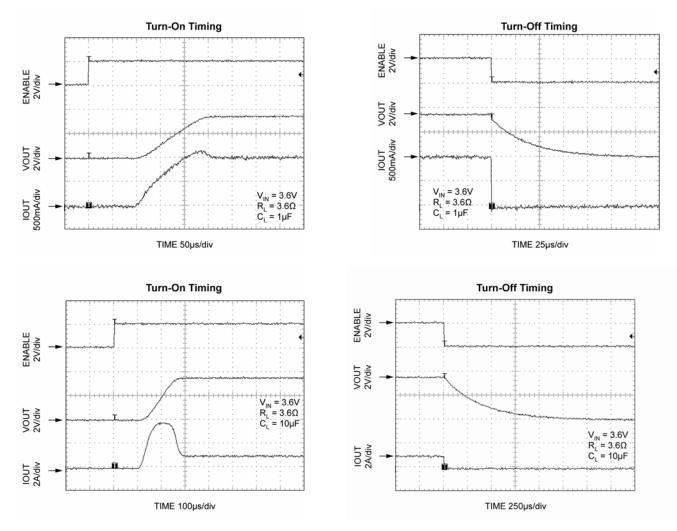




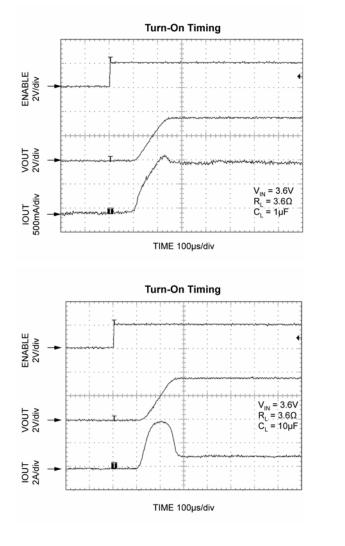


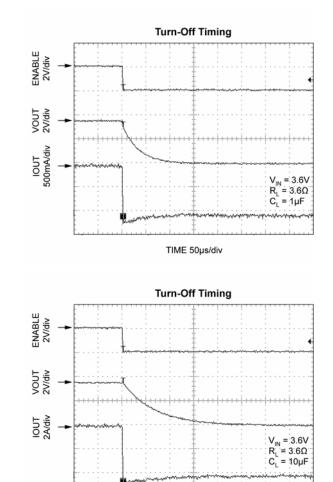
INPUT VOLTAGE (V)

Functional Characteristics MIC94064



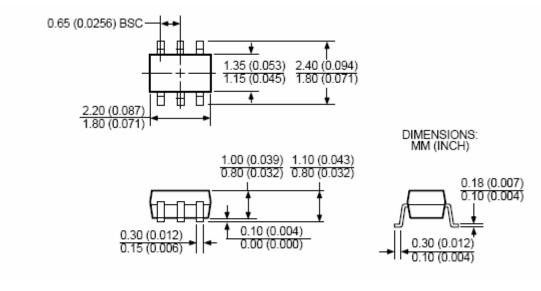
MIC94065



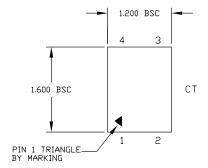


TIME 250µs/div

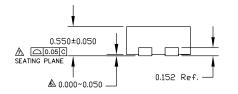
Package Information



6-Pin SC-70 (C6)







0.860±0.050 Exp.DAP ▲ 0.250±0.050 PIN #1 ID R0.100 3 4 0.500±0.050 Exp.DAP 0.350±0.050 2 1 -— 0.500 Bsc

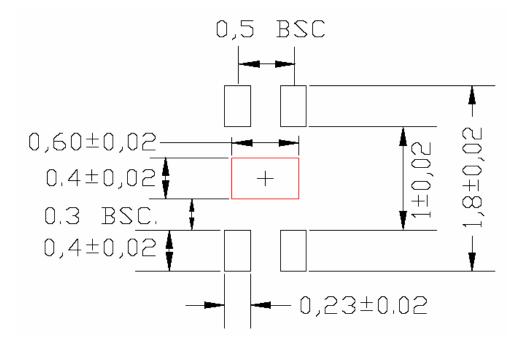
BOTTOM VIEW

- NDTE: 1. ALL DIMENSIONS ARE IN MILLIMETERS. 2. MAX. PACKAGE VARPAGE IS 0.05 mm. 3. MAXIMUM ALLOWABE BURRS IS 0.076 mm IN ALL DIRECTIONS. 4. PIN #1 ID ON TOP WILL BE LASER/INK MARKED. 5. DIMENSION APPLIES TO METALIZED TERMINAL AND IS MEASURED BETWEEN 0.20 AND 0.25 mm FROM TERMINAL AND IS MEASURED 5. APPLIED DNLY FOR TERMINALS.

SIDE VIEW

4-Pin Thin MLF[®] (MT)

Recommended Land Pattern for MLF[®] 1.2x1.6 4 Lead



Optional for maximum thermal performance, heatsink should be connected to GND plane of PCB for maximum thermal performance.

Disclaimer: This is only a recommendation based on information available to Micrel from its suppliers. Actual land pattern may have to be significantly different due to various materials and processes used in PCB assembly. Micrel makes no representation or warranty of performance based on the recommended land pattern."

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