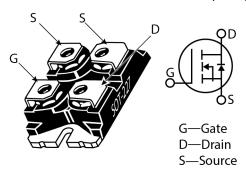
# **MSC080SMA120J**

## Silicon Carbide N-Channel Power MOSFET

#### **Product Overview**

The silicon carbide (SiC) power MOSFET product line from Microsemi increases the performance over silicon MOSFET and silicon IGBT solutions while lowering the total cost of ownership for high-voltage applications. The MSC080SMA120J device is a 1200 V, 80 m $\Omega$  SiC MOSFET in an SOT-227 package.



#### Features

The following are key features of the MSC080SMA120J device:

- · Low capacitances and low gate charge
- · Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, T<sub>J(max)</sub> = 175 °C
- · Fast and reliable body diode
- Superior avalanche ruggedness
- RoHS compliant
- · Isolated voltage to 2500 V

#### **Benefits**

The following are benefits of the MSC080SMA120J device:

- · High efficiency to enable lighter, more compact system
- · Simple to drive and easy to parallel
- · Improved thermal capabilities and lower switching losses
- · Eliminates the need for external freewheeling diode
- Lower system cost of ownership

#### **Applications**

The MSC080SMA120J device is designed for the following applications:

- · PV inverter, converter, and industrial motor drives
- · Smart grid transmission and distribution
- Induction heating and welding
- H/EV powertrain and EV charger
- Power supply and distribution

## 1. Device Specifications

This section shows the specifications of the MSC080SMA120J device.

### 1.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of the MSC080SMA120J device.

Table 1-1. Absolute Maximum Ratings

| Symbol           | Parameter   | Ratings   | Unit |
|------------------|---|-----------|------|
| V <sub>DSS</sub> | Drain source voltage                                | 1200      | V    |
| I <sub>D</sub>   | Continuous drain current at T <sub>C</sub> = 25 °C  | 31        | А    |
|                  | Continuous drain current at T <sub>C</sub> = 100 °C | 22        |      |
| I <sub>DM</sub>  | Pulsed drain current <sup>1</sup>                   | 77        |      |
| V <sub>GS</sub>  | Gate-source voltage                                 | 23 to -10 | V    |
| P <sub>D</sub>   | Total power dissipation at T <sub>C</sub> = 25 °C   | 143       | W    |
|                  | Linear derating factor                              | 0.95      | W/°C |

#### Note:

1. Repetitive rating: pulse width and case temperature limited by maximum junction temperature.

The following table shows the thermal and mechanical characteristics of the MSC080SMA120J device.

Table 1-2. Thermal and Mechanical Characteristics

| Symbol                 | Characteristic/Test Conditions   | Min  | Тур  | Max  | Unit   |
|------------------------|--|------|------|------|--------|
| $R_{\theta JC}$        | Junction-to-case thermal resistance  |      | 0.70 | 1.05 | °C/W   |
| T <sub>J</sub>         | Operating junction temperature   | -55  |      | 175  | °C     |
| T <sub>STG</sub>       | Storage temperature  | -55  |      | 150  |        |
| TL                     | Soldering temperature for 10 seconds (1.6 mm from case)                                    |      |      | 300  |        |
| V <sub>ISOLATION</sub> | RMS voltage (50 Hz–60 Hz sinusoidal waveform from terminals to mounting base for 1 minute) | 2500 |      |      | V      |
|                        | Mounting torque, M4 screw  |      |      | 10   | lbf-in |
|                        |  |      |      | 1.1  | N-m    |
| Wt                     | Package weight   |      | 1.03 |      | oz     |
|                        |  |      | 29.2 |      | g      |

### 1.2 Electrical Performance

The following table shows the static characteristics of the MSC080SMA120J device.  $T_J$  = 25 °C unless otherwise specified.

**Table 1-3. Static Characteristics** 

| Symbol                         | Characteristic                          | Test Conditions  | Min  | Тур  | Max  | Unit  |
|--------------------------------|---|--|------|------|------|-------|
| V <sub>(BR)DSS</sub>           | Drain-source<br>breakdown<br>voltage    | $V_{GS} = 0 \text{ V}, I_D = 100 \mu\text{A}$                            | 1200 |      |      | V     |
| R <sub>DS(on)</sub>            | Drain-source on resistance <sup>1</sup> | $V_{GS} = 20 \text{ V}, I_D = 15 \text{ A}$                              |      | 80   | 100  | mΩ    |
| V <sub>GS(th)</sub>            | Gate-source threshold voltage           | $V_{GS} = V_{DS}$ , $I_D = 1 \text{ mA}$                                 | 1.9  | 2.8  |      | V     |
| $\Delta V_{GS(th)}/\Delta T_J$ | Threshold voltage coefficient           | $V_{GS} = V_{DS}$ , $I_D = 1 \text{ mA}$                                 |      | -4.5 |      | mV/°C |
| I <sub>DSS</sub>               | Zero gate voltage drain current         | V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V                          |      |      | 100  | μΑ    |
|                                | urain current                           | V <sub>DS</sub> = 1200 V, T <sub>J</sub> = 125 °C, V <sub>GS</sub> = 0 V |      |      | 500  |       |
| I <sub>GSS</sub>               | Gate-source<br>leakage current          | V <sub>GS</sub> = 20 V/–10 V   |      |      | ±100 | nA    |

#### Note:

1. Pulse test: pulse width < 380  $\mu$ s, duty cycle < 2%.

The following table shows the dynamic characteristics of the MSC080SMA120J device.  $T_J$  = 25 °C unless otherwise specified.

**Table 1-4. Dynamic Characteristics** 

| Symbol           | Characteristic               | Test Conditions  | Min | Тур | Max | Unit |
|------------------|------------------------------|--|-----|-----|-----|------|
| C <sub>iss</sub> | Input capacitance            | V <sub>GS</sub> = 0 V, V <sub>DD</sub> = 1000 V              |     | 838 |     | pF   |
| C <sub>rss</sub> | Reverse transfer capacitance | $V_{AC} = 25 \text{ mV}, f = 1 \text{ MHz}$                  |     | 9   |     |      |
| C <sub>oss</sub> | Output capacitance           |  |     | 84  |     |      |
| $Q_g$            | Total gate charge            | $V_{GS} = -5 \text{ V}/20 \text{ V}, V_{DD} = 800 \text{ V}$ |     | 64  |     | nC   |
| $Q_{gs}$         | Gate-source charge           | I <sub>D</sub> = 15 A  |     | 12  |     |      |
| $Q_{gd}$         | Gate-drain charge            |  |     | 19  |     |      |

| continued           |                                |   |     |      |     |      |  |
|---------------------|--------------------------------|---|-----|------|-----|------|--|
| Symbol              | Characteristic                 | Test Conditions   | Min | Тур  | Max | Unit |  |
| t <sub>d(on)</sub>  | Turn-on delay time             | $V_{DD}$ = 850 V, $V_{GS}$ = -5 V/20 V<br>$I_{D}$ = 20 A, $R_{g(ext)}$ = 4 $\Omega$ |     | 14   |     | ns   |  |
| t <sub>r</sub>      | Voltage rise time              | Freewheeling diode =  |     | 14   |     |      |  |
| t <sub>d(off)</sub> | Turn-off delay time            | MSC080SMA120J ( $V_{GS} = -5V$ ) (reference Fig. 1-17)                              |     | 19   |     |      |  |
| t <sub>f</sub>      | Voltage fall time              |   |     | 13   |     |      |  |
| E <sub>on</sub>     | Turn-on switching energy       |   |     | 350  |     | μJ   |  |
| E <sub>off</sub>    | Turn-off switching energy      |   |     | 65   |     |      |  |
| ESR                 | Equivalent series resistance   | f = 1 MHz, 25 mV, drain short   |     | 1.9  |     | Ω    |  |
| SCWT                | Short circuit withstand time   | V <sub>DS</sub> = 960 V, V <sub>GS</sub> = 20 V                                     |     | 3    |     | μS   |  |
| E <sub>AS</sub>     | Avalanche energy, single pulse | V <sub>DS</sub> = 150 V, I <sub>D</sub> = 15 A                                      |     | 1000 |     | mJ   |  |

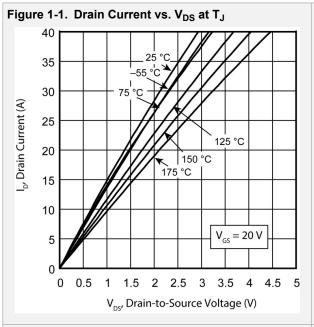
The following table shows the body diode characteristics of the MSC080SMA120J device.  $T_J$  = 25 °C unless otherwise specified.

Table 1-5. Body Diode Characteristics

| Symbol           | Parameter                | Test Conditions                                | Min | Тур | Max | Unit |
|------------------|--------------------------|--|-----|-----|-----|------|
| $V_{SD}$         | Diode forward voltage    | I <sub>SD</sub> = 15 A, V <sub>GS</sub> = 0 V  |     | 4.0 |     | V    |
|                  |                          | $I_{SD} = 15 \text{ A}, V_{GS} = -5 \text{ V}$ |     | 4.2 |     |      |
| t <sub>rr</sub>  | Reverse recovery time    | $I_{SD} = 15 \text{ A}, V_{GS} = -5 \text{ V}$ |     | 34  |     | ns   |
| Q <sub>rr</sub>  | Reverse recovery charge  | V <sub>DD</sub> = 800 V, dl/dt = -1000 A/μs    |     | 200 |     | nC   |
| I <sub>RRM</sub> | Reverse recovery current |  |     | 6.5 |     | А    |

## 1.3 Typical Performance Curves

This section shows the typical performance curves of the MSC080SMA120J device.



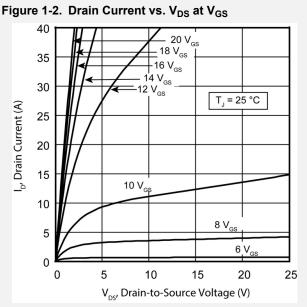


Figure 1-3. Drain Current vs.  $V_{DS}$  at  $V_{GS}$ 

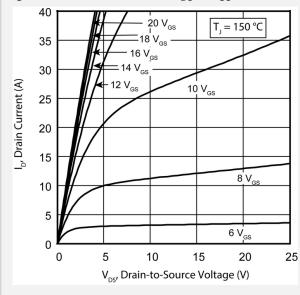
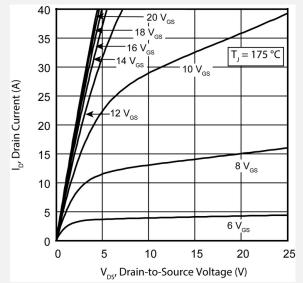
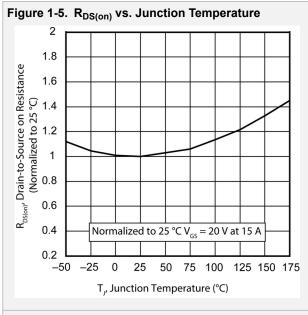


Figure 1-4. Drain Current vs.  $V_{DS}$  at  $V_{GS}$ 





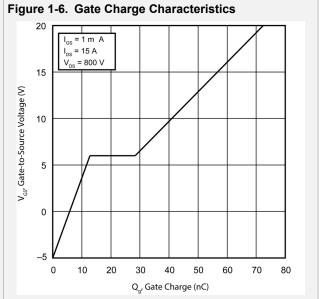
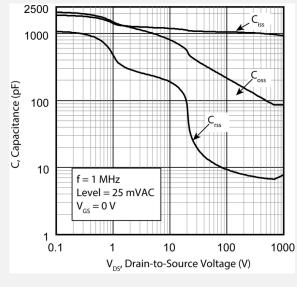
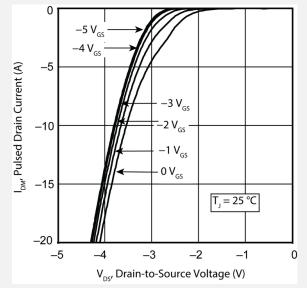
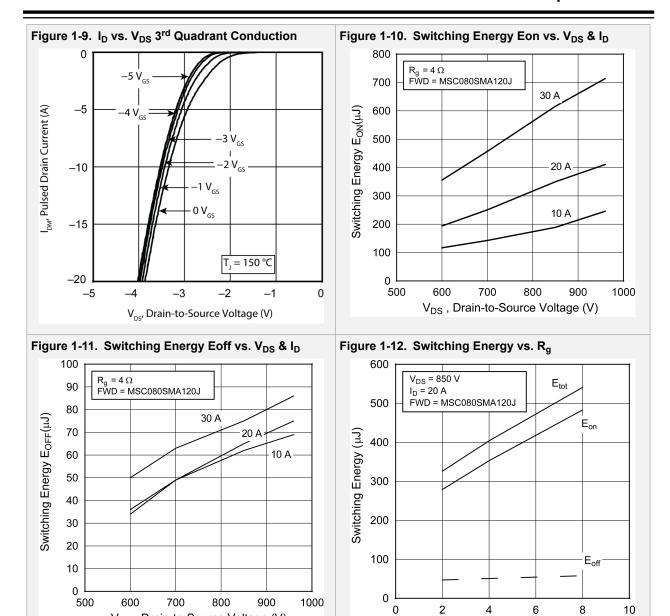


Figure 1-7. Capacitance vs. Drain-to-Source Voltage | Figure 1-8. I<sub>D</sub> vs. V<sub>DS</sub> 3<sup>rd</sup> Quadrant Conduction

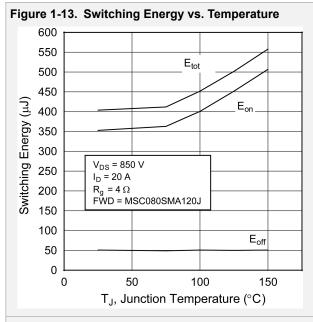






 $R_g(\Omega)$ 

 $V_{DS}$  , Drain-to-Source Voltage (V)



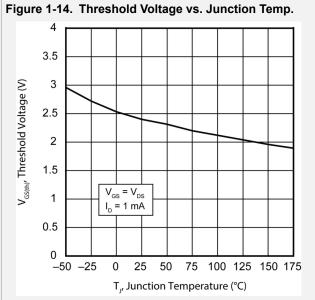
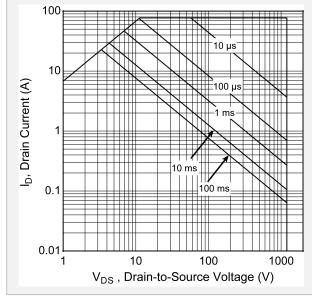
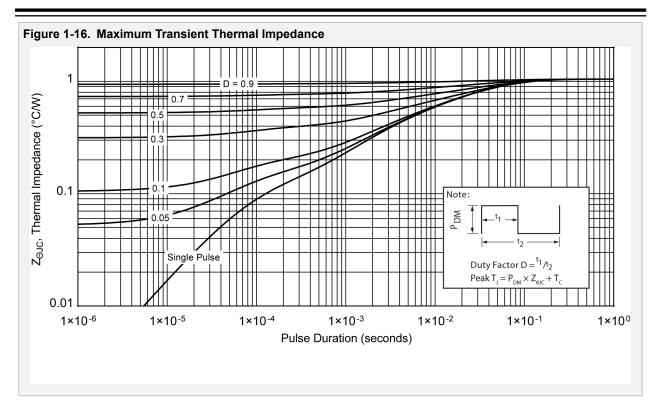


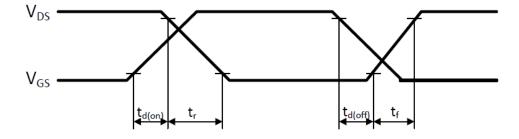
Figure 1-15. Forward Safe Operating Area





The following figure shows the switching waveform diagram of the MSC080SMA120J device.

Figure 1-17. Switching Waveform



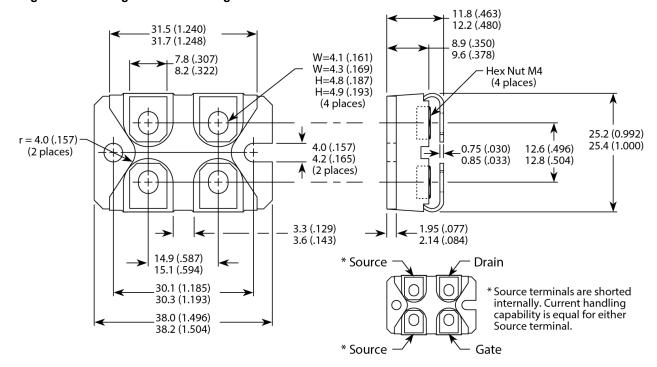
## 2. Package Specification

This section shows the package specification of the MSC080SMA120J device.

### 2.1 Package Outline Drawing

The following figure illustrates the SOT-227 package outline of the MSC080SMA120J device. The dimensions in the figure below are in millimeters and (inches).

Figure 2-1. Package Outline Drawing



# 3. Revision History

Table 3-1. Revision History

| Revision                               | Date    | Description  |
|--|---------|--|
| A                                      | 06/2021 | Document migrated from Microsemi template to Microchip template; Assigned Microchip literature number DS-00004138A,which replaces the previous Microsemi literature number 050-7767. |
| Initial release (Microsemi Revision A) | 02/2020 | Document created.  |

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