

REVISIONS		
#	DESCRIPTION	DATE

NOTES

Allow clearance for finger squeeze access from the two sides shown.

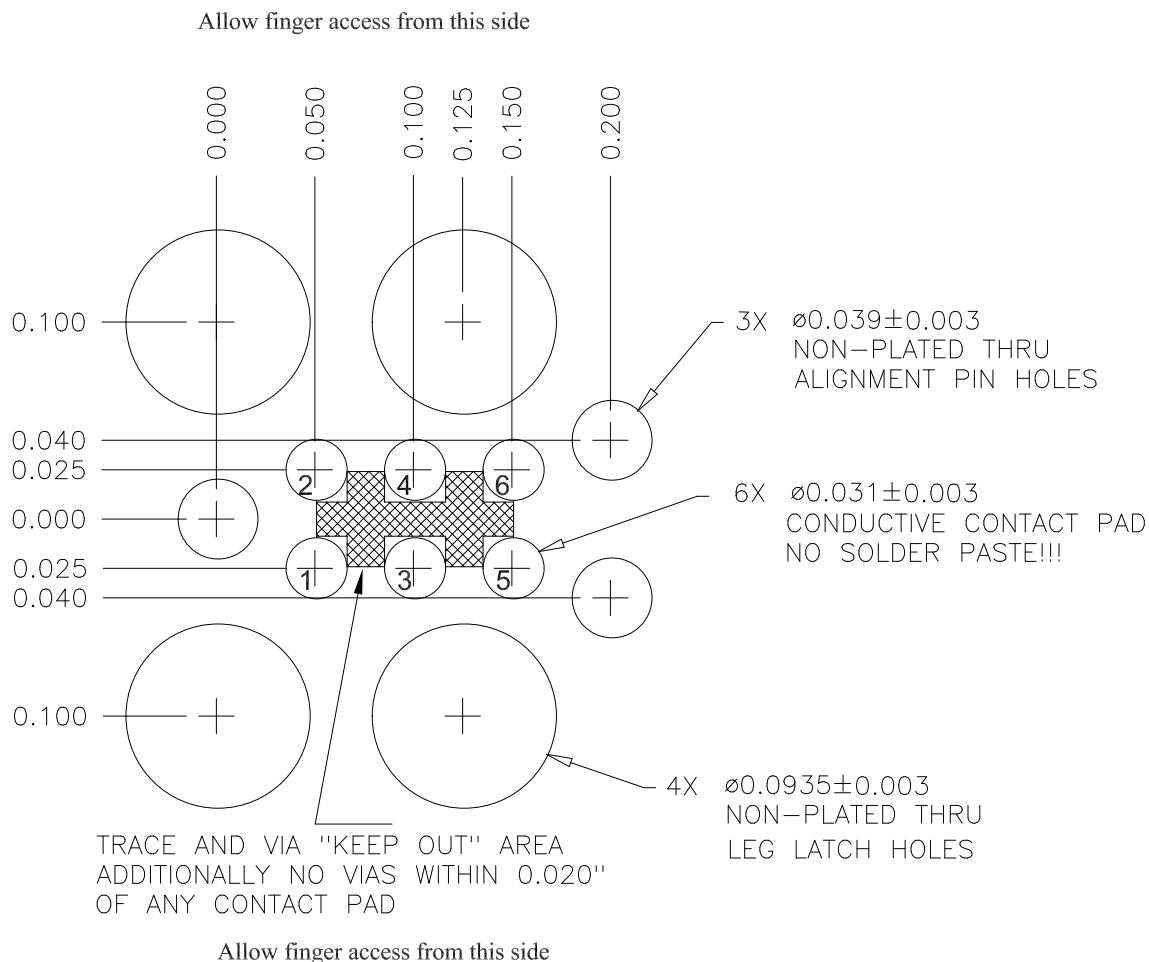
No tracks or vias in the shaded keepout area.

No other track or signal within 0.020" of any contact pad.

Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with finished hole size of 0.008" or less.

Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.

To prevent purchasing confusion, please specify DNL in your BOM!



Microchip ICD / ICSP SIGNALS

Pin 1: -MCLR/Vpp

Pin 2: Vdd

Pin 3: Ground

Pin 4: PGD (ICSPDAT)

Pin 5: PGC (ICSPCLK)

Pin 6: Unused or LVP

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TITLE TC2030-MCP-FP
FOOTPRINT FOR TC2030-MCP

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	.XXX±	.XX±	ANGLE ±	DRAWN	DATE
	.005	.01	1.5°	PM / NS	4/5/10
	THIRD ANGLE PROJECTION			CHECKED	DATE

SIZE	SHEET	DWG. NO.	REV.
A	1 OF 1		D

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