



SMSC[®]
SUCCESS BY DESIGN

TMC2074

Dual Mode CircLink™ Controller

PRODUCT FEATURES

Data Brief

- Low Power CMOS, 3.3 Volt Power Supply with 5 Volt Tolerant I/O
- Supports 8/16-Bit Data Bus
 - Both 86xx and 68hxx Platforms
- 1K On-chip Dual Port Buffer Memory
 - Sequential I/O Mapped Access
- Enhanced Token Passing Protocol from ARCNET
 - Maximum 31 Nodes per Network
 - Token Retry Mechanism
 - Maximum 256 Bytes per Packet
 - Consecutive Node ID Assignment
- Memory Mirror
 - Shared Memory within Network
- Network Standard Time
 - Network Time Synchronization
 - Automatic Time Stamping
- Coded Mark Inversion
 - Intelligent 1-Bit Error Correction
 - Magnetic Saturation Prevention
- Dual Operation Modes
 - Peripheral (Host) Mode Operates with MCU
 - Standalone (I/O) Mode Operates without MCU
- Supports 8 Bit Programmable General Purpose I/O at peripheral Mode
- Supports 16 Bit Input and 16 Bit Output at Standalone Mode
- Dual Communication Modes (with Peripheral Mode)
 - Free Format Mode
 - Remote Buffer Mode
- 3 Port Hub Integrated
 - 1 Internal and 2 External
- Flexible Topologies
 - Bus, Star and Tree
- Low Cost Media can be Used
 - RS485 Differential Driver
- Fiber Optics and Twisted Pair Cable Supported
- 128-Pin, VTQFP Lead-Free RoHS Compliant Package
- Temperature Range from 0 to 70 Degrees C

ORDER NUMBER:**TMC2074-NU FOR 128 PIN, VTQFP LEAD-FREE ROHS COMPLIANT PACKAGE**

80 ARKAY DRIVE, HAUPPAUGE, NY 11788 (631) 435-6000, FAX (631) 273-3123

Copyright © 2007 SMSC or its subsidiaries. All rights reserved.

Circuit diagrams and other information relating to SMSC products are included as a means of illustrating typical applications. Consequently, complete information sufficient for construction purposes is not necessarily given. Although the information has been checked and is believed to be accurate, no responsibility is assumed for inaccuracies. SMSC reserves the right to make changes to specifications and product descriptions at any time without notice. Contact your local SMSC sales office to obtain the latest specifications before placing your product order. The provision of this information does not convey to the purchaser of the described semiconductor devices any licenses under any patent rights or other intellectual property rights of SMSC or others. All sales are expressly conditional on your agreement to the terms and conditions of the most recently dated version of SMSC's standard Terms of Sale Agreement dated before the date of your order (the "Terms of Sale Agreement"). The product may contain design defects or errors known as anomalies which may cause the product's functions to deviate from published specifications. Anomaly sheets are available upon request. SMSC products are not designed, intended, authorized or warranted for use in any life support or other application where product failure could cause or contribute to personal injury or severe property damage. Any and all such uses without prior written approval of an Officer of SMSC and further testing and/or modification will be fully at the risk of the customer. Copies of this document or other SMSC literature, as well as the Terms of Sale Agreement, may be obtained by visiting SMSC's website at <http://www.smisc.com>. SMSC is a registered trademark of Standard Microsystems Corporation ("SMSC"). Product names and company names are the trademarks of their respective holders.

SMSC DISCLAIMS AND EXCLUDES ANY AND ALL WARRANTIES, INCLUDING WITHOUT LIMITATION ANY AND ALL IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, TITLE, AND AGAINST INFRINGEMENT AND THE LIKE, AND ANY AND ALL WARRANTIES ARISING FROM ANY COURSE OF DEALING OR USAGE OF TRADE. IN NO EVENT SHALL SMSC BE LIABLE FOR ANY DIRECT, INCIDENTAL, INDIRECT, SPECIAL, PUNITIVE, OR CONSEQUENTIAL DAMAGES; OR FOR LOST DATA, PROFITS, SAVINGS OR REVENUES OF ANY KIND; REGARDLESS OF THE FORM OF ACTION, WHETHER BASED ON CONTRACT; TORT; NEGLIGENCE OF SMSC OR OTHERS; STRICT LIABILITY; BREACH OF WARRANTY; OR OTHERWISE; WHETHER OR NOT ANY REMEDY OF BUYER IS HELD TO HAVE FAILED OF ITS ESSENTIAL PURPOSE, AND WHETHER OR NOT SMSC HAS BEEN ADVISED OF THE POSSIBILITY OF SUCH DAMAGES.

General Description

About CircLink

The CircLink networking controller was developed for small control-oriented local network data communication based on ARCNET's token-passing protocol that guarantees message integrity and calculatable maximum cycle time.

In a CircLink network, when a node receives the token it becomes the temporary master of the network for a fixed, short period of time. No node can dominate the network since token control must be relinquished when transmission is complete. Once a transmission is completed the token is passed on to the next node (logical neighbor), allowing it to become the master.

Because of this token passing scheme, maximum waiting time for network access can be calculated and the time performance of the network is predictable or deterministic. Control networking applications require predictable performance to ensure that controlled events occur when required. However, reconfiguration of a regular ARCNET network becomes necessary when the token is missed due to electronic and magnetic noise. In these cases, the maximum wait time for sending datagrams cannot be guaranteed and the real-time characteristic is impaired. CircLink makes several modifications to the original ARCNET protocol (such as maximum and consecutive node ID assignment) to avoid token missing as much as possible and reduce the network reconfiguration time.

CircLink implements other enhancements to the ARCNET protocol including a smaller-sized network, shorter packet size, and remote buffer mode operation that enable more efficient and reliable small, control-oriented LANs. In addition, CircLink introduces several unique features for reducing overall system cost while increasing system reliability.

CircLink can operate under a special mode called "Standalone" or "I/O" mode. In this mode, CircLink does not need an administrating CPU for each node. Only one CPU is needed to manage a CircLink network composed up to maximum 31 nodes, reducing cost and complexity.

In a CircLink network, the data sent by the source node is received by all other nodes in the network and stored according to node source ID. For the target node the received data is executed per ARCNET flow control and the data is stored in its buffer RAM. The receiving node processes the data while the remaining nodes on the network discard the data when the receiving node has completed. This memory-mirroring function assures higher reliability and significantly reduces network traffic.

Network Standard Time (NST) is also a unique CircLink feature. NST is realized by synchronizing the individual local time on each network node to the clock master in the designated node from which the packet is sent. CircLink also uses CMI code for transmitting signals, rather than the dipulse or bipolar signals that are the standard ARCNET signals. Since CMI encoding eliminates the DC element, a simple combination of a standard RS485 IC and a pulse transformer can be used to implement a transformer-coupled network.

About TMC2074

The TMC2074 network controller is CirLink technology's flagship product. The TMC2074's flexibility and rich feature set enable a high-reliability and high-performance, real-time and control-oriented network without the cumbersome middle layer protocol stacks and complex packet prioritization schemes typically required.

TMC2074 operates at network data transfer rates up to 5 Mbps. Its embedded 1 kByte RAM can be configured into a maximum of 32 pages to implement a 31-node network where each node in the network has the same local memory.

The TMC2074 has two operational modes: "Peripheral Mode" and "Standalone Mode". It can operate with or without the existence of a system CPU on a network node. In Peripheral Mode, the TMC2074 has two selectable communication modes, "Free Format Mode" and "Remote Buffer Mode". Free Format mode, retained from ARCNET, is "packet oriented" communication. Remote Buffer mode communication is a CirLink-specific feature, and is a token oriented communication, which includes automatic data transmission when the token arrives.

The TMC2074 has a flexible 8-bit or 16-bit databus to interface various CPU types including X86, 68XX, and SHX with multiplexed or non-multiplexed address/data. When operating in Peripheral mode, the TMC2074 has 8-bit programmable I/O available. When operating in Standalone mode, the TMC2074's I/O configuration is 16-bit. The TMC2074 also integrates a 3-port hub (two ports for external connection) to accommodate various network topologies (Bus, Star, etc.) and combinations.

Block Diagram

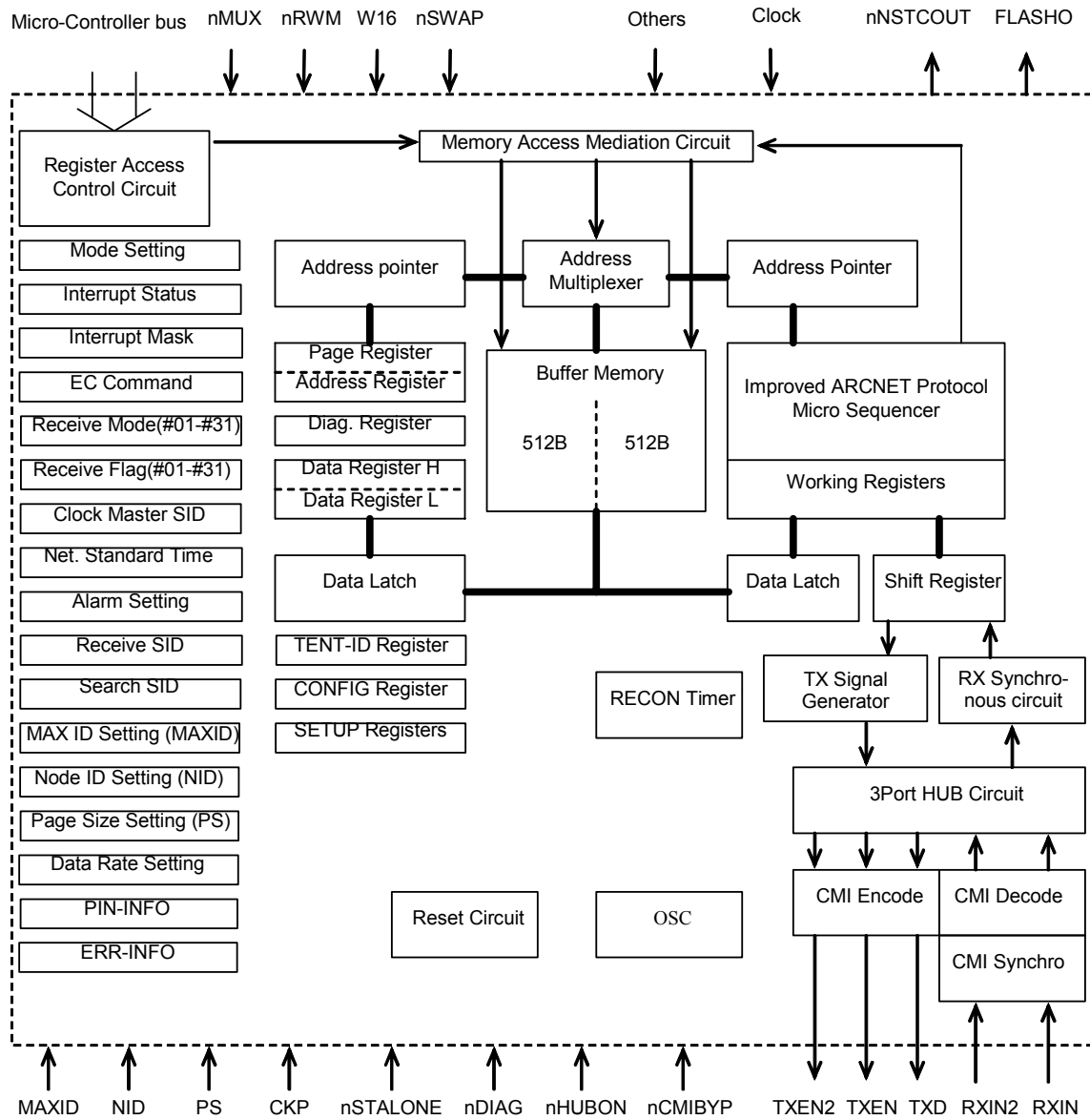


Figure 1 TMC2074 Internal Block Diagram

Package Outline

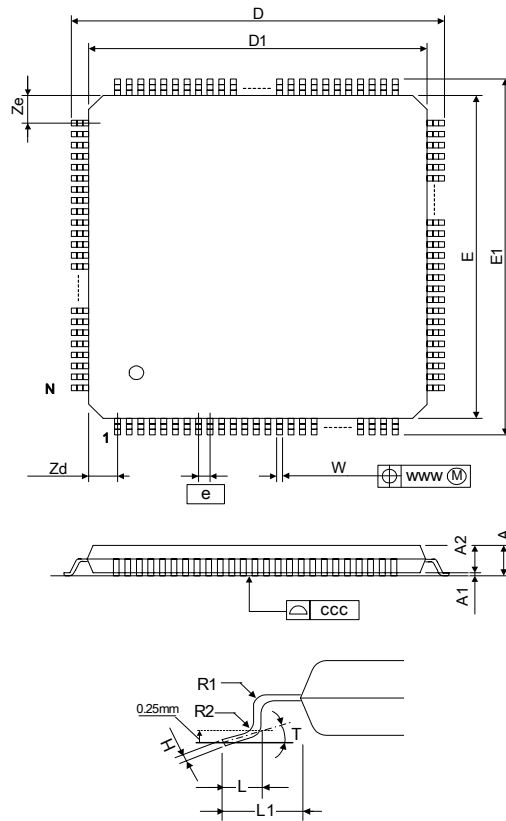


Figure 2 TMC2074 128-Pin VTQFP Package Diagram

Table 1 TMC2074 128-Pin VTQFP Package Parameters

SYMBOL	ITEMS	MIN	TYP	MAX
A	Overall Package Height	-	-	1.2
A1	Standoff	0.05	-	0.15
A2	Body Thickness	0.95	-	1.05
D	X Span	15.8	-	16.2
D1	X body Size	13.8	-	14.2
E	Y Span	15.8	-	16.2
E1	Y body Size	13.8	-	14.2
H	Lead Frame Thickness	0.09	-	0.2
L	Lead Foot Length	0.45	0.6	0.75
L1	Lead Length	-	1.0	-
e	Lead Pitch	0.4 Basic		
T	Lead Foot Angle	0°	-	7°
W	Lead Width	0.13	0.18	0.23
www	Lead position Tolerance	-0.035	-	0.035
R1	Lead Shoulder Radius	0.08	-	-
R2	Lead Foot Radius	0.08	-	0.2
ccc	Coplanarity	-	-	0.08
N	Pin count	128		

Notes:

- Controlling Unit: millimeter.
- Package body dimensions D1 and E1 do not include the mold protrusion. Maximum mold protrusion is 0.25 mm.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [Network Controller & Processor ICs](#) category:

Click to view products by [Microchip](#) manufacturer:

Other Similar products are found below :

[COM20019I3V-HT](#) [MIC3003GFL](#) [Z8523L10VEG](#) [NCN49597MNG](#) [BCM63168UKFEBG](#) [TMC2074-NU](#) [WAV624A1MC S LN25](#)
[WAV654A1MC S LN23](#) [WAV614A1MC S LN24](#) [73M2901CE-IM/F](#) [COM20020I-DZD-TR](#) [COM20020I-DZD](#) [KSZ8692PBI](#) [KSZ9692PB](#)
[73M2901CE-IGV/F](#) [MPL360BT-I/Y8X](#) [COM20019I-DZD](#) [COM20020I3V-DZD-TR](#) [COM20022I-HT](#) [KSZ8695P](#) [LAN9360A-I/CQB-100](#)
[LAN9360A-I/CQBT-100](#) [MPL360B-I/SCB](#) [MIC3001GML-TR](#) [2751807](#) [NCN49599MNG](#) [TMC2072-MT](#) [ST7590](#) [73M2901CE-IGVR/F](#)
[Z8523316ASG](#) [Z8523010PEG](#) [Z8523008PSG](#) [Z8523020VSG](#) [Z8523016VEG](#) [Z8523010VSG](#) [Z8523010VEG](#) [Z8523008VSG](#)
[Z8523016VSG](#) [Z8523008VEG](#) [Z8523L16VSG](#) [AMIS49587C5872G](#) [COM20020I-HT](#) [CY8CPLC20-28PVXI](#) [KSZ8692XPB](#) [KSZ8695X](#)
[ST7580TR](#)