

### **Features**

- High Density Cell Design for Low R<sub>DS(ON)</sub>
- · Voltage Controlled Small Signal Switch
- · ESD Protected up to 2KV (HBM)
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- · Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

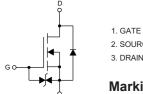
# **Maximum Ratings**

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 357°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V <sub>DS</sub>	60	V	
Gate-Source Voltage	$V_{GS}$	±20	V	
Drain Current-Continuous	I <sub>D</sub>	0.34	Α	
Power Dissipation	P <sub>D</sub>	0.35	W	

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

### **Internal Structure**



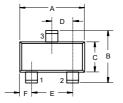
2. SOURCE

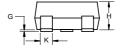
3. DRAIN

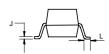
Marking:72K

# **N-Channel MOSFET**

## SOT-23

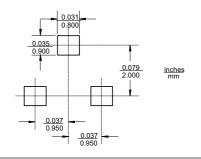






DIMENSIONS						
DIM	INC	HES	MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOIL	
Α	0.110	0.120	2.80	3.04		
В	0.083	0.104	2.10	2.64		
С	0.047	0.055	1.20	1.40		
D	0.034	0.041	0.85	1.05		
E	0.067	0.083	1.70	2.10		
F	0.018	0.024	0.45	0.60		
G	0.0004	0.006	0.01	0.15		
Н	0.035	0.043	0.90	1.10		
J	0.003	0.007	0.08	0.18		
K	0.012	0.020	0.30	0.51		
L	0.007	0.020	0.20	0.50		

#### **Suggested Solder Pad Layout**



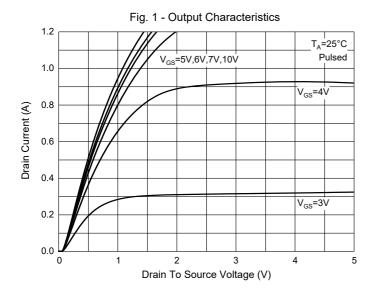


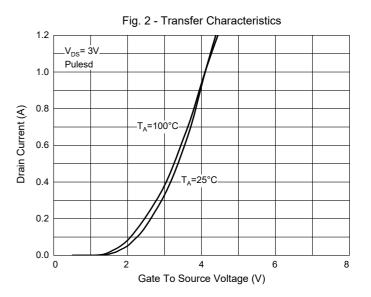
# ELECTRICAL CHARACTERISTICS (Ta=25 $^{\circ}$ C unless otherwise specified)

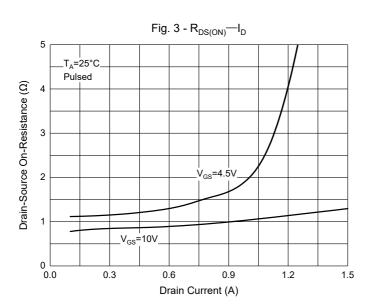
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> = <b>G</b> 0μA	60			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> = <b>G</b> ́ €µA	1.0	1.3	2.5	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V			1.0	μΑ	
		V <sub>DS</sub> =0V, V <sub>GS</sub> =±G0V			±10	μΑ	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±10V			±200	nA	
		V <sub>DS</sub> =0V, V <sub>GS</sub> =±5V			±100	nA	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =300mA			1.5	V	
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =500mA			2.5	Ω	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =200mA			3.0		
Recovered Charge	Q <sub>r</sub>	$V_{GS}$ =0V, $I_{S}$ =300mA, $V_{R}$ =25V, dl/dt=-100A/ $\mu$ s		30		nC	
Input Capacitance	C <sub>iss</sub>				40		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V, f=1MHz			30	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>				10		
Turn-On Time	t <sub>d(on)</sub>	$V_{DD}$ =50V,R <sub>L</sub> =250 $\Omega$ , R <sub>GS</sub> =50 $\Omega$ ,V <sub>GS</sub> =10V,			10		
Turn-Off Time	t <sub>d(off)</sub>	$R_{GS}$ -5002, $V_{GS}$ -10V, $R_{GEN}$ =50 $\Omega$			15	ns	
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS}$ =0V, $I_{S}$ =300mA, $V_{R}$ =25V, dl/dt=-100A/ $\mu$ s		30			

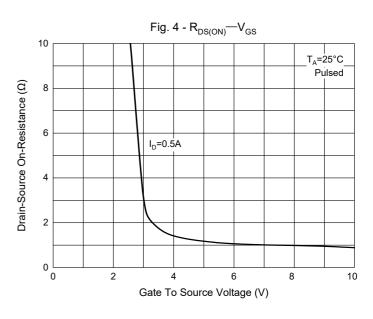


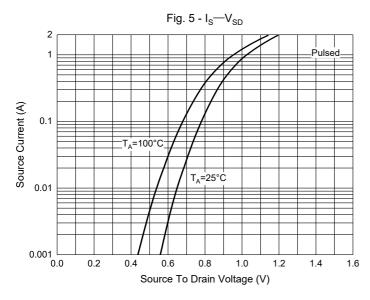
### **Curve Characteristics**

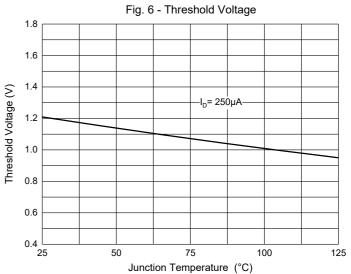






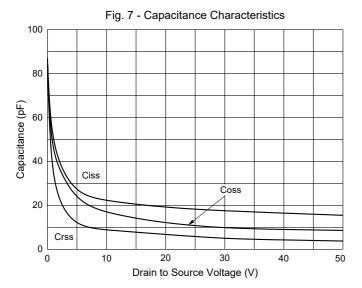








# **Curve Characteristics**





### **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel:3Kpcs/Reel	

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