

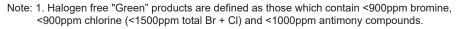
Features

- Low Gate Threshold Voltage
- · Low Input Capacitance
- Low On-Resistance
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

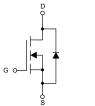
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 833°C/W Junction to Ambient

Param	Symbol	Rating	Unit	
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage	Continuous	V	±20	V
	Pulsed	V_{GS}	±40	V
Drain-Gate Voltage	R _{GS} ≤1.0MΩ	V_{DGR}	60V	V
Drain Current ⁽²⁾ Continuous			0.115	А
	Continuous@100°C	Ι _D	0.073	А
	Pulsed		0.80	А
Power Dissipation ⁽²⁾		P _D	0.15	W



2. Valid Provided That Terminals are Kept at Specified Ambient Temperature.

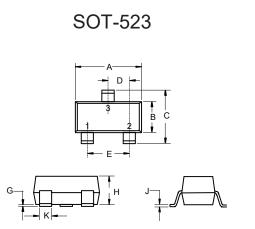
Internal Structure



2. SOURCE 3. DRAIN

1 GATE

Marking: K72

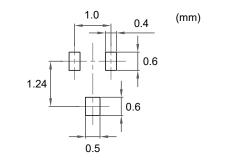


N-Channel

MOSFET

	DIMENSIONS				
DIM	INCHES		MM		NOTE
Divi	MIN	MAX	MIN	MAX	NOTE
Α	0.059	0.067	1.50	1.70	
В	0.030	0.033	0.75	0.85	
С	0.057	0.069	1.45	1.75	
D	0.020		0.50		TYP.
E	0.035	0.043	0.90	1.10	
G	0.000	0.004	0.00	0.10	
Н	0.024	0.031	0.60	0.80	
J	0.004	0.008	0.10	0.20	
K	0.006	0.014	0.15	0.35	

Suggested Solder Pad Layout





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

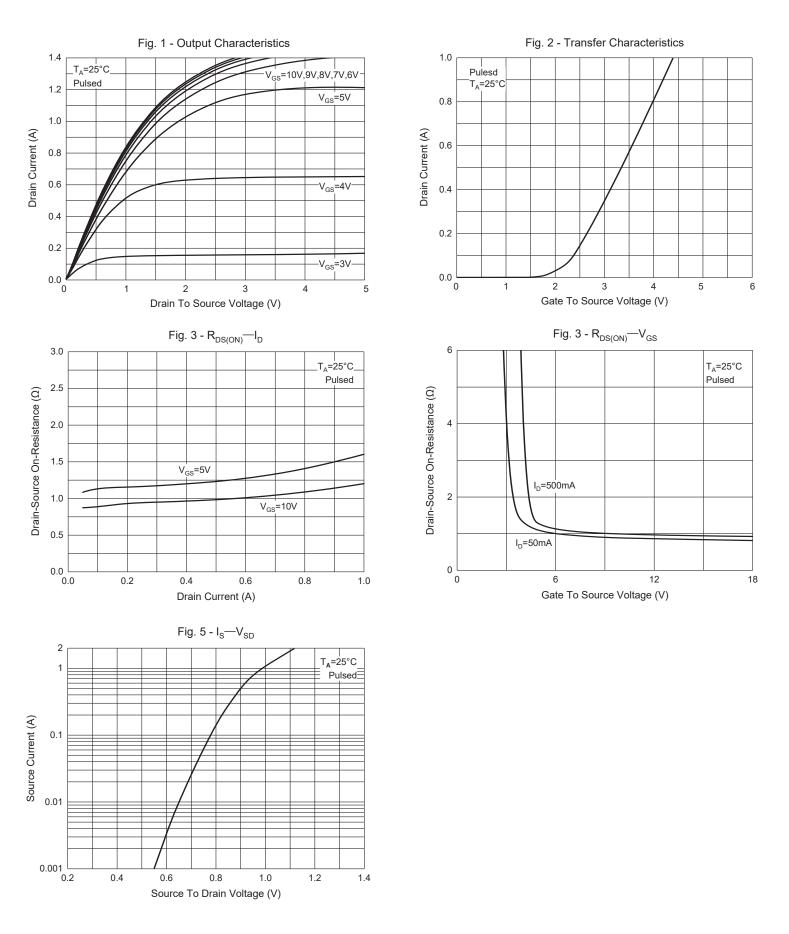
Parameter	Symbol	Test conditions	Min	Тур	Мах	Unit	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} =0V, I _D =10µA	60			V	
Gate-Threshold Voltage ⁽³⁾	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0		2.0	V	
Gate-Body Leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V,T _C =25°C			1.0	μA	
		V _{DS} =60V, V _{GS} =0V,T _C =125°C			500		
On-State Drain Current	I _{D(on)}	V _{DS} =7.5V, V _{GS} =10V	500	1000		mA	
Drain-Source On-Resistance ⁽³⁾	R _{DS(on)}	V _{GS} =10V, I _D =500mA		4.4	13.5	Ω	
		V_{GS} =5V, I _D =50mA		2.0	7.5		
Forward Transconductance	g _{fs}	V _{DS} =10V, I _D =200mA	80			ms	
Input Capacitance	C _{iss}			22	50		
Output Capacitance	C _{oss}	V_{DS} =25V, V_{GS} =0V, f=1MHz		11	25	pF	
Reverse Transfer Capacitance	C _{rss}			2	5		
Turn-On Time	t _{d(on)}	V_{DD} =30V, V_{GEN} =10V, R_{L} =150 Ω ,		7.0	20		
Turn-Off Time	t _{d(off)}	I_D =200mA,R _{GEN} =25 Ω		11	20	ns	

Note: 2. Valid Provided That Terminals are Kept at Specified Ambient Temperature.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.



Curve Characteristics





Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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