

#### Features

- High Density Cell Design for Low R<sub>DS(on)</sub>
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

## **Maximum Ratings**

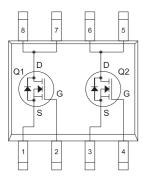
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 107°C/W Junction to Ambient<sup>(Note 1)</sup>

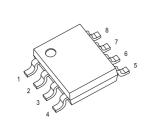
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Volltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D</sub>	8	Α
Pulsed Drain Current <sup>(Note 2)</sup>	I <sub>DM</sub>	40	Α
Total Power Dissipation	P <sub>D</sub>	1.4	W

Note:

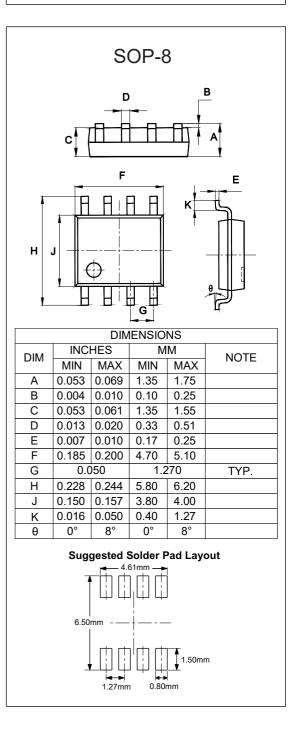
- 1. The Value of  $R_{\theta JA}$  is Measured with the Device Mounted on 1 in<sup>2</sup> FR-4 Board with 2oz. Copper, in a Still Air Environment with  $T_A$ =25°C.
- 2.Repetitive Rating; Pulse Width Limited by Maximum Junction Temperature.

## **Internal Structure**





# Dual N-Channel Power MOSFET





#### Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics					1	
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60			V
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V			1	μA
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1	1.7	3	V
Drain-Source On-Resistance	_	V <sub>GS</sub> =10V, I <sub>D</sub> =10A		16	19.5	mΩ
	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =6A		21	28	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-8A			1.2	V
Continuous Body Diode Current	I <sub>S</sub>				8	А
Dynamic Characteristics	·					
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V,V <sub>GS</sub> =0V,f=1MHz		864		pF
Output Capacitance	C <sub>oss</sub>			282		
Reverse Transfer Capacitance	C <sub>rss</sub>			27		
Total Gate Charge	Qg	V <sub>DS</sub> =30V,V <sub>GS</sub> =10V,I <sub>D</sub> =10A		17		
Gate-Source Charge	Q <sub>gs</sub>			3.1		nC
Gate-Drain Charge	Q <sub>gd</sub>			4.3		
Turn-On Delay Time	t <sub>d(on)</sub>			3.4		
Turn-On Rise Time	t <sub>r</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V,		5.2		- ns
Turn-Off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> =6Ω, I <sub>D</sub> =10A		13		
Turn-Off Fall Time	t <sub>f</sub>			7		
Reverse Recovery Time	t <sub>RR</sub>			22		ns
Reverse Recovery Charge	Q <sub>RR</sub>	I <sub>F</sub> =10A, di/dt=100A/μs		11		nC



## **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel: 4Kpcs/Reel	

Note : Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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