



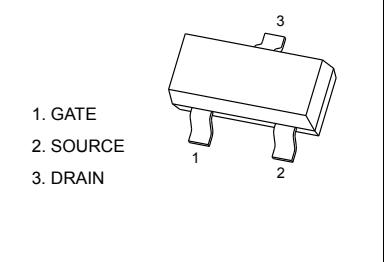
2N7002K

SOT-23 Plastic-Encapsulate MOSFETS

60V N-Channel Enhancement Mode MOSFET

$V_{(BR)DSS}$	$R_{DS(on)}\text{Typ}$	$I_D \text{ MAX}$
60V	0.9Ω@10V	500mA
	1.1Ω@4.5V	

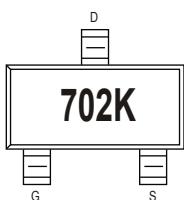
SOT-23



FEATURE

- High density cell design for low $R_{DS(ON)}$
- Voltage controlled small signal switch
- Rugged and reliable
- High saturation current capability
- HBM ESD protected (2000V)

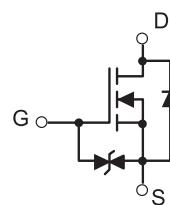
MARKING



APPLICATION

- Load Switch for Portable Devices
- DC/DC Converter

Equivalent circuit



PACKAGE SPECIFICATIONS

Package	Reel Size	Reel DIA. (mm)	Q'TY/Reel (pcs)	Box Size (mm)	QTY/Box (pcs)	Carton Size (mm)	Q'TY/Carton (pcs)
SOT-23	7'	330	3000	203×203×195	45000	438×438×220	180000

MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	0.5	A
Power Dissipation	P_D	0.3	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	400	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{stg}	-50 ~+150	



2N7002K

MOSFET ELECTRICAL CHARACTERISTICS

$T_a=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Condition	Min	Typ	Max	Unit
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current($T_A=25^\circ\text{C}$)	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_A=125^\circ\text{C}$)	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	--	--	± 10	μA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_D=250\mu\text{A}$	1.0	1.6	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=10\text{V}$, $I_D=0.5\text{A}$	--	0.9	2	Ω
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance②	$V_{\text{GS}}=4.5\text{V}$, $I_D=0.3\text{A}$	--	1.4	3	Ω

Dynamic Electrical Characteristics

C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	--	23.8	--	pF
C_{oss}	Output Capacitance		--	3.9	--	pF
C_{rss}	Reverse Transfer Capacitance		--	1.5	--	pF
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}$ $I_D=0.5\text{A}$, $V_{\text{GS}}=10\text{V}$	--	0.93	--	nC
Q_{gs}	Gate Source Charge		--	0.18	--	nC
Q_{gd}	Gate Drain Charge		--	0.31	--	nC

Switching Characteristics

$t_{\text{d}(\text{on})}$	Turn on Delay Time	$V_{\text{DD}}=30\text{V}$, $I_D=0.3\text{A}$, $R_G=3.3\Omega$, $V_{\text{GS}}=10\text{V}$	--	6	--	ns
t_r	Turn on Rise Time		--	3.5	--	ns
$t_{\text{d}(\text{off})}$	Turn Off Delay Time		-	20	--	ns
t_f	Turn Off Fall Time		--	5.9	--	ns

Source Drain Diode Characteristics

I_{SD}	Source drain current(Body Diode)	$T_A=25^\circ\text{C}$	--	--	0.2	A
V_{SD}	Forward on voltage②	$T_j=25^\circ\text{C}$, $I_{\text{SD}}=0.5\text{A}$, $V_{\text{GS}}=0\text{V}$	--	0.78	1.2	V

Notes:

① Pulse width limited by maximum allowable junction temperature

②Pulse test ; Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Characteristics

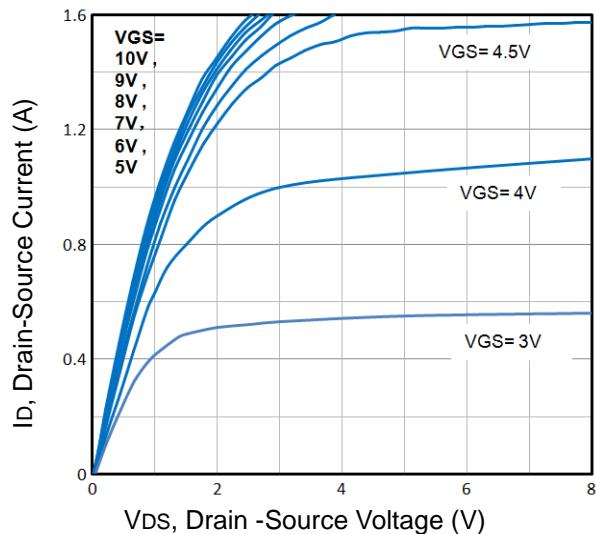


Fig1. Typical Output Characteristics

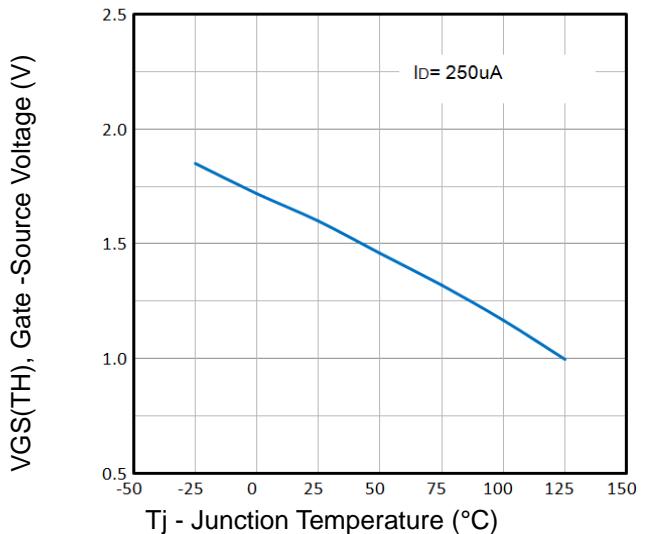


Fig2. Normalized Threshold Voltage Vs. Temperature

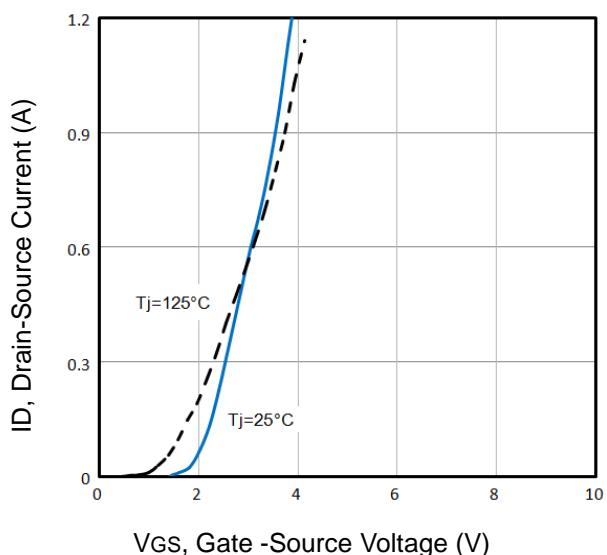


Fig3. Typical Transfer Characteristics

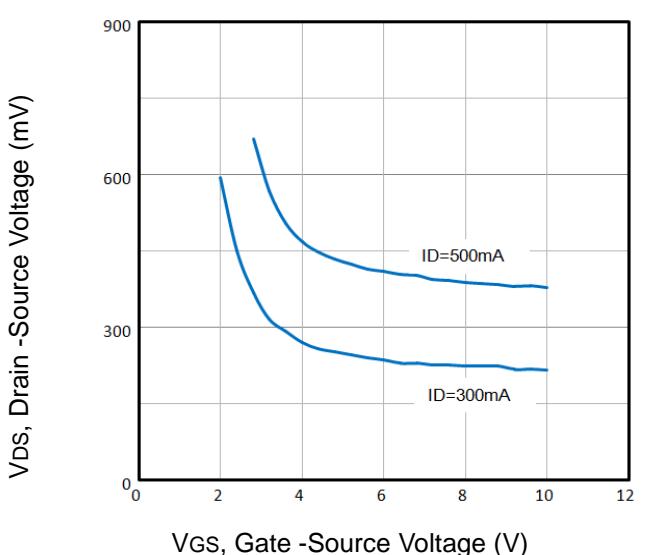


Fig4. Drain -Source Voltage vs Gate -Source Voltage

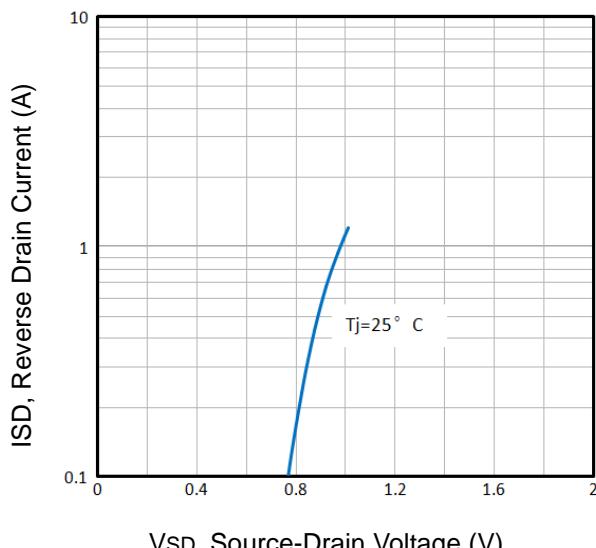


Fig5. Typical Source-Drain Diode Forward Voltage

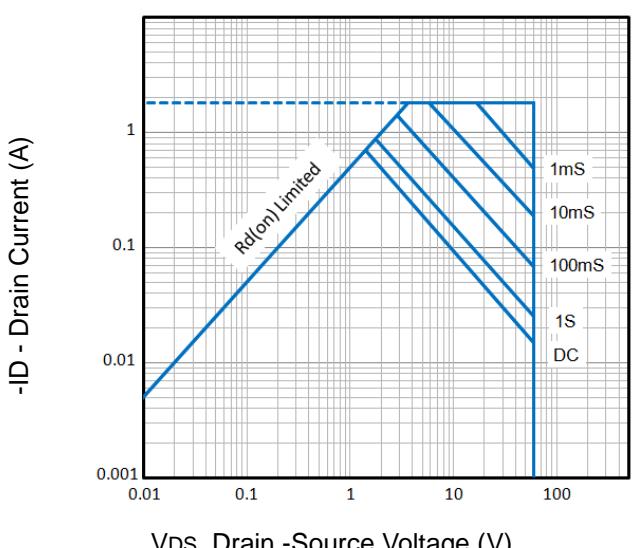


Fig6. Maximum Safe Operating Area

The curve above is for reference only.

Typical Characteristics

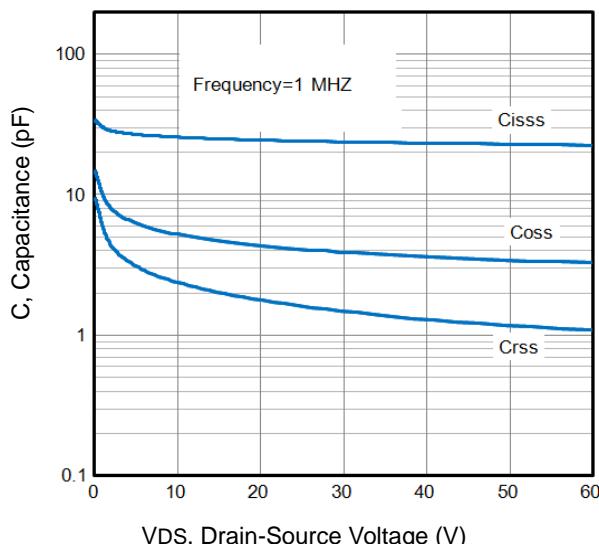


Fig7. Typical Capacitance Vs. Drain-Source Voltage

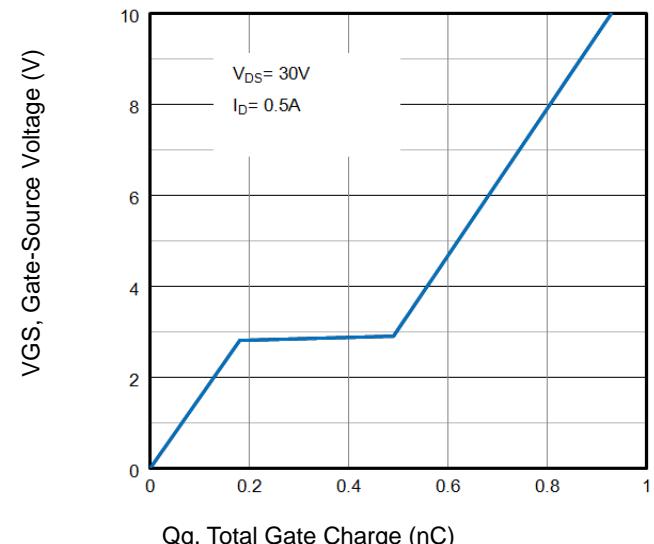


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

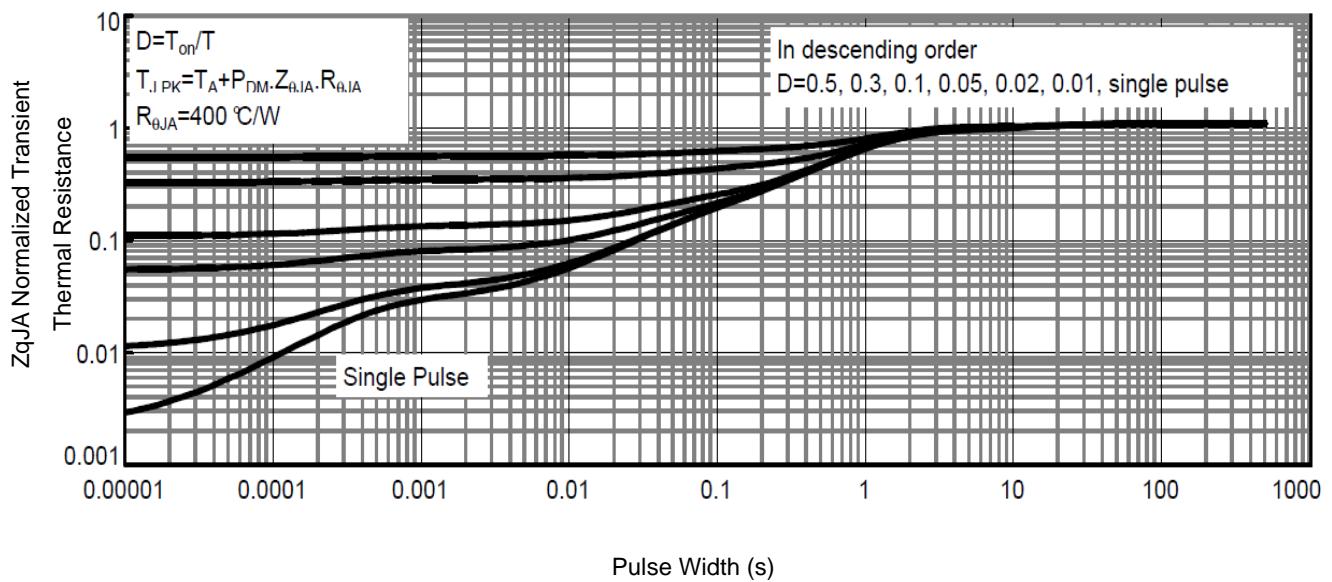


Fig9. Normalized Maximum Transient Thermal Impedance

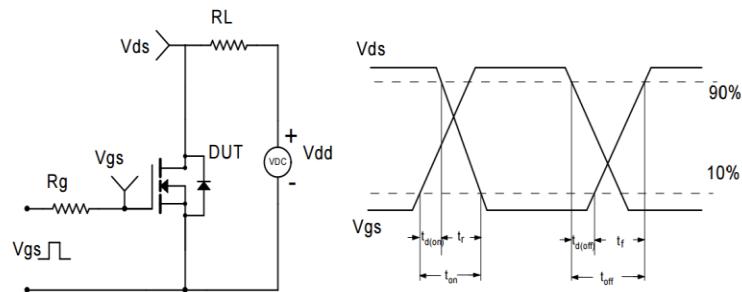
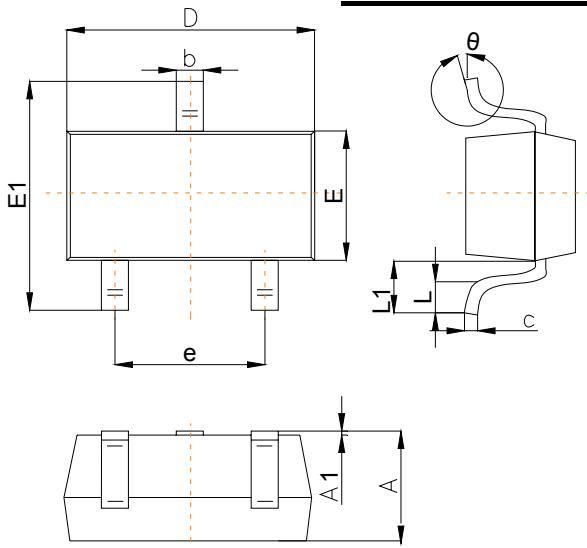


Fig10. Switching Time Test Circuit and waveforms

The curve above is for reference only.

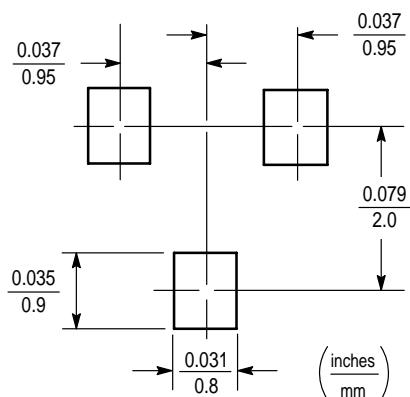
Outlitne Drawing

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		
	Min	Typ	Max
A	1.00		1.40
A1			0.10
b	0.35		0.50
c	0.10		0.20
D	2.70	2.90	3.10
E	1.40		1.60
E1	2.4		2.80
e		1.90	
L	0.10		0.30
L1	0.4		
θ	0°		10°

Suggested Pad Layout



Note:

1. Controlling dimension:in/millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purposes only.

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