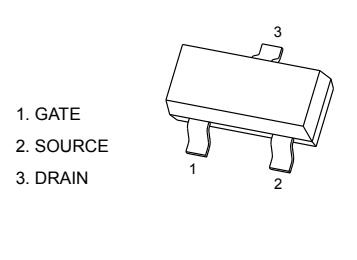


SOT-23 Plastic-Encapsulate MOSFETS

30V N-Channel MOSFET

V_{(BR)DSS}	R_{DS(on)MAX}	I_D
30V	28 mΩ@10V	5A
	34 mΩ@4.5V	

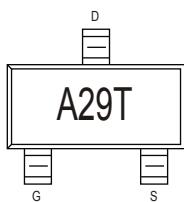
SOT-23



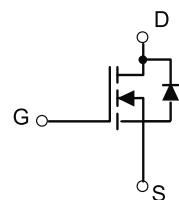
Features

Lead free product is acquired
Surface mount package

MARKING



Equivalent circuit



PACKAGE SPECIFICATIONS

Package	Reel Size	Reel DIA. (mm)	Q'TY/Reel (pcs)	Box Size (mm)	QTY/Box (pcs)	Carton Size (mm)	Q'TY/Carton (pcs)
SOT-23	7'	178	3000	203×203×195	45000	438×438×220	180000

Maximum Ratings and Thermal Characteristics (TA = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±16	
Continuous Drain Current	I _D	5.0	A
		4	
Maximum Power Dissipation ²⁾	P _D	1.5	W
		0.9	
Pulsed Drain Current ¹⁾	I _{DM}	20.4	A
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-50 to 150	°C
Thermal Resistance Junction-Ambient	R _{θJA}	80	°C/W

Notes

1) Pulse width limited by maximum junction temperature.

2) Surface Mounted on FR4 Board, t ≤ 5 sec.

The above data are for reference only.



AO3402

MOSFET ELECTRICAL CHARACTERISTICS

 $T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition	Min.	Typ.	Miax.	Unit
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 250\mu\text{A}$	30			V
Drain-Source On-State Resistance ¹⁾	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 4.0\text{A}$		28	36	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_{\text{D}} = 3.0\text{A}$		34	50	
		$V_{\text{GS}} = 2.5\text{V}, I_{\text{D}} = 1.0\text{A}$		55	80	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250\mu\text{A}$	0.5	0.8	1.2	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate Body Leakage	I_{GSS}	$V_{\text{GS}} = \pm 16\text{V}, V_{\text{DS}} = 0\text{V}$			100	nA
Forward Transconductance ¹⁾	g_{f}	$V_{\text{DS}} = 15\text{V}, I_{\text{D}} = 4\text{A}$		8	—	S
Dynamic						
Total Gate Charge	Q_g	$V_{\text{DS}} = 15\text{V}, I_{\text{D}} = 4\text{A}$ $V_{\text{GS}} = 4.5\text{V}$		3.1		nC
Gate-Source Charge	Q_{gs}			0.4		
Gate-Drain Charge	Q_{gd}			1.3		
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 15\text{V}, R_{\text{G}} = 3.3\ \Omega$ $I_{\text{D}} = 1\text{A}, V_{\text{GS}} = 10\text{V}$		4.4		ns
Turn-On Rise Time	t_r			2.6		
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$			25.5		
Turn-Off Fall Time	t_f			3.3		
Input Capacitance	C_{iss}	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 0\text{V}$ $f = 1.0\ \text{MHz}$		240		pF
Output Capacitance	C_{oss}			35		
Reverse Transfer Capacitance	C_{rss}			30		
Source drain current(Body Diode)	I_{SD}				1.8	A
Diode Forward Voltage	V_{SD}	$I_{\text{S}} = 4.0\text{A}, V_{\text{GS}} = 0\text{V}$		0.85	1.2	V

¹⁾ Pulse test : Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Characteristics

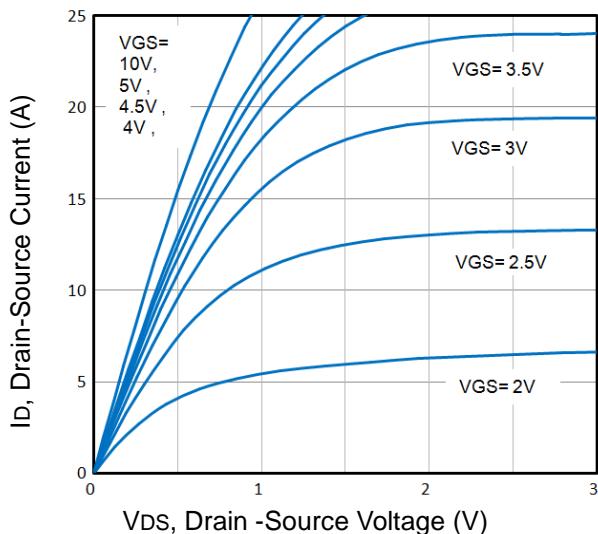


Fig1. Typical Output Characteristics

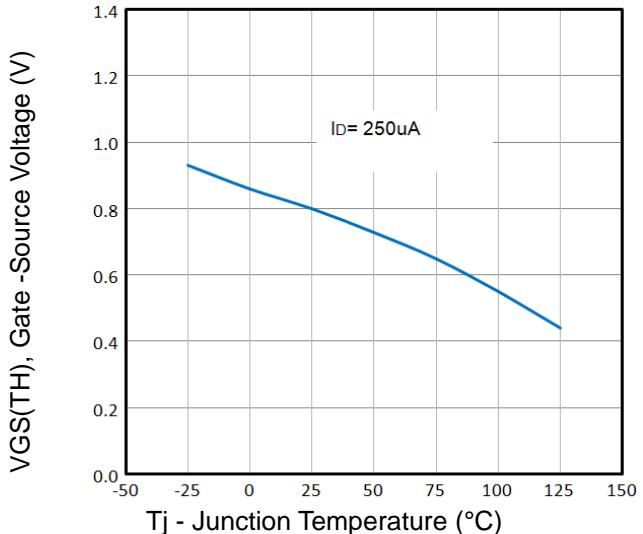


Fig2. Normalized Threshold Voltage Vs. Temperature

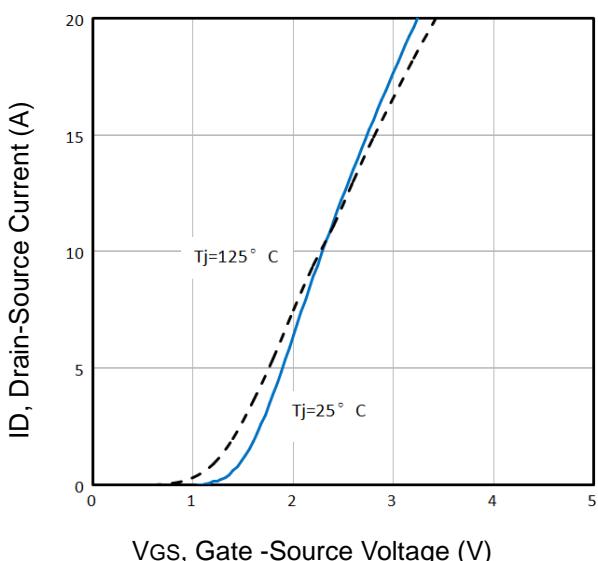


Fig3. Typical Transfer Characteristics

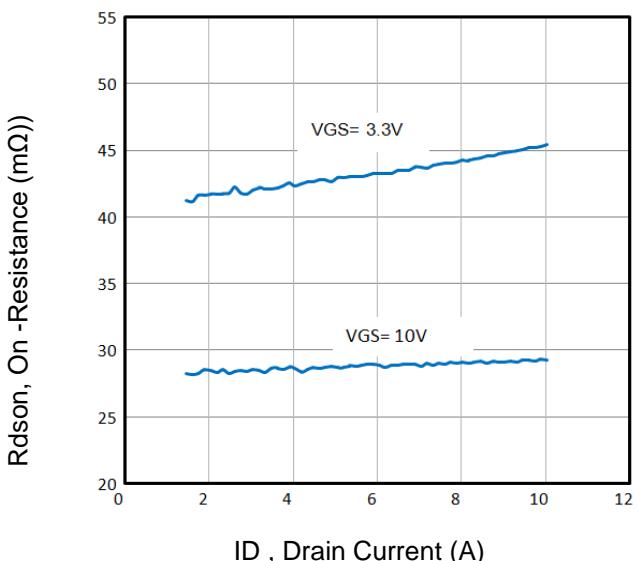


Fig4. On-Resistance vs. Drain Current and Gate

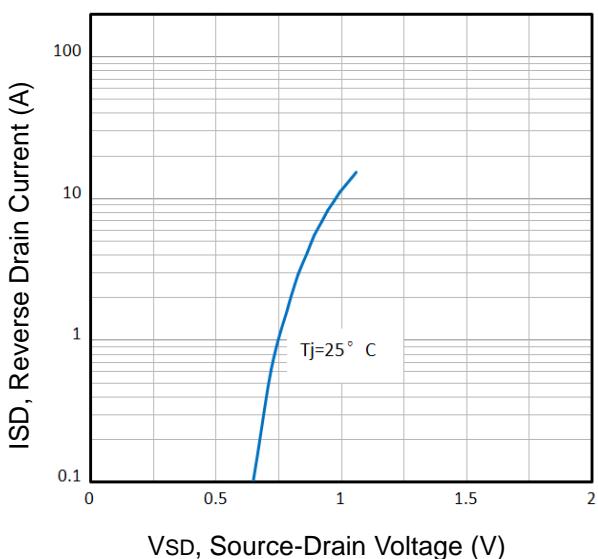


Fig5. Typical Source-Drain Diode Forward Voltage

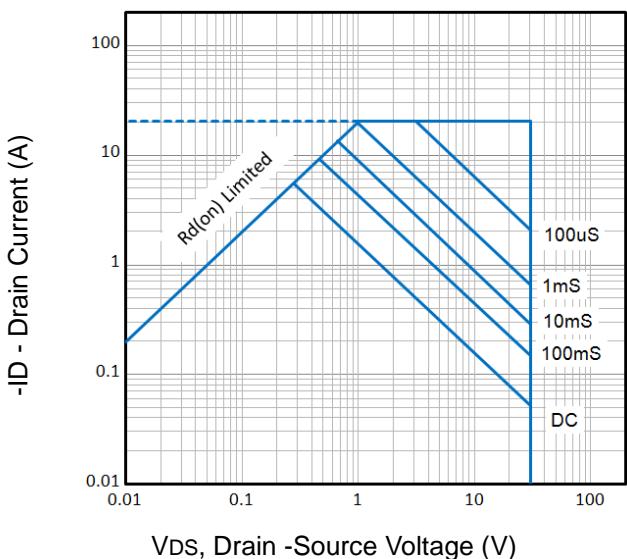


Fig6. Maximum Safe Operating Area

Typical Characteristics

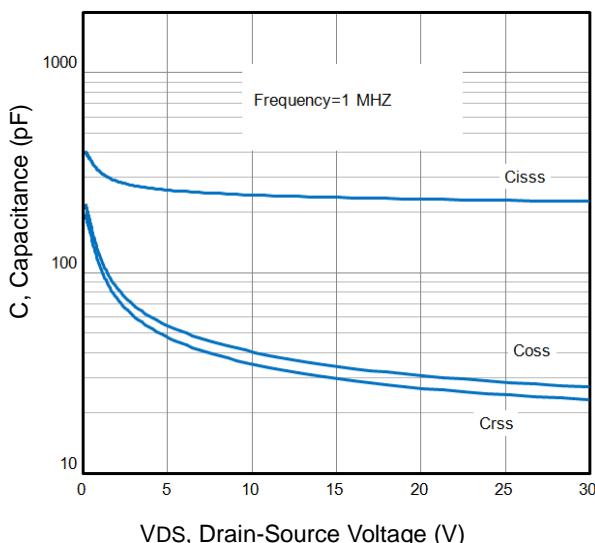


Fig7. Typical Capacitance Vs. Drain-Source Voltage

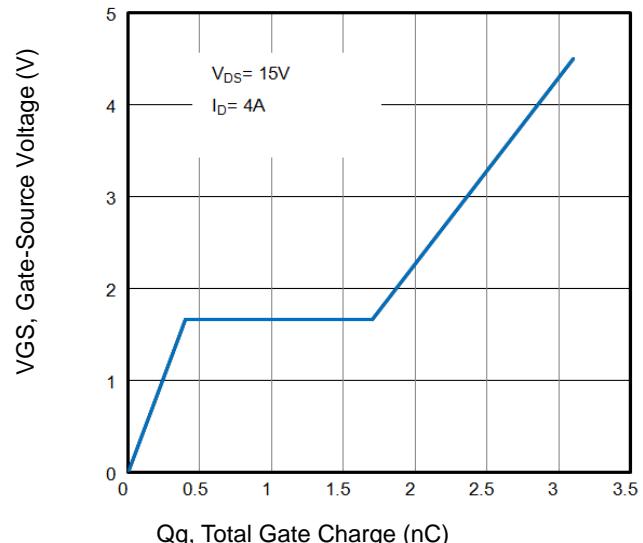


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

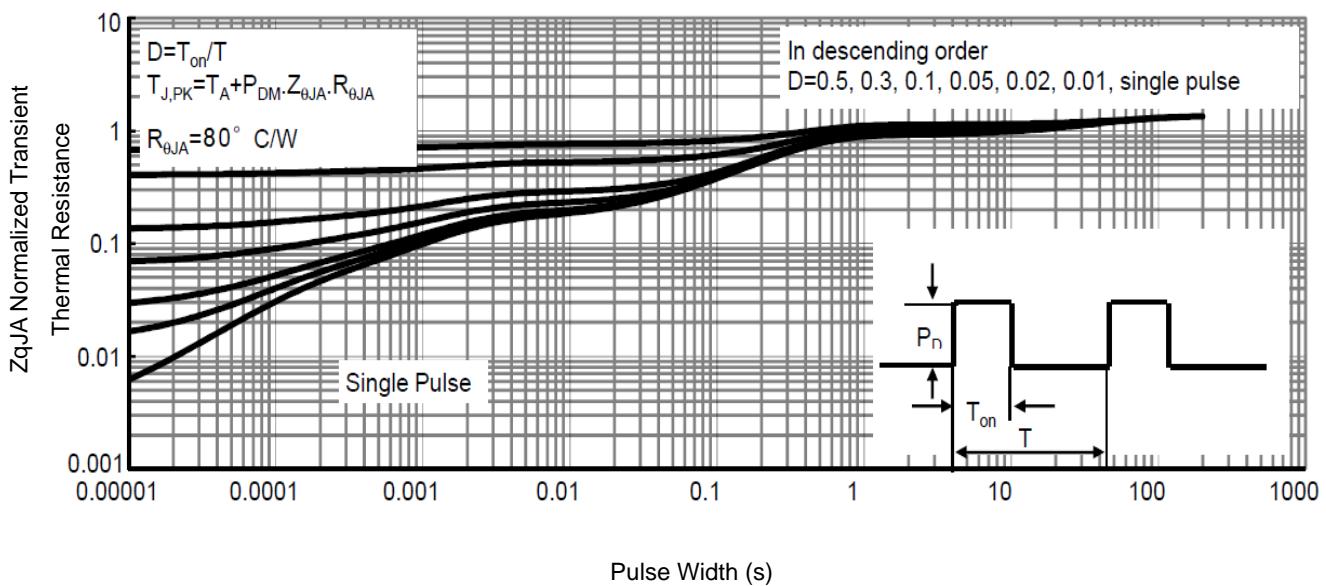


Fig9. Normalized Maximum Transient Thermal Impedance

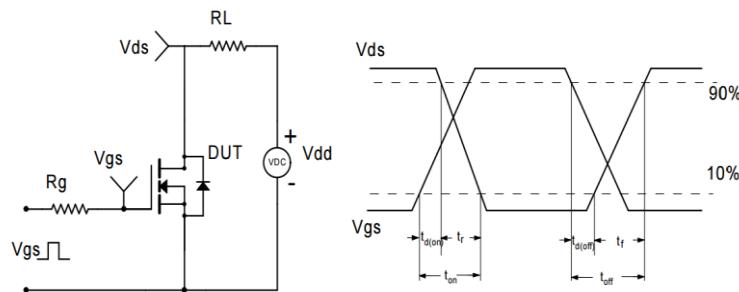
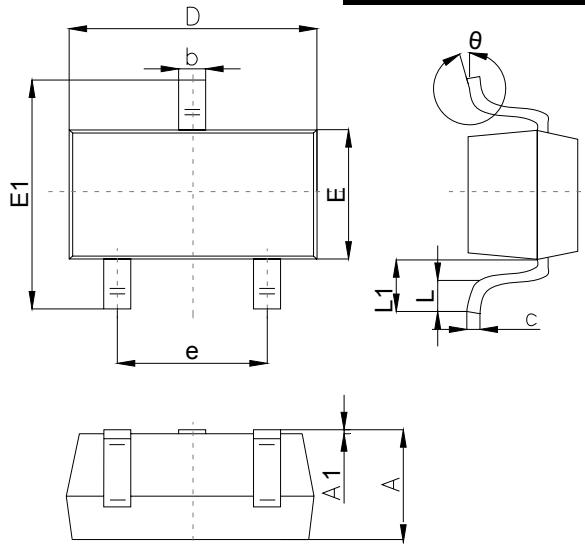


Fig10. Switching Time Test Circuit and waveforms

The curve above is for reference only.

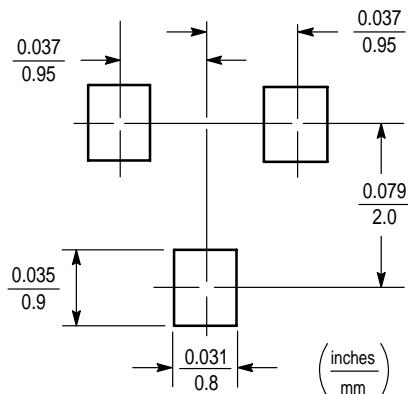
Outlitne Drawing

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		
	Min	Typ	Max
A	1.00		1.40
A1			0.10
b	0.35		0.50
c	0.10		0.20
D	2.70	2.90	3.10
E	1.40		1.60
E1	2.4		2.80
e		1.90	
L	0.10		0.30
L1	0.4		
θ	0°		10°

Suggested Pad Layout



Note:

1. Controlling dimension:in/millimeters.
- 2.General tolerance: ±0.05mm.
- 3.The pad layout is for reference purposes only.

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