ST10100

## SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

## Features

- Trench MOS Schottky barrier diodes
- Low forward voltage drop
- low profile - typical body height 1.1 mm
- Moisture sensitivity: level 1, per J-STD-020
- High temperature soldering guaranteed: $260^{\circ} \mathrm{C} / 10$ seconds - RoHS Compliant
- Halogen-free according to IEC 61249-2-21 definition


## Mechanical Data

Case : JEDEC TO-277 Molded plastic body
Case Material: Molding compound meets UL 94V-0 flammability rating
Terminals :Plated Leads Solderable per MIL-STD-202,Method
208 Polarity : Polarity symbol marking on body
Mounting Position : Any
Weight : 0.003 ounce, 0.092 grams

## Maximum Ratings And Electrical Characteristics

Ratings at $25^{\circ} \mathrm{C}$ ambient temperature unless otherwise specified.
Single phase half-wave 60 Hz ,resistive or inductive load,for capacitive load current derate by $20 \%$.

| Parameter | SYMBOL | ST10100 | UNIT |
| :---: | :---: | :---: | :---: |
| Marking Code |  | $\begin{gathered} \text { MDD } \\ \text { ST10100 } \end{gathered}$ |  |
| Maximum repetitive peak reverse voltage | VRRM |  |  |
| Maximum DC blocking voltage | Voc |  |  |
| RMS Reverse voltage | VRMS | 70 | V |
| Average Rectified Output Current | I(0) | 10 | A |
| Non-Repetitive Peak Forward Surge8.3ms Single Half Sine-Wave Superimposed on rated load(JEDEC Method) | Ifsm | 200 | A |

Electrical Characteristics $\mathrm{Ta}=25^{\circ} \mathrm{C}$ unless otherwise noted

| Parameter | Test Conditions | Symbol | Value |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Typ | Max |  |
| Forward Voltage Drop at 10A | $\begin{aligned} & \mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C} \\ & \mathrm{TA}_{\mathrm{A}}=125^{\circ} \mathrm{C} \end{aligned}$ | VF | 0.66 | 0.70 | V |
|  |  |  | - | 0.61 |  |
| Peak reverse curent at rated DC blocking voltage | $\begin{aligned} & \mathrm{TA}=25^{\circ} \mathrm{C} \\ & \mathrm{TA}=125^{\circ} \mathrm{C} \end{aligned}$ | IR | 15 | 30 | uA |
|  |  |  | - | 20 | mA |
| TTypical junction capacitance | $4.0 \mathrm{~V}, 1 \mathrm{MHz}$ | CJ | 240 |  | PF |
| Typical thermal resistance Junctionto Ambient |  | RөJa Rөлl |  |  | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating junction and storage temperaturerange |  | TJ,Tstg | -55 to +150 |  | ${ }^{\circ} \mathrm{C}$ |

Note:1.The typical data above is for referenceonly.
DN:T20520AO

## Ratings And Characteristic Curves



The curve above is for reference only.

## ST10100

## Packing information



Note:Devices are packed in accor dance with EIA standar RS-481-A and specifications listed above.

## Reel packing

| PACKAGE | REEL SIZE | REEL <br> $(\mathrm{pcs})$ | COMPONENT <br> SPACING <br> $(\mathrm{m} / \mathrm{m})$ | BOX <br> $(\mathrm{pcs})$ | INNER <br> BOX <br> $(\mathrm{m} / \mathrm{m})$ | REEL <br> DIA, <br> $(\mathrm{m} / \mathrm{m})$ | CARTON <br> SIZE <br> $(\mathrm{m} / \mathrm{m})$ | APPROX. <br> $(\mathrm{pcs})$ | CARTON <br> $(\mathrm{kg})$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| TO- 277 | $13 "$ | 5,000 | 4.0 | 10,000 | $210 * 208^{*} 203$ | 330 | $430 * 430 * 235$ | 80,000 | 13.0 |

## Suggested Pad Layout



| Symbol | Unit (mm) | Unit (inch) |
| :---: | :---: | :---: |
| A | 3.60 | 0.142 |
| B | 5.35 | 0.211 |
| C | 1.50 | 0.059 |
| D | 1.85 | 0.073 |
| E | 4.30 | 0.169 |

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