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# MT29F32G08CBACAWP-Z

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## Data Sheets (1)

**Data Sheet**

**L73A NAND datasheet**

L73A NAND datasheet

China  
MLC NAND  
NAND Flash  
Taiwan  
USA

**File Type:** PDF      **Updated:** 08/2015

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## Specs

Orderable Parts for: MT29F32G08CBACAWP-Z

[SEE ALL MLC NAND PARTS](#)

	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBACAWP-Z:C	<b>Contact Factory</b>	N/A	N/A	N/A	N/A	No		<a href="#">VIEW</a>

## Detailed Specifications

<b>Density</b>	32Gb	<b>Status</b>	Contact Factory	<b>NAND Type</b>	2D	<b>RoHS</b>	Yes
<b>Width</b>	x8	<b>Voltage</b>	3.3V	<b>Package</b>	TSOP	<b>Pin Count</b>	48-pin
<b>Op. Temp.</b>	0C to +70C	<b>MT/s</b>		<b>I/O</b>	Common	<b>Product Name</b>	

# Sim Models & Software

## Sim Models

HSpice



**HSpice: NAND 32/64/128/256Gb MLC (Rev C) L73A (ZIP)**

MLC NAND  
NAND Flash

REV 2.1

**File Type:** ZIP

**Updated:** 01/13/2012

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IBIS



**IBIS: NAND 32/64/128/256Gb MLC (Rev C) L73A (ZIP)**

MLC NAND  
NAND Flash

Rev. 2.1

**File Type:** ZIP

**Updated:** 01/13/2012

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## Software

Software Support Pack

**mtd-nand-use-a-lower-value-for-badblockbits-when-working-with-MLC-NAND (ZIP)**

MLC NAND  
NAND Flash

MLC NANDs have more bit flips than SLC. When looking for bad block marker we have a lot of false positive if we check for the whole byte. To avoid this tolerate a few (4 here) bit flips for byte.

**File Type:** ZIP

**Updated:** 03/31/2016

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Software Support Pack

**NAND FLASH lockunlock bug fix (ZIP)**

MLC NAND  
NAND Flash  
SLC NAND

Reset NAND before write protect check.

**File Type:** ZIP

**Updated:** 03/31/2016

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Software Support Pack

**fixup-ubi-cannot-recover-master node issue (ZIP)**

MLC NAND  
NAND Flash

For MLC NAND, paired page issue is now a common known issue. This patch is just for master node cannot be recovered while there will two pages be damaged in one single master block.

**File Type:** ZIP

**Updated:** 03/31/2016

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Software Support Pack

**UBI power loss issue for paired page Ver2.0 (ZIP)**

MLC NAND  
NAND Flash

These patches aim to solve MLC NAND paired page power loss issue by adding a bakvol (backup volume) module in UBI layer. 70 series and 80 series families.

File Type: ZIP

Updated: 03/31/2016

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### Software Support Pack

#### YAFFS mark bad block issue (ZIP)

If the bitflips num over mtd->bitflip\_threshold the mtd\_read\_oob will return -EUCLEAN and tags->ecc\_result > YAFFS\_ECC\_RESULT\_NO\_ERROR. Then we will call yaffs\_handle\_chunk\_error.

MLC NAND  
NAND Flash  
SLC NAND

File Type: ZIP

Updated: 03/31/2016

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## RoHS Certificates

### RoHS Certificates

#### RoHS Certificate of Compliance (PDF)

MT29F32G08CBACAWP-Z

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 11/2016

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### RoHS Certificates

#### China RoHS Certificate (PDF)

MT29F32G08CBACAWP-Z

Part-specific certification as required by China's Management Methods for Controlling Pollution by Electronic Information Products.

File Type: (PDF)

Updated: 11/2016

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## Documentation & Support

[See All MLC NAND Documentation](#)

### Technical Notes

SEARCH (4) MLC NAND TECHNICAL NOTES

### Technical Notes

#### TN-00-33: Power Integrity Simulation with IBIS 5.0 Models (PDF)

3D NAND  
DDR3 SDRAM

(TN-00-33) This technical note describes the new features in IBIS 5.0 enabling PI simulation. It also provides an overview of some of the modeling accuracy challenges and compares SSO simulation results using various electronic design automation (EDA) software tools.

**File Type:** PDF **Updated:** 01/25/2016

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**Technical Notes**

**TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)**

DDR SDRAM  
 DDR2 SDRAM  
 DDR3 SDRAM  
 DDR3L-RS  
[See More Tags](#)

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

**File Type:** PDF **Updated:** 02/14/2013

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**Technical Notes**

**TN-29-65: Software Device Drivers for Very Large Page Micron NAND Flash Memory (PDF)**

MLC NAND  
 NAND Flash  
 SLC NAND

(TN-29-65) Download the low-level driver described in this document [here](#).

**File Type:** PDF **Updated:** 10/15/2010

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**Technical Notes**

**TN-29-61: Wear Leveling in NAND Flash Memory (PDF)**

MLC NAND  
 NAND Flash  
 SLC NAND

(TN-29-61) This technical note describes the recommended wear leveling algorithm to be implemented in the FTL software for NAND Flash memory.

**File Type:** PDF **Updated:** 10/15/2010

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[SEARCH \(4\) MLC NAND TECHNICAL NOTES](#)

Parts with the same Data Sheet (4)

MT29F32G08CBACAWP-Z ( Current )		MT29F32G08CBACAWP-ITZ	MT29F32G08CBCCBH1-12Z	MT29F32G08CBCCBH1-12ITZ
Status	Contact Factory	Contact Factory	Contact Factory	Production
Density	32Gb	32Gb	32Gb	32Gb
NAND Type	2D	2D	2D	

RoHS	<b>Yes</b>	Yes	Yes	Yes
Width	<b>x8</b>	x8	x8	x8
Voltage	<b>3.3V</b>	3.3V	3.3V	3.3V
Package	<b>TSOP</b>	TSOP	<b>VBGA</b>	<b>VBGA</b>
Pin Count	<b>48-pin</b>	48-pin	<b>100-ball</b>	<b>100-ball</b>
Op. Temp.	<b>0C to +70C</b>	<b>-40C to +85C</b>	0C to +70C	<b>-40C to +85C</b>
	MT/s		<b>166 MT/s</b>	<b>166 MT/s</b>
I/O	<b>Common</b>	Common	Common	Common
Product Name				

## Where to Buy

### Orderable Parts



	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29F32G08CBACAWP-Z:C	<b>Contact Factory</b>	N/A	N/A	N/A	N/A	No		<a href="#">VIEW</a>

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