# ULTRA－SMALL PACKAGE PWM／PFM SWITCHING CONTROL STEP－UP SWITCHING REGULATOR 

## General Description

The ME2129 series is a CMOS step－up switching regulator which mainly consists of a reference voltage source，an oscillation circuit， an error amplifier，a phase compensation circuit， a PWM／PFM switching control circuit．Without the use of external low－ON－resistance Nch Power MOS，can be applied to need a high efficiency，high output current of the application circuit．The ME2129 series switches its operation to the PFM control circuit whose duty ratio is 15 \％with to the PWM／PFM switching control circuit under a light load and to prevent decline in the efficiency by IC operation current．

## Features

－Low voltage operation：Start－up is guaranteed from $0.9 \mathrm{~V}(\mathrm{IOUT}=1 \mathrm{~mA})$
－Duty ratio：Built－in PWM／PFM switching control circuit 15 to 78 \％．
－oscillator frequency： 300 KHz
－External parts：coil，diode，capacitor，and transistor
－Output voltage range：＜20V
－Output voltage accuracy：$\pm 2 \%$
－Soft start function： 2 ms

## Applications

－Mobile phones（PDC，GSM，CDMA，IMT200 etc．）
－Bluetooth equipment
－PDA
－Portable communication modem
－Portable games
－Cameras
－Digital cameras
－Cordless phones
－Notebook computers

## Package

－5－pin SOT23－5

## Typical Application Circuit



Fig． 1 For use chip enable


Fig． 2 For Feedback and chip enable

## Selection Guide



| product series | switching <br> transistor | CE <br> function | VDD <br> function | FB <br> function | features |
| :---: | :---: | :---: | :---: | :---: | :---: |
| ME2129C535M5G | Build in <br> Transistor | Yes | Yes | No | LX＋Enable |
| ME2129FM5G | Build in <br> Transistor | Yes | Yes | Yes | LX＋Feedback |

## Pin Configuration



## Pin Assignment

## ME2129CxxM5G

| Pin Number | Pin <br> Name | Function |
| :---: | :---: | :---: |
| SOT－23－5 | CE | Shutdown pin |
| 1 | LX | Swich |
| 2 | GND | GND pin |
| 3 | NC | NC |
| 4 | VOUT | Output voltage pin |
| 5 |  |  |

ME2129FM5G

| Pin Number | Pin <br> Name | Function |
| :---: | :---: | :---: |
| SOT－23－5 | CE | Shutdown pin |
| 1 | LX | Swich |
| 2 | GND | GND pin |
| 3 | VDD | IC power supply pin |
| 4 | FB | Feed Back voltage pin |
| 5 |  |  |

## Block Diagram



ME2129C Series


ME2129F Series

## Absolute Maximum Rangs

| PARAMETER | SYMBOL | RATINGS | UNITS |
| :---: | :---: | :---: | :---: |
| VDD Pin Voltage | VDD | $-0.3 \sim 6.5$ | V |
| LX Pin Voltage | LX | $-0.3 \sim \mathrm{VDD}+0.3$ | V |
| CE Pin Voltage | VCE | $-0.3 \sim$ VDD +0.3 | V |
| LX Pin Voltage | ILX | $\pm 1000$ | mA |
| Power Dissipation（SOT23－5） | Pd | 300 | mW |
| OperatingTemperature Range | $\mathrm{T}_{\text {Opr }}$ | $-25 \sim+85$ | ${ }^{\circ} \mathrm{C}$ |
| StorageTemperature Range | $\mathrm{T}_{\mathrm{stg}}$ | $-40 \sim+125$ | ${ }^{\circ} \mathrm{C}$ |

ME2129

## Electrical Characteristics

## ME2129CxxG

Measuring conditions: VDD=VOUT(S)X0.6,IOUT=100mA,VCE=VDD, Topt= $25^{\circ} \mathrm{C}$ 。Unless otherwise specified。


ME2129
ME2129FxxG
Measuring conditions: VDD=VCE $=3.3 \mathrm{~V}$, $\mathrm{Topt}=25^{\circ} \mathrm{C}$ 。Unless otherwise specified。

| Parameter | SYMBOL | CONDITIONS |  | MIN | TYP | MAX | UNITS | CIRCUIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Feed back voltage | VFB | - |  | 1.225 | 1.25 | 1.275 | V | 4 |
| Input voltage | VDD | - |  |  | - | 6 | V | 4 |
| Operation start voltage | VST | IOUT $=1 \mathrm{~mA}$ |  | - | - | 0.9 | V | 4 |
| Operation holding voltage | VHLD | $\mathrm{IOUT}=1 \mathrm{~mA}$, Measured by decreasing VIN voltage gradually |  | 0.7 | - | - | V | 4 |
| Current consumption 1 | ISS1 | $\mathrm{VFB}=\mathrm{VFB}(\mathrm{S}) \times 0.95$ |  | - | 1.5 | - | mA | 3 |
| Current consumption 2 | ISS2 | $\mathrm{VFB}=1.5 \mathrm{~V}$ |  | - | 15 | - | uA | 3 |
| Current consumption during shutdown | ISSS | VCE=0V |  | - | 0.01 | 0.5 | uA | 3 |
| Feed back voltage <br> temperature coefficient |  | $\mathrm{Ta}=-25-85^{\circ} \mathrm{C}$ |  | - | $\pm 50$ | - | ppm/ ${ }^{\circ} \mathrm{C}$ | 4 |
| Oscillation frequency | fosc | VFB $=\mathrm{VFB}(\mathrm{S}) \times 0.95-$ |  | 255 | 300 | 345 | kHz | 3 |
| Max. duty ratio | MAXDUTY | $\mathrm{VFB}=\mathrm{VFB}(\mathrm{S}) \times 0.95$ |  | - | 78 | - | \% | 3 |
| PWM/PFM switching <br> duty ratio | PFMDUTY | $\mathrm{VFB}=\mathrm{VFB}(\mathrm{S}) \times 1.5$, no load |  | - | 15 | - | \% | 3 |
| Shutdown pin input voltage | VSH | Measured the oscillation at LX pin |  | 0.75 | - | - | V | 3 |
|  | VSL1 | Judged the stop of oscillation at LX pin | VOUT $\geq 1.5 \mathrm{~V}$ | - | - | 0.3 | V | 3 |
| Shutdown pin input voltage | ISH | $\mathrm{VCE}=\mathrm{VFB}(\mathrm{S}) \times 0.95$ |  | -0.1 | - | 0.1 | uA | 3 |
|  | ISL | VCE=0V |  | -0.1 | - | 0.1 | uA | 3 |
| Soft start time | tss | - |  | - | 2 | - | mS | 4 |
| Efficiency | EFFI | - |  | - | 85 | - | \% | 4 |

1. $\operatorname{VOUT}(\mathrm{S})$ is the set output voltage value,and VOUT is the typital value of the output voltage.
2. $\operatorname{VOUT}(\mathrm{S})$ can be set by using the rate of VFB and output voltage setting resisitors(R1,R2).
3. $\operatorname{VFB}(\mathrm{S})$ is the set output voltage value.
4. This product from the start when the $\mathrm{VDD}=0.9 \mathrm{~V}$ booster work , but in order to stabilize the output voltage and oscillation frequency ,to control the VDD, $1.8 \mathrm{~V} \leqq \mathrm{VDD}<6 \mathrm{~V}$.

## Test Circuit

1. 


2.

3.

4.


## External parts（suggest）

1，Diode use Schottky diode such as IN5817 or IN5819（forward voltage drop：0．2V）
2，Inductor： $22 \mu \mathrm{H}$（ $\mathrm{r}<0.5 \Omega$ ）
3，Capacitor：Tantalum type 47uF

## External parts selection for DC／DC converter

The relationship between major characteristics of the step－up circuit and characteristics parameters of the external parts are shown in Figure 1.


Figure 1 Relationship between major characterstics of the step－up circuit and external parts

## 1．Inductor

An inductance has strong influence on maximum output current IOUT and efficiency $\eta .1$ ．
Figure 2 shows the relation between IOUT，and $\eta$ characteristics to L of ME2129．


Figure 2 L －lout and $\eta$ characteristics

The peak current（IPK）increases by decreasing L and the stability of a circuit improves and IOUT increases．If L is furthermore made small，efficiency falls and in running short，IOUT decreases．（ Based on the current drive capability of external switching transistor．）

The loss of IPK by the switching transistor decreases by increasing $L$ and the efficiency becomes maximum at a certain $L$ value．Further increasing L decreases efficiency due to the loss of DC resistance of the coil．Also，IOUT decreases，too．

Oscillation frequency is higher，smaller one can be choosed and also makes coil smaller．The recommended inductances are 22 to $100 \mu \mathrm{H}$ inductor for ME2129．

Choose a value for $L$ by refering to the reference data because the maximum output current is due to the input voltage in an actual case．Choose an inductor so that IPK does not exceed the allowable current．Exceeding the allowable current of the inductor causes magnetic saturation，remarkable low efficiency and destruction of the IC chip due to a large current．

IPK in uncontinuous mode is caluculated from the following equatuon

$$
I_{P K}=\sqrt{\frac{2 I_{\text {OUT }}\left(V_{\text {OUT }}+V_{D}-V_{I N}\right)}{f_{\text {OSC }} \cdot L}}(A)
$$

fosc $=$ oscillation frequency， $\mathrm{VD}_{\mathrm{D}} \cong 0.4 \mathrm{~V}$ ．

## 2．Diode

Use an external diode that meets the following requirements：
－Low forward voltage：（ $\mathrm{V} \subset<0.3 \mathrm{~V}$ ）
－High switching speed：（50 ns max．）
－Reverse voltage：Vout＋Vfor more
－Rated current：Ipk or more

## 3．Capacitor（Cin，Co）

A capacitor at the input side（Cin）improves the efficiency by reducing the power impedance and stabilizing the input current．Select a CIN value according to the impedance of the power supply used．

A capacitor at the output side（Co）is used for smoothing the output voltage．For step－up types，the output voltage flows intermittently to the load current so that step－up types need a larger capacitance than step－down types． Therefore，select an appropriate capacitor depending on the ripple voltage that increases in case of a higher output voltage or a higher load current．The capacitor value should be $10 \mu \mathrm{~F}$ minimum．
Select an appropriate capacitor with an ESR（Equivalent Series Resistance）for stable output voltage．A stable range of the volatge at this IC depends on the ESR．Although the inductance（L）is also a factor，an ESR of $30 \mathrm{~m} \Omega$ to 500 $\mathrm{m} \Omega$ draws out the characteristics．However，the best ESR may depend on L，capacitance，wiring and applications （output load）．Therefore，fully evaluate ESRs under an actual condition to determine the best value．

## 4．Precautions

－Mount external capacitors，a diode，and a coil as close as possible to the IC．
－Unique ripple voltage and spike noise occur in switching regulators．Because they largely depend on the coil and the capacitor used，check them using an actually mounted model．
－Make sure dissipation of the switching transistor（especially at a high temperature）does not exceed the allowable power dissipation of the package．
－The performance of this IC varies depending on the design of the PCB patterns，peripheral circuits and external parts．Thoroughly test all settings with your device．Also，try to use recommended external parts．

## Typical Performance Characteristics

## 1．Output Waveforms


lout $=1 \mathrm{~mA}$

lout＝100mA
2．Transient Response characteristics
（1）Powering ON（Vin： $\mathbf{0} \boldsymbol{\rightarrow} \mathbf{2 V}$ ）

lout $=10 \mathrm{~mA}$

lout $=200 \mathrm{~mA}$

lout $=100 \mathrm{~mA}$
（2）Responses of CE pin（CE： $\mathbf{0} \boldsymbol{\rightarrow} \mathbf{2 V}$ ）

lout＝1mA

lout $=100 \mathrm{~mA}$

## 3．Output Current vs．Output Voltage



4．Output Current vs．Efficiency


## Packaging Information

－SOT23－5


| DIM | Millimeters |  | Inches |  |
| :---: | :---: | :---: | :---: | :---: |
|  | Min | Max | Min | Max |
| A | 0.9 | 1.45 | 0.0354 | 0.0571 |
| A1 | 0 | 0.15 | 0 | 0.0059 |
| A2 | 0.9 | 1.3 | 0.0354 | 0.0512 |
| A3 | 0.6 | 0.7 | 0.0236 | 0.0276 |
| b | 0.25 | 0.5 | 0.0098 | 0.0197 |
| c | 0.1 | 0.26 | 0.0039 | 0.0102 |
| D | 2.8 | 3.1 | 0.1102 | 0.122 |
| e1 | 1．9（TYP） |  | 0．0748（TYP） |  |
| E | 2.6 | 3.1 | 0.1024 | 0.1201 |
| E1 | 1.5 | 1.8 | 0.05118113 | 0.07086618 |
| e | 0．95（TYP） |  | 0．0374（TYP） |  |
| L | 0.25 | 0.6 | 0.0098 | 0.0236 |
| L1 | 0．59（TYP） |  | 0．0232（TYP） |  |
| $\theta$ | 0 | $8^{\circ}$ | 0 | $8^{\circ}$ |
| c1 | 0．2（TYP） |  | 0．0079（TYP） |  |

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