

P-Channel MOSFET MEM2307XG

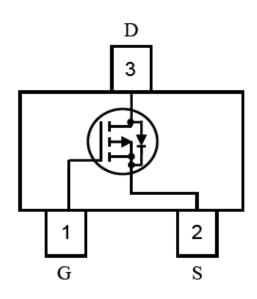
General Description

MEM2307XG Series P-channel enhancement mode field-effect transistor ,produced with high cell density DMOS trench technology, which is especially used to minimize on-state resistance. This device particularly suits low voltage applications, and low power dissipation, and low power dissipation in a very small outline surface mount package.

Features

- $\begin{array}{l} -30 \text{V} \text{--}4.1 \text{A} \\ \text{R}_{\text{DS(ON)}} \!\! < \! 88 \text{m} \Omega @ \text{V}_{\text{GS}} \!\! = \!\! -10 \text{V}, \text{I}_{\text{D}} \!\! = \!\! -4.1 \text{A} \\ \text{R}_{\text{DS(ON)}} \!\! < \! 108 \text{m} \Omega @ \text{V}_{\text{GS}} \!\! = \!\! -4.5 \text{V}, \text{I}_{\text{D}} \!\! = \!\! -3 \text{A} \end{array}$
- High Density Cell Design For Ultra Low On-Resistance
- Subminiature surface mount package: SOT23

Pin Configuration



Typical Application

- Power management
- Load switch
- Battery protection

Absolute Maximum Ratings

Parameter		Symbol	Ratings	Unit	
Drain-Source Voltage		V_{DSS}	-30V	V	
Gate-Source Voltage		V_{GSS}	±20	V	
Drain	T _A =25℃	1	-4.1	^	
Current	T _A =70°C	ID	-3.5	A	
Pulsed Drain Current ^{1,2}		I _{DM}	-20	A	
Total Power	T _A =25℃	Pd	1.4	W	
Dissipation	T _A =70°C	ru	1	VV	
Operating Temperature Range		T _{Opr}	150	$^{\circ}$	
Storage Temperature Range		T _{stg}	-55/150	$^{\circ}$ C	



MEM2307XG

Thermal Characteristics

Param	neter	Symbol	TYP.	MAX.	Unit
Thermal Resistance,	t≤10s	D	65	90	°C/W
Junction-to-Ambient	12105	$R_{ hetaJA}$	05	90	C/VV
Thermal Resistance,	Steady-State	$R_{ hetaJA}$	85	125	°C/W
Junction-to-Ambient	Steady-State				
Thermal Resistance,	Stoody State	$R_{ heta JL}$	43	60	°CM
Junction-to-Lead	Steady-State				

Electrical Characteristics

Parameter	Symbol	Test Condition	Min	Туре	Max	Unit
	S	tatic Characteristics				
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250uA	-30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = -250uA$	-1	-1.3	-2	V
Coto Rody Lookago	I _{GSS}	$V_{DS}=0V$, $V_{GS}=20V$			100	nA
Gate-Body Leakage		V_{DS} =0V, V_{GS} =-20V			-100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V V _{GS} =0V			-1000	nA
Static Drain-Source	R _{DS(ON)1}	V _{GS} =-10V,I _D =-4.1A			88	mΩ
On-Resistance	R _{DS(ON)2}	V_{GS} =-4.5V, I_D =-3A			108	mΩ
Forward Transconductance	g FS	$V_{DS} = -5 \text{ V}, I_{D} = -4 \text{A}$	5.5	8.2		S
Maximum Body-Diode Continuous Current	Is				-2.2	Α
Source-drain (diode forward) voltage	V _{SD}	V _{GS} =0V,I _S =-1A		0.77	-1.0	V
	Dy	namic Characteristics				
Input Capacitance	Ciss	VGS=0V, VDS=-15V, f=1MHz		700	840	pF
Output Capacitance	Coss			120		
Reverse Transfer Capacitance	Crss	I= HVIITZ		75		
Gate resistance	Rg	VGS=0V, VDS=0V, f=1MHz		10	15	Ω
	Sw	itching Characteristics		-1		
Turn-On Delay Time	td(on)			8.6		
Rise Time	tr	VGS=-10V,VDS=-15V,		5		no
Turn-Off Delay Time	td(off)	RL=3.6Ω,RGEN=6Ω		28.2		ns
Fall-Time	tf			13.5		
Total Gate Charge	Qg	$V_{DS} = -15 \text{ V},$		14.3		
Gate-Source Charge	Qgs	$V_{GS} = -4.5 \text{ V},$		3.1		nc
Gate-Drain Charge	Qgd	I _D = -4A		3		

- 1. Repetitive rating, pulse width limited by junction temperature.
- 2. The static characteristics are obtained using 80 µs pulses, duty cycle 0.5% max.



Typical Performance Characteristics

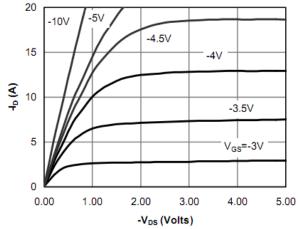


Figure 1: On-Region Characteristics

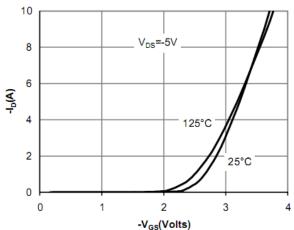


Figure 2: Transfer Characteristics

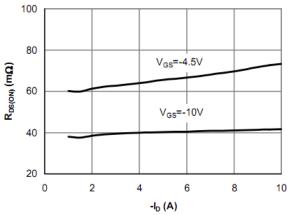


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

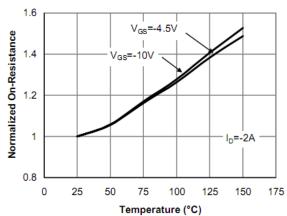


Figure 4: On-Resistance vs. Junction
Temperature

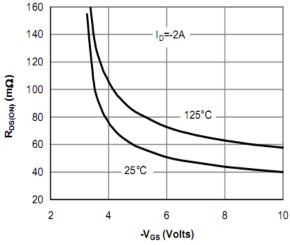


Figure 5: On-Resistance vs. Gate-Source Voltage

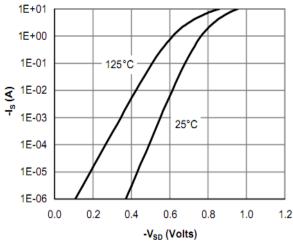
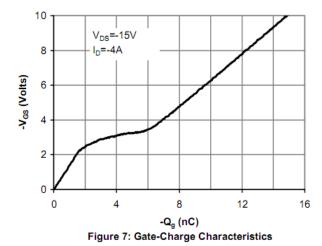


Figure 6: Body-Diode Characteristics





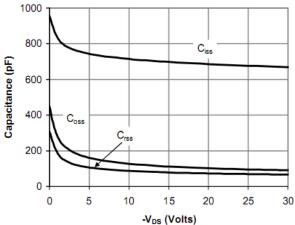


Figure 8: Capacitance Characteristics

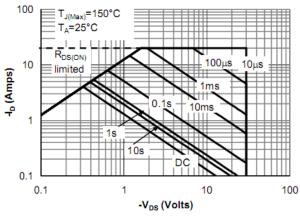


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

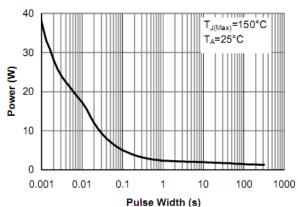


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

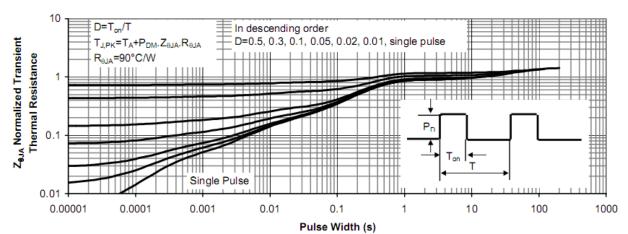
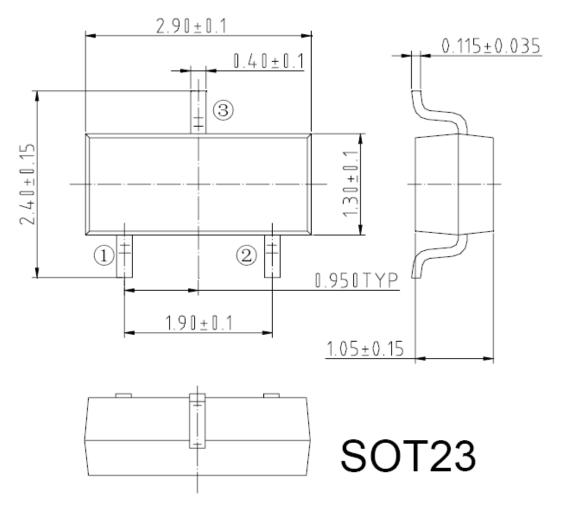


Figure 11: Normalized Maximum Transient Thermal Impedance



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