

6 Lake Street, Lawrence, MA 01841 1-800-446-1158 / (978) 620-2600 / Fax: (978) 689-0803

Website: http://www.microsemi.com

PNP POWER SILICON TRANSISTOR

 ${\it Qualified per MIL-PRF-19500/545}$

DEVICES

2N5151 2N5153 2N5151L 2N5153L 2N5151U3 2N5153U3 LEVELS
JAN
JANTX
JANTXV
JANS

ABSOLUTE MAXIMUM RATINGS ($T_C = +25^{\circ}C$ unless otherwise noted)

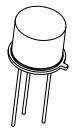
Parameters / Test Conditions	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V_{CBO}	100	Vdc
Emitter-Base Voltage	V_{EBO}	5.5	Vdc
Collector Current	I_{C}	2.0	Adc
	P_{T}	1.0 10 1.16 100	W
Operating & Storage Junction Temperature Range	T_{J} , T_{stg}	-65 to +200	°C
Thermal Resistance, Junction-to Case	$R_{ heta JC}$	10 1.75 (U3)	°C/W

Note:

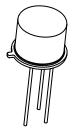
- 1) Derate linearly $5.7 \text{mW}/^{\circ}\text{C}$ for $T_A > +25^{\circ}$
- 2) Derate linearly $66.7 \text{mW}/^{\circ}\text{C}$ for $T_A > +25^{\circ}$
- 3) Derate linearly $6.63 \text{mW}/^{\circ}\text{C}$ for $T_A > +25^{\circ}$
- 4) Derate linearly $571 \text{mW}/^{\circ}\text{C}$ for $T_A > +25^{\circ}$

ELECTRICAL CHARACTERISTICS ($T_A = +25^{\circ}C$, unless otherwise noted)

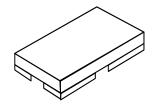
Parameters / Test Conditions	Symbol	Min.	Max.	Unit			
OFF CHARACTERTICS	OFF CHARACTERTICS						
	$V_{(BR)CEO}$	80		Vdc			
	I_{EBO}		1.0 1.0	μAdc mAdc			
$\begin{aligned} & \text{Collector-Emitter Cutoff Current} \\ & V_{\text{CE}} = 60 \text{Vdc}, \ V_{\text{BE}} = 0 \\ & V_{\text{CE}} = 100 \text{Vdc}, \ V_{\text{BE}} = 0 \end{aligned}$	I_{CES}		1.0 1.0	μAdc mAdc			
Collector-Base Cutoff Current $V_{CE} = 40 V dc$, $I_B = 0$	I_{CEO}		50	μAdc			



TO-5 2N5151L, 2N5153L (See Figure 1)



TO-39 (TO-205AD) 2N5151, 2N5153



U-3 2N5151U3, 2N5153U3



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ELECTRICAL CHARACTERISTICS

Parameters / Test Conditions		Symbol	Min.	Max.	Unit
ON CHARACTERTICS		<u> </u>			
Forward-Current Transfer Ratio $I_C = 50 \text{mAdc}, \ V_{CE} = 5 \text{Vdc}$	2N5151 2N5153		20 50		
$I_C = 2.5 Adc$, $V_{CE} = 5 Vdc$	2N5151 2N5153	$h_{ m FE}$	30 70	90 200	
$I_C = 5 Adc$, $V_{CE} = 5 Vdc$	2N5151 2N5153		20 40		
Collector-Emitter Saturation Voltage $I_{C}=2.5 Adc, I_{B}=250 mAdc \\ I_{C}=5.0 Adc, I_{B}=500 mAdc$		V _{CE(sat)}		0.75 1.5	Vdc
Base-Emitter Voltage Non-Saturation $I_C = 2.5 Adc, V_{CE} = 5 Vdc$		$V_{ m BE}$		1.45	Vdc
Base-Emitter Saturation Voltage $I_{C}=2.5 Adc, I_{B}=250 mAdc \\ I_{C}=5.0 Adc, I_{B}=500 mAdc$		V _{BE(sat)}		1.45 2.2	Vdc
DYNAMIC CHARACTERISTICS				•	•
Magnitude of Common Emitter Small-Signal S	hort-Circuit				

Magnitude of Common Emitter Small-Signal Sh Forward Current Transfer Ratio	ort-Circuit				
$I_C = 500$ mAdc, $V_{CE} = 5$ Vdc, $f = 10$ MHz	2N5151 2N5153	$ \mathbf{h}_{\mathrm{fe}} $	6 7		
Common-Emitter Small-Signal Short-Circuit. Forward-Current Transfer Ratio					
$I_C = 100 \text{mAdc}, V_{CE} = 5 \text{Vdc}, f = 1 \text{kHz}$ 2N5151 2N5153		h_{fe}	20 50		
Output Capacitance $V_{CB} = 10 Vdc$, $I_E = 0$, $f = 1.0 MHz$		C _{obo}		250	pF

SWITCHING CHARACTERISTICS

Parameters / Test Conditions	Symbol	Min.	Max.	Unit
$\begin{aligned} & \text{Turn-On Time} \\ & I_C = 5 \text{Adc, } I_{B1} = 500 \text{mAdc} \\ & I_{B2} = -500 \text{mAdc} \\ & R_L = 6 \Omega \\ & V_{BE(OFF)} = 3.7 V \text{dc} \end{aligned}$	t _{on}		0.5	μs
$\begin{aligned} & \text{Turn-Off Time} \\ & I_C = 5 \text{Adc}, I_{B1} = 500 \text{mAdc} \\ & I_{B2} = -500 \text{mAdc} \\ & R_L = 6 \Omega \\ & V_{BE(OFF)} = 3.7 \text{Vdc} \end{aligned}$	${ m t_{off}}$		1.5	μs



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SWITCHING CHARACTERISTICS (cont.)

Parameters / Test Conditions		Symbol	Min.	Max.	Unit
Storage Time	$I_{C} = 5Adc, I_{B1} = 500mAdc$ $I_{B2} = -500mAdc$	t _s		1.4	μs
Fall Time	$R_{L} = 6\Omega$ $V_{BE(OFF)} = 3.7Vdc$	t_{f}		0.5	μs

SAFE OPERATING AREA

DC Tests

 $T_C = +25$ °C, 1 Cycle, $t_P = 1.0$ s

Test 1

 $V_{CE} = 5.0 Vdc$, $I_C = 2.0 Adc$

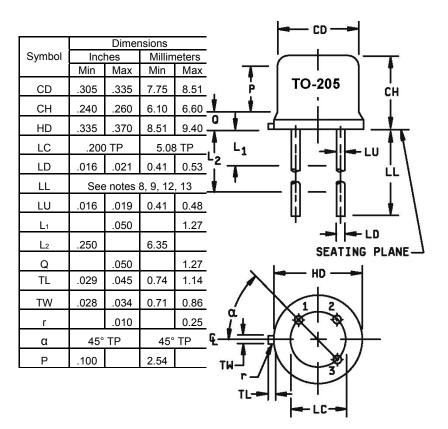
Test 2

 $V_{CE} = 32Vdc$, $I_C = 310mAdc$

Test 3

 $V_{CE} = 80 Vdc$, $I_C = 14.5 mAdc$

FIGURE 1 (TO-5, TO-39) PACKAGE DIMENSIONS





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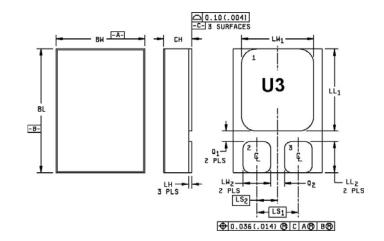
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NOTES:

- 1 Dimensions are in inches.
- 2 Millimeters are given for general information only.
- 3 Beyond r (radius) maximum, TW shall be held for a minimum length of .011 inch (0.28 mm).
- 4 TL measured from maximum HD.
- 5 Outline in this zone is not controlled.
- 6 CD shall not vary more than .010 inch (0.25 mm) in zone P. This zone is controlled for automatic handling.
- 7 Leads at gauge plane .054 +.001 -.000 inch (1.37 +0.03 -0.00 mm) below seating plane shall be within.007 inch (0.18 mm) radius of true position (TP) at maximum material condition (MMC) relative to tab at MMC.
- 8 LU applied between L1 and L2. LD applies between L2 and LL minimum. Diameter is uncontrolled in L1 and beyond LL minimum.
- 9 All three leads.
- 10 The collector shall be electrically and mechanically connected to the case.
- 11 r (radius) applies to both inside corners of tab.
- 12 In accordance with ASME Y14.5M, diameters are equivalent to φx symbology.
- 13 For transistor types 2N5151 and 2N5153, LL is .5 inch (13 mm) minimum, and .75 inch (19 mm) maximum.
- 14 For transistor types 2N5151L and 2N5153L, LL is 1.5 inch (38 mm) minimum and 1.75 inch (44.4 mm) maximum.
- 15 Lead designation, depending on device type, shall be as follows: lead numbering; lead 1 = emitter, lead 2 = base, and lead 3 = collector.

FIGURE 2 (U3) PACKAGE DIMMENSIONS

Symbol	Dimensions				
Cymbol	Inches Millime			eters	
	Min	Max	Min	Max	
BL	.395	.405	10.04	10.28	
BW	.291	.301	7.40	7.64	
CH	.1085	.1205	2.76 3.06		
LH	.010	.020	0.25 0.51		
LL1	.220	.230	5.59	5.84	
LL2	.115	.125	2.93	3.17	
LS1	.150 BSC		3.81 BSC		
LS2	.075 BSC		1.91 BSC		
LW1	.281	.291	7.14	7.39	
LW2	.090	.100	2.29	2.54	
Q1	.030	_	0.762		
Q2	.030		0.762		



NOTES:

- 1 Dimensions are in inches.
- 2 Millimeters are given for general information only.
- 3 Terminal 1 collector, terminal 2 base, terminal 3 emitter

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