Messrs.									
Product Specification	Model	NMTC S20200XRCHS 10B	Rev. No.	Issued Date.	Page.				
	WIOUCI	NWITC-520200ARGH5-10D	A	Sept. 06, 17	1 / 28				

# LIQUID CRYSTAL DISPLAY MODULE MODEL: NMTC-S20200XRGHS-10B Customer's No.: None.



Microtips Technology Inc. 12F. No.31 Lane 169, Kang Ning St., His-Chih, Taipei Hsien, Taiwan, R.O.C. FAX: 886-2-26958625

Approved and Checked by





//////// Microtips Technology Inc.

Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	WIGUEI		A	Sept. 06, 17	2 / 28

#### **Revise Records**

Rev.	Date	Contents	Written	Approved
А.	2017/09/06	See Note 1.	Jill Hsu	Danny Lien

#### Special Notes

Note 1.	The LCD module is compliant with RoHS
Note 2.	
Note 3.	
Note 4.	
Note 5.	



Messrs.					
Product Specification	Model	NMTC S20200XPCHS 10B	Rev. No.	Issued Date.	Page.
	Model	NWITC-520200AR0H5-10D	A	Sept. 06, 17	3 / 28

# **CONTENTS**

ITEM	PAGE
FEATURES	4.
GENERAL SPECIFICATION	5.
ABSOLUTE MAXIMUN RATING	5.
ELECTRICAL CHARACTERISTICS	6.
OPTICAL CHARACTERISTICS	6.
MECHANICAL SPECIFICATION	9.
INTERFACE PIN ASSIGNMENT	10.
BLOCK DIAGRAM	11.
POWER SUPPLY DIAGRAM	11.
TIMING CHARACTERISTICS	12.
READ / WRITE TIMING CHART	13.
COMMANDS	14.
COMMANDS DESCRIPTION	15.
DD RAM ADDRESSING	19.
CG RAM MAPPING	21.
CHARACTER FONT TABLE	22.
RELIABILITY TEST	23.
APPEARANCE CHECK	23.
HANDLING PRECAUTIONS	24.
LCD PRODUCT QUALITY STANDARD	25.
WARRANTY	27.
DIMENSIONAL OUTLINES	28.



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	WIGGET	NWITC-520200AR0115-10D	A	Sept. 06, 17	4 / 28

## **FEATURES of**

## LIQUID CRYSTAL DISPLAY MODULE

## MODEL NO.: <u>NMTC-S20200XRGHS-10B</u>

The Microtips Customized LCD module, model: NMTC-S20200XRGHS-10B is compliant with RoHS

Character Type Dot Matrix LCD Module

Easy interface of 4 - bit or 8 - bit

Display character pattern:  $5 \times 7$  font with cursor (208 kinds)

The special character pattern can be programmable by Character Generator RAM directly.

A customer character pattern can be programmable by mask option.

Automatically power on reset.

Internal Memory:

Character Generator ROM (CGROM): 10,080 bits (204 characters  $\times$  5  $\times$  8 dot) -

Character Generator RAM (CGRAM):  $64 \times 8$  bits (8 characters  $\times 5 \times 8$  dot) \_

Low power operation:

- Power supply voltage range:  $2.7 \sim 5.5 \text{ V} (V_{DD})$ 

- LCD drive voltage range:  $3.0 \sim 13.0 \text{ V} (V_{DD} - \text{Vo})$ 

Low power consumption

CMOS process

Duty – cycle: 1/16

View Direction	☑ 6 O'clock				□ 12 0	□ 12 O'clock				
L CD Tures	□ FSTN Positive				□ FS7	□ FSTN Negative				
LCD Type	STN Gra	ay	$\Box$ S	TN Y	ellow	Green			STN B	lue
Rear Polarizer	$\square$ Reflective $\square$ T			ransf	lectiv	e		, ,	Fransn	nissive
Dealtlight Type	□ LED □ Internal P		Powe	r	□ EL	□ EL		□ 5V input		
Backlight Type	LED	□ External		Power			□ CCFL		□ 12V input	
Backlight Color	□ White		Ambe	r		lue reen				□ Other
Temperature Range	□ Normal			🗹 Wide		□ Super Wide				
CCFL Inverter	🗆 Build –	□ Build – in		☑ Not Build – in						
EL Driver IC	□ Build – in		☑ Not Build – in							
Touch Screen	□ With			🗹 Without						

### **TO BE VERY CAREFUL !**

The LCD driver ICs are made of CMOS process, which is very easy to be damaged by static charge, make sure the user is grounded when handling the LCM.



Messrs.					
Product Specification	Model	NMTC S20200YPGHS 10B	Rev. No.	Issued Date.	Page.
	WIOdel	NWITC-520200AR0H5-10D	A	Sept. 06, 17	5 / 28

## **GENERAL SPECIFICATION**

Item	Content
Display Resolution	20 Characters $\times$ 2 lines
Dimensional Outline (mm)	116.0 (W) × 37.0 (H) × 10.0 max (D)
Display mode	Reflective / Positive Type
Circuit	IC, ST 7066
Interface	Data (DB0~DB7), RS, R / W, E

## **ABSOLUTE MAXIMUN RATING**

#### (1) Electrical Absolute Ratings

Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	$V_{DD} - V_{SS}$	-0.3	7.0	Volt	
Power Supply for LCD	V <sub>DD</sub> -Vo	0	15.0	Volt	
Input Voltage	$V_{IN}$	-0.3	$V_{DD}$ +0.3	Volt	
Supply Current for LED backlight	I <sub>LED</sub>		420	mA	

Note: Operator should be grounded during handling LCM.

### (2) Environmental Absolute Maximum Ratings

	Normal Temperature				Wide Temperature					
Item	Operating		Storage		Operating		Storage			
	Max,	Min.	Max,	Min.	Max,	Min.	Max,	Min.		
Ambient Temperature	0°C	+50°C	-20°C	+70°C	-20°C	+70°C	-30°C	+80°C		
Humidity (without condensation)	Note	2,4	Note	Note 3, 5 Note 4, 5		Note	e 4, 6			

Note 2: Ta $\leq$ 50°C : 80% RH max

Ta>50°C : Absolute humidity must be lower than the humidity of 85% RH at 50°C

Note 3: Ta at  $-20^{\circ}$ C will be < 48hrs at 70°C will be < 120hrs when humidity is higher than 70%.

- Note 4: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.
- Note 5: Ta $\leq$ 70°C : 75RH max

Ta > 70°C : absolute humidity must be lower than the humidity of 75% RH at 70°C Note 6: Ta at  $-30^{\circ}$ C will be < 48hrs, at 80°C will be < 120hrs when humidity is higher than 70%.



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	widdei	NWITC-520200AK0H5-10D	A	Sept. 06, 17	6/28

# **ELECTRICAL CHARACTERISTICS**

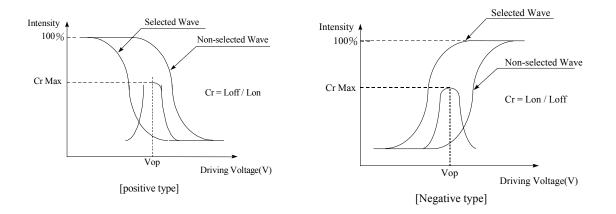
Item	Symbol	Condition	Min.	Тур.	Max.	Unit	
Power Supply for Logic	$V_{DD} - V_{SS}$		2.7	4.5	5.5	Volt	
Legent Maltaga	V <sub>IL</sub>	L level	$\mathbf{V}_{\mathbf{SS}}$	$0.2 \ V_{\text{DD}}$		Volt	
Input Voltage	V <sub>IH</sub>	H level	$0.8 \ V_{DD}$	$V_{DD}$		Volt	
LCM Recommend	V = 5.0V	$Ta = 0^{\circ}C$	4.129	4.429	4.729		
LCD Module Driving Voltage	$V_{DD} = 5.0V$ $V_{DD} - Vo = 4.0V$		$Ta = 25^{\circ}C$	4.0	4.30	4.6	Volt
Note 1		$Ta = 50^{\circ}C$	3.871	4.171	4.471		
Power Supply Current for LCM	I <sub>DD</sub> (B/L OFF)	$V_{DD} = 5.0V$ $V_{DD} - V_{0} = 4.0V$		1.5	2.0		
	$I_{LED}$	$Ta = 25^{\circ}C$	130	210	315	mA	

# **OPTICAL CHARACTERISTICS**

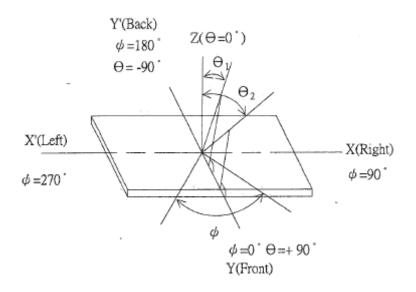
Item	Symbol	Condition	Min.	Тур	Max.	Unit	note
Viewing Angle Range	$\Phi f(12 \text{ o'clock})$			40			Note 2
	$\Phi b$ (6 o'clock)	When $C_{n} > 2$		50		Degree	
	$\Phi$ l (9 o'clock)	When $Cr \ge 2$		40			
	$\Phi r$ (3 o'clock)			40			
Rise Time	T <sub>r</sub>	$V_{DD} - Vo =$		65	250	G	
Fall Time	T <sub>f</sub>	4.0V		165	250	mS	Note 4
Contrast	Cr	$Ta = 25^{\circ}C$		4.5			Note 3

Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	widdei	Model NMTC-S20200ARGHS-10B	A	Sept. 06, 17	7 / 28

## [Note 1] Definition of Operation Voltage (Vop)



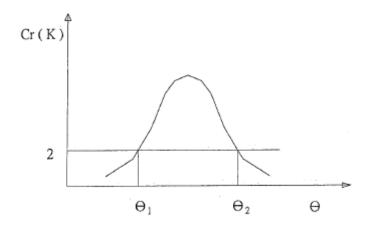
[Note 2] Definition of Angle  $\theta \& \phi$ 



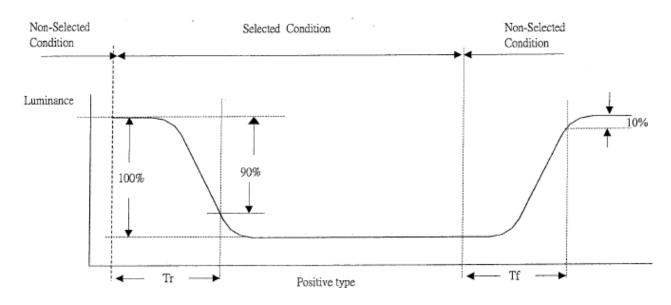


Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	Model		A	Sept. 06, 17	8 / 28

## [Note 3] Definition of Viewing Angle $\theta$ 1& $\theta$ 2



## [Note 4] Definition of Response Time (Tr, Tf)



Electro - Optical Characteristics are measured by



Messrs.						
Product Specification	Model	NMTC S20200XPCHS 10B	Rev. No.	Issued Date.	Page.	
	Model	NWITC-520200AR0H5-10D	A	Sept. 06, 17	9/28	

# **MECHANICAL SPECIFICATION**

Product No.		NMTC-S20200XRGHS-10B
Module Size		116.0 (W) mm × 37.0 (H) mm × 10.0 (D) mm Max
Display Format		20 characters × 2 lines
Character Size		3.2 mm × 5.55 mm
Character Pitch		3.7 mm × 5.95 mm
Dot Size		0.60 (W) mm × 0.65 (H) mm
Dot Pitch		0.65 (W) mm × 0.70 (H) mm
Duty Ratio		1/16 Duty
Character Font Table		☑English □European □Other
	STN	□Gray Mode <b>☑Yellow Mode</b> □Blue Mode
LCD Display Mode	FSTN	<ul> <li>Black &amp; White (Normally White / Positive Image)</li> <li>Black &amp; White (Normally White / Negative Image)</li> </ul>
	Rear Polarizer	☑ Reflective □ Transflective □ Transmissive
Viewing Direction		☑ 6 O'clock □12 O'clock □3 O'clock □9 O'clock
Backlight		☑Without □CCFL □EL □LED
Controller		ST 7066
DC/DC Converter		Without
CCFL Inverter		Without
EL Driver		Without



Messrs.					
Product Specification	Model	NMTC S20200XPGHS 10B	Rev. No.	Issued Date.	Page.
	Widdei	NWITC-520200AR0H5-10D	A	Sept. 06, 17	10/28

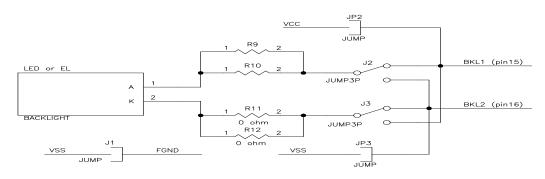
# **INTERFACE PIN ASSIGNMENT**

PIN NO.	<b>PIN OUT</b>	Description
1.	V <sub>SS</sub>	Ground
2.	V <sub>DD</sub>	Logic Circuit Power Supply
3.	$\mathbf{V}_{\mathrm{EE}}$	Power Supply For LCD Panel, please refer to page 11 for connection.
4.	RS	Data/ Instruction Register Select
5.	R/W	Read/ Write Select
6.	Е	Enable Signal
7.	DB0	
		3 – State I/O Data Bus
14.	DB7	
15.	BKLA	Power Supply for Backlight.
16.	BKLK	$100V$ / 400Hz AC for EL, 4.2V or $210 \sim 315$ mA DC for LED backlight Don't care if no backlight

## JUMPER EXPLANATION

- 1. JP2 and JP3 are both short: Pin15 is short with pin2 and pin16 is short with pin1. Backlight powered up via  $V_{DD}$  (pin2) and  $V_{SS}$  (pin1). Customer does not need to supply power to pin15 and pin16.
- 2. JP2 short and JP3 open: Pin 15 is short with Pin2 and pin16 is NOT short with pin1. Backlight is powered with  $V_{DD}$  (same as logic circuit) and customer should keep pin 15 floating or connected to  $V_{DD}$ . Customer could control the backlight independently ON or OFF with pin16 L or H.
- 3. JP2 open and JP3 short: Pin 15 is NOT short with pin 2 and pin16 is short with pin1. Backlight is common V<sub>SS</sub> with logic and customer should keep pin16 floating or connected to ground (V<sub>SS</sub>). Customer could control the backlight ON or OFF with pin15 H or L
- 4. JP2 and JP3 are both open: The backlight is fully independent with the logic, control the backlight via pin15 / pin16 or A/K.
- 5. J1 short: Bezel and screw holes connected to GND. J1 open: Bezel and screw holes floating
- 6. Never change the J2 and J3, it may burn off your system.

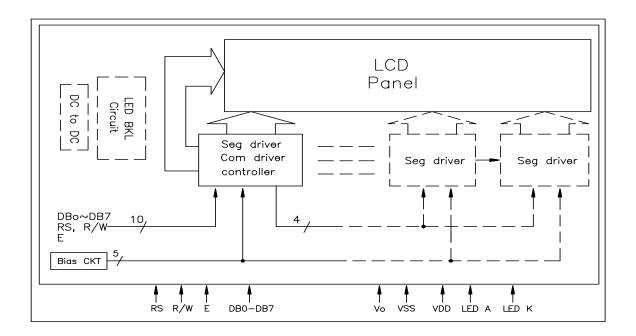
Note: J1, JP2 and JP3 are #0805 0 ohm resistors on the rear side of the PCB.



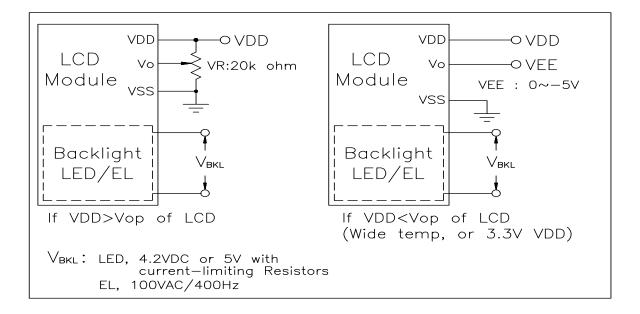


Messrs.					
Product Specification	Model	NMTC S20200YPGHS 10B	Rev. No.	Issued Date.	Page.
	Model	NMIC-520200ARGH5-10B	A	Sept. 06, 17	11 / 28

# **BLOCK DIAGRAM**



## **POWER SUPPLY**





Messrs.					
Product Specification	Model	NMTC S20200XPCHS 10B	Rev. No.	Issued Date.	Page.
	Model	odel NMIC-S20200ARGHS-10B	A	Sept. 06, 17	12 / 28

# TIMING CHARACTERISTICS

	AC Characteristics ( $V_{SS} = 0V$ , $V_{DD} = 4.5V$ to 5.0V, Ta = 0 to 5								
Mode	Characteristic	Symbol	Min.	Тур.	Max.	Unit			
	E Cycle Time	t <sub>C</sub>	500			ns			
e	E Rise / Fall Time	$t_R, t_F$			20	ns			
Iod	E Pulse Width (High, Low)	$t_{\rm W}$	230			ns			
Write Mode	R/W And RS Setup Time	t <sub>SU1</sub>	40			ns			
Vrit	R/W And RS Hold Time	$t_{\rm H1}$	10			ns			
Ν	Data Setup Time	t <sub>SU2</sub>	80			ns			
	Data Hold Time	t <sub>H2</sub>	10			ns			
	E Cycle Time	t <sub>C</sub>	500			ns			
Φ	E Rise / Fall Time	$t_R, t_F$			20	ns			
lode	E Pulse Width (High, Low)	$t_{ m W}$	230			ns			
N I	R/W And RS Setup Time	$t_{SU}$	40			ns			
Read Mode	R/W And RS Hold Time	t <sub>H</sub>	10			ns			
R	Data Setup Time	t <sub>D</sub>			120	ns			
	Data Hold Time	t <sub>DH</sub>	5			ns			

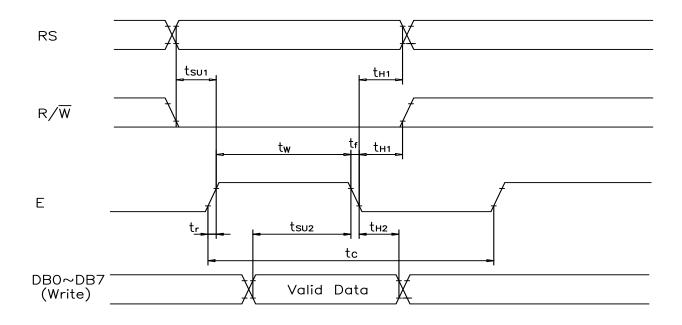
AC Characteristics	$(V_{SS} = 0V, V_{DD} = 2.7V \text{ to } 4.5V, Ta = 0 \text{ to } 50^{\circ}\text{C})$
--------------------	--

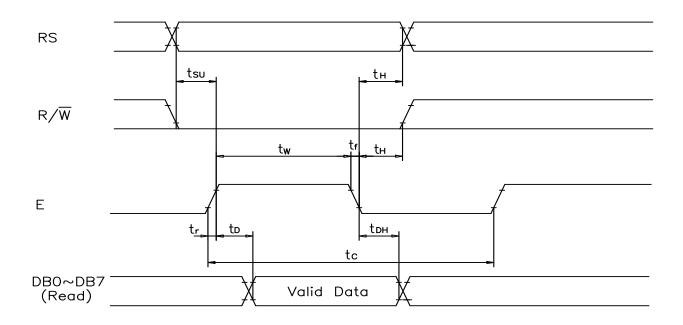
Mode	Characteristic	Symbol	Min.	Тур.	Max.	Unit
	E Cycle Time	t <sub>C</sub>	1000			ns
e	E Rise / Fall Time	$t_{\rm R}, t_{\rm F}$			25	ns
lod	E Pulse Width (High, Low)	t <sub>W</sub>	450			ns
Write Mode	R/W And RS Setup Time	t <sub>SU1</sub>	60			ns
Vrit	R/W And RS Hold Time	t <sub>H1</sub>	20			ns
2	Data Setup Time	t <sub>SU2</sub>	195			ns
	Data Hold Time	t <sub>H2</sub>	10			ns
	E Cycle Time	t <sub>C</sub>	1000			ns
e	E Rise / Fall Time	t <sub>R</sub> , t <sub>F</sub>			25	ns
Iod	E Pulse Width (High, Low)	$t_{\rm W}$	450			ns
d N	R/W And RS Setup Time	$t_{SU}$	60			ns
Read Mode	R/W And RS Hold Time	t <sub>H</sub>	20			ns
L A	Data Setup Time	t <sub>D</sub>			360	ns
	Data Hold Time	t <sub>DH</sub>	5			ns



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
i ioduct Specification	WIOUCI	141411C-520200AR0115-10D	A	Sept. 06, 17	13 / 28

# **READ/WRITE TIMING CHART**







Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
i ioduct Specification	WIOUCI	NWITC-520200AR0115-10D	A	Sept. 06, 17	14 / 28

# **COMMANDS**

				Ins	tructi	on C	ode					Execution
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	time (f <sub>OSC</sub> is 270kHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC	1.53mS
Return Home	0	0	0	0	0	0	0	0	1	*	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53mS
Entry Mode	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and make shift of entire display enable.	39 µS
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	Set display (D), cursor (C), and blinking of cursor (B) on / off control bit.	39 µS
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	*	*	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.	39 µS
Function Set	0	0	0	0	1	DL	N	F	*	*	Set interface data length (DL: 4 - bit / 8 - bit), numbers of display line (N: $1 - line / 2 - line$ ), display font type(F: 5 X 8 dots / 5 X 11 dots)	39 µS
Set CG RAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 µS
Set DD RAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 µS
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μS
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM / CGRAM).	43 µS
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM / CGRAM).	43 µS

\* means don't care



Messrs.					
Product Specification	Model	NMTC S20200YRGHS 10B	Rev. No.	Issued Date.	Page.
roddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	15 / 28

## **COMMANDS DESCRIPTION**

#### **Clear Display**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	0	0	0	0	0	1

Clear all the display data by writing "20H" (space code) to all DDRAM address, and set DDRAM address to "00H" into AC (address counter). Return cursor to the original status. namely, bring the cursor to the left edge on first line of the display. Make entry mode increment (I/D = "1").

#### **Return Home**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	0	0	0	0	1	*

Return Home is cursor return home instruction. Set DDRAM address to "00H" into the address counter.

Return cursor to its original site and return display to its original status, if shifted. Content of DDRAM is not changed.

#### **Entry Mode Set**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	0	0	0	1	I/D	SH

Set the moving direction of cursor and display.

I/D: Increment / decrement of DDRAM address (cursor or blink)

When I/D = "High", cursor / blink moves to right and DDRAM address is increased by 1.

When I/D = "Low", cursor / blink moves to left and DDRAM address is decreased by 1.

\* CGRAM operates the same as DDRAM, when read from or write to CGRAM.

#### SH: Shift of entire display

When DDRAM read (CGRAM read/write) operation or SH = "Low", shift of entire display is not performed.

If SH = "High" and DDRAM write operation, shift of entire display is performed according to I/D value (I/D = "1", shift left, I/D = "0": shift right).



Messrs.					
Product Specification	Model	NMTC S20200YPGHS 10B	Rev. No.	Issued Date.	Page.
i ioduct Speemeation	WIGUEI	NWITC-520200AR0115-10D	A	Sept. 06, 17	16/28

#### **Display ON/OFF Control**

0 0 0 0 0 0 0 1 D C B	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
	0	0	0	0	0	0	1	D	С	В

Control display / cursor / blink ON/OFF 1 bit register.

#### **D:** Display ON/OFF control bit

When D = "High", entire display is turned on.

When D = "Low", display is turned off, but display data is remained in DDRAM.

#### C: Cursor ON/OFF control bit

When C = "High", cursor is turned on.

When C = "Low", cursor is disappeared in current display, but I/D register remains its data.

#### **B:** Cursor Blink ON/OFF control bit

When B = "High", cursor blink is on, that performs alternate between all the high data and display character at the cursor position.

When B = "Low", blink is off.

#### **Cursor or Display Shift**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	0	1	S/C	R/L	*	*

Without writing or reading of display data, shift right / left cursor position or display. This instruction is used to correct or search display data. (Refer to Table 4)

During 2 – line mode display, cursor moves to the 2nd line after 40th digit of 1st line. Note that display shift is performed simultaneously in all the line.

When displayed data is shifted repeatedly, each line shifted individually.

When display shift is performed, the contents of address counter are not changed.

S/C	R/L	Operation
0	0	Shift cursor to the left, AC is decreased by 1.
0	1	Shift cursor to the right, AC is increased by 1.
1	0	Shift all of the display to the left, cursor moves according to the display.
1	1	Shift all of the display to the right, cursor moves according to the display.

Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
I foddet Speemeation	WIOUCI	NWITC-520200AR0115-10D	A	Sept. 06, 17	17/28

#### **Function Set**

00 0 0 0 1 DL N F *								221	10/11	RS	
	*	*	F	Ν	DL	1	0	0	0	00	

### DL: Interface data length control bit

When DL = "High", it means 8 – bit bus mode with MPU.

When DL = "Low", it means 4 – bit bus mode with MPU. So to speak, DL is a signal to select 8 - bit or 4 - bit bus mode. When 4 - bit bus mode, it needs to transfer 4 - bit data by two times. N: Display line number control bit

When N = "Low", it means 1 - line display mode.

When N = "High", 2 – line display mode is set.

#### F: Display font type control bit

When F = "Low", it means 5 X 8 dots format display mode

When F = "High", 5 x11 dots format display mode.

#### Set CG RAM Address

				DB5		-			-
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Set CGRAM address to AC.

This instruction makes CGRAM data available from MPU.

#### **Set DD RAM Address**

		-			DB4	-			
0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0

Set DDRAM address to AC.

This instruction makes DDRAM data available from MPU.

When 1 - line display mode (N = 0), DDRAM address is from "00H" to "4FH".

In 2 – line display mode (N = 1), DDRAM address in the 1st line is from "00H" to "27H", and DDRAM address in the 2nd line is from "40H" to "67H".



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
roduct specification	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	18/28

#### **Read Busy Flag and Address**

RS			-	-		-			-
0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0

This instruction shows whether ST 7066 is in internal operation or not. If the resultant BF is High, it means the internal operation is in progress and you have to wait until BF to be Low, and then the next instruction can be performed. In this instruction you can read also the value of address counter.

#### Write Data to RAM

	R/W		-	-		-			-
1	0	D7	D6	D5	D4	D3	D2	D1	D0
TTT 1 1	• •	1 . 1 .		AN C / C					

Write binary 8 – bit data to DDRAM / CGRAM.

The selection of RAM from DDRAM, CGRAM, is set by the previous address set instruction: DDRAM address set, CGRAM address set. RAM set instruction can also determine the AC direction to

RAM. After write operation, the address is automatically increased / decreased by 1, according to the entry mode.

#### **Read Data to RAM**

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0
 <b>1</b> 1.	0	1 . 1 .	C DI		CODA	1			

Read binary 8 – bit data from DDRAM / CGRAM.

The selection of RAM is set by the previous address set instruction. If address set instruction of RAM is not performed before this instruction, the data that read first is invalid, because the direction of

AC is not determined. If you read RAM data several times without RAM address set instruction before read operation, you can get correct RAM data from the second, but the first data would be incorrect, because there is no time margin to transfer RAM data.

In case of DDRAM read operation, cursor shift instruction plays the same role as DDRAM address set instruction : it also transfers RAM data to output data register. After read operation address counter is automatically increased/decreased by 1 according to the entry mode. After CGRAM read operation, display shift may not be executed correctly.

NOTE: In case of RAM write operation, after this AC is increased / decreased by 1 like read operation. In this time, AC indicates the next address position, but you can read only the previous data by read instruction.



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
i foddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	19/28

## **DD RAM ADDRESSING**

#### For 16 × 1 or 8 × 1 Display

101 10 101	-															
Character	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DD RAM Address	00	01	02	03	04	05	06	07	40	41	42	43	44	45	46	47

#### For $16 \times 2$ or $8 \times 2$ Display

	<u> </u>		Jing													
Character	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DD RAM	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	<b>0</b> E	0F
Address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	<b>4</b> E	4F

#### For 16 × 4 Display

Character	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	<b>0</b> E	0F
DD RAM	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F
Address	10	11	12	13	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F
	50	51	52	53	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F

#### For $20 \times 2$ Display

Character	1	2	3	4	5	6	7	8	9	10	 	17	18	19	20
DD RAM	00	01	02	03	04	05	06	07	08	09	 	10	11	12	13
Address	40	41	42	43	44	45	46	47	48	49	 	50	51	52	53



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
I foddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	20/28

#### For 20 × 4 Display

Character	1	2	3	4	5	6	7	8	9	10	 	17	18	19	20
	00	01	02	03	04	05	06	07	08	09	 	10	11	12	13
DD RAM	40	41	42	43	44	45	46	47	48	49	 	50	51	52	53
Address	14	15	16	17	18	19	1A	1B	1C	1D	 	24	25	26	27
	54	55	56	57	58	59	5A	5B	5C	5D	 	64	65	66	67
	54	55	56	57	58	59	5A	5B	5C	5D	 	64	65	66	67

#### For 40 × 2 Display

Character	1	2	3	4	5	6	7	8	9	10	 •••	37	38	39	40
DD RAM	00	01	02	03	04	05	06	07	08	09	 	24	25	26	27
Address	40	41	42	43	44	45	46	47	48	49	 	64	65	66	67

#### For 40 × 4 Display

Character	Е	1	2	3	4	5	6	7	8	9	10	 	37	38	39	40
	E1	00	01	02	03	04	05	06	07	08	09	 	24	25	26	27
DD RAM	EI	40	41	42	43	44	45	46	47	48	49	 	64	65	66	67
Address	E2	00	01	02	03	04	05	06	07	08	09	 	24	25	26	27
	ΕZ	40	41	42	43	44	45	46	47	48	49	 	64	65	66	67



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
I foddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	21 / 28

# CG RAM MAPPING

		Cha DD							C	G R	AM	1 Ac	ldre	ess					ncte RA					
7 Hig	6 gh	5	4	3	2	1 I	0 Low		5 Hig	4 gh	3	2	1 L	0 .ow		7 Hig	6 gh	5	4	3	2	1 L	0 .ow	
0	0	0	0	*	0	0	0		0	0	0	0 0 0 1 1 1 1	0 0 1 0 0 1 1	0 1 0 1 0 1 0 1		*	*	*	0 1 0 1 0 0 0 0	1 0 1 1 0 0 0 0	1 0 1 0 1 0 0 0 0	0 1 0 1 0 0 0 0 0	0 0 0 0 0 0 0 0	←Character Pattern ←Cursor
0	0	0	0	*	0	0	1		0	0	1	0 0 0 1 1 1 1	0 0 1 0 0 1 1	0 1 0 1 0 1 0 1		*	*	*	1 1 1 1 1 1 0	1 0 0 0 0 0 1 0	1 0 1 1 0 1 0	1 0 1 0 0 1 1 0	1 1 1 1 1 1 1 0	←Character Pattern ←Cursor
	•				• • • • •	•		• • • • •			• • • •			• • • • • • •	• • • • • • • • •	•	•	• • • • •			• • • • •	• • • • •	••••••	
0	0	0	0	*	1	1	1		1	1	1	0 0 0 1 1 1 1 1	0 0 1 1 0 0 1 1	0 1 0 1 0 1 0 1		*	*	*	$     \begin{array}{c}       1 \\       1 \\       1 \\       1 \\       1 \\       1 \\       1 \\       0 \\       \end{array} $	1 0 1 0 0 0 1 0	1 0 1 0 1 0 1 0	1 0 0 1 0 1 0	1 1 1 1 1 1 1 0	←Character Pattern ←Cursor

Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
I foddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	22 / 28

# **CHARACTER FONT TABLE**

<b>N N</b>																
Upper 4 bit Lower 4 bit	LLLL	LLLH	LLHL	LLHH	LHLL	LHLH	LIHHL	LHIHH	HILLL	HLLH	HLHL	HLHH	HIHILL	HHLH	нны	нннн
1111																
1111																
LLHL																
LLHH																
lHLL																
lHLH																
lHHL																
гннн																
HLLL																
HLLH																
HLHL																
нгнн																
HHLL																
ннін																
нннг																
нннн																



Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
I foddet Speemeation	Model	NWITC-520200AR0115-10D	A	Sept. 06, 17	23 / 28

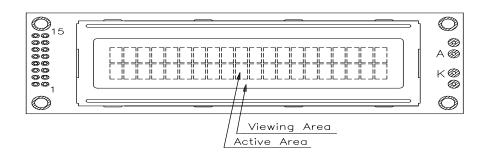
## **RELIABILITY TEST**

No	Item	Conditions		Note
1.	High Temp. Operation	70°C	240 HR	
2.	High Temp. Storage	80°C	240 HR	
3.	Low Temp. Operation	-20°C	240 HR	
4.	Low Temp. Storage	-30°C	240 HR	
5.	High Temp. / Humid Storage	40°C 90% RH	240 HR	
6.	Thermal Shock	0°C, 30min +50°C, 30min	10 cycles	
7.	Vibration Test $(IEC - 68 - 2 - 6)$	Frequency: 10 ~ 55 Hz Duration: 20 times, 6 min / time Amplitude: 0.75 mm		
8.	Shock (IEC 68 – 2 – 27)	Duration: 11 mS Acceleration: 100g		X, Y, Z direction

# **APPEARANCE CHECK**

CONDIITON OF APPEARANCE CHECK:

(1)Specimen shall be checked by eyes in distance of 30cm under 40w – fluorescence lamp. (2)Checking direction shall be in 45 degree from perpendicular line op specimen surface.





Messrs.					
Product Specification	Model	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	widdei		A	Sept. 06, 17	24 / 28

## HANDLING PRECAUTIONS

- (1) Treat polarizer very carefully since it is easy to be damaged.
- (2) When cleaning the display surface, use soft cloth (e.g. gauss) with a solvent (recommended below) and wipe lightly.
  - $\blacklozenge$  ethyl alcohol
  - ♦ iso prcolol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvents:

- water
- ♦ ketone
- aromatics
- (3) Direct current causes electro-chemical reaction with remarkable degradation of the display quality. Give careful consideration to prevent direct current at ON / OFF timing and during operation.
- (4) Avoid strong shock and drop from the height.
- (5) To prevent LCD panels from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.
- (6) Give careful consideration to avoid electrical static discharge with causes uneven contrast.
- (7) Even a small condensation on the contact pads (terminals) causes electro chemical reaction which makes missing row and column. Give careful attention to avoid condensation. When assembling with zebra connector, clean the surface of the pads with alcohol and keep the air very clean.



Messrs.					
Product Specification	Modal	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	Model		A	Sept. 06, 17	25 / 28

# **LCD PRODUCT QUALITY STANDARD**

	DISPLAY APPEARANCE					
No	Item	Criteria				
1.	Inclusions (Black spot, White spot, dust)	(1) Round type Diameter mm (a*) no of defect* $a \le 0.20$ neglect $0.20 < a \le 0.35$ 5max 0.35 < a none (2) Linear type Length mm (1) width mm (W) no. of defect na W \le 0.03 neglect $1 \le 3$ $0.03 < W \le 0.08$ 6 3 < 1 $0.08 < W$ none				
2.	Scratch	<ol> <li>Scratch on protective film is permitted.</li> <li>Scratch on polarizer shall be as follow:         <ol> <li>Round type</li> <li>Diameter mm (a*) no of defect</li> <li>a≤0.15 neglect</li> <li>0.15 &lt; a≤0.20 2 max</li> <li>0.20 &lt; a none</li> <li>Linear type</li> <li>be judged bye 1. – (2) linear type</li> </ol> </li> </ol>				
3.	Dent	Diameter < 1.5mm				
4.	Bubble	Not exceeding 0.5mm average diameter is acceptable between glass and polarizing film				
5.	Pin hole	$(a + b)/2 \leq 0.15$ mm Maximum number: ignored $0.15 < (a + b)/2 \leq 0.20$ mm Maximum number: 10				



Messrs.					
Product Specification	Modal	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	WIGGET		A	Sept. 06, 17	26/28

No	Item	Criteria		
6.	Dot defect	$(a + b)/2 \le 0.20$ mm Maximum number: ignored $0.20 < (a + b)/2 \le 0.30$ mm Maximum number: 5 X = Width		
7.	Contrast irregularity (Spot)	$\begin{array}{llllllllllllllllllllllllllllllllllll$		
8.	Dot width	Design width $\pm 15\%$		
9.	Color tone and uniformity	Obvious uneven color is not permitted		



Messrs.					
Product Specification	Madal	NMTC-S20200XRGHS-10B	Rev. No.	Issued Date.	Page.
	Model		A	Sept. 06, 17	27 / 28

# WARRANTY

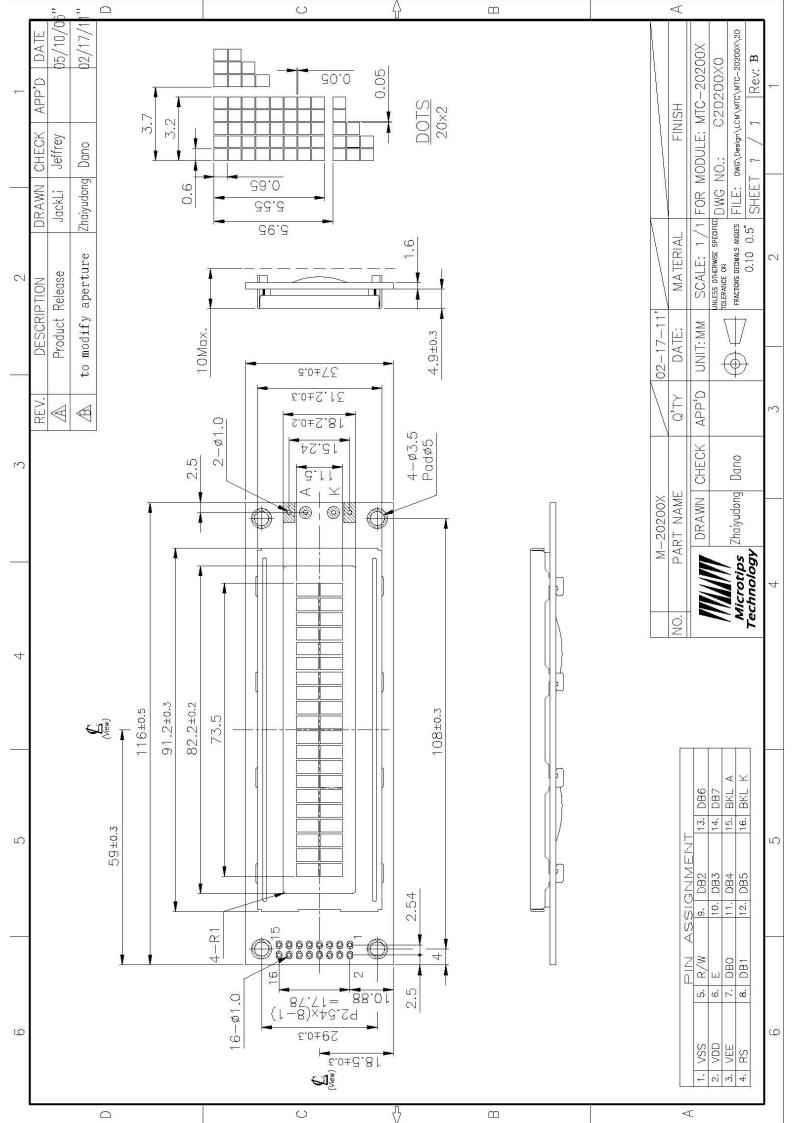
This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1 13-month guarantee starts from the date code.
- We cannot accept responsibility for any defect, which may arise from additional 2 manufacturing of the product (including disassembly and reassembly), after product delivery.
- 3 We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 4 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 5 We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

## **DIMENSIONAL OUTLINES**

See the next page.....





# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for LCD Character Display Modules & Accessories category:

Click to view products by Microtips manufacturer:

Other Similar products are found below :

HG9Z-AC501 HG9Z-SCI25B LCM-S01602DTRA-3 HG9Z-XC213 EA FL-10P MDLS-81809-LV-GLED4G MDLS-20265-LV-GLED4G NMTC-S16205DFYHSAY-22 NHD-0420AZ-FSW-GBW-33V33-0 CM1644-SGR1 162A BC BW LCM-S02002DSM-Y 162D BA BC HDM24216H-2-P9XS DEM 16207 SYH-PY RC0802A1-LLG-JWVE RC0802A1-LLR-JWVE RC0802A-TIW-ESV RC1202A-BIY-CSX RC1202A-BIY-ESX RC1602A-BIW-CSV RC1602A-YHW-CSV RC1602B-GHY-CSXD RC1602B-TIW-CSV RC1602B-YHY-CSVD RC1602D-GHY-ESX RC2002A2-LLB-JSVE RC2002A-GHG-CSV RC2004A-BIW-ESX RC2004A-GHW-CSV RC2004A-LLB-JSVE RC2002A-GHG-CSV RC2004A-BIW-ESX RC2004A-GHW-CSV RC2004A-LLB-JSVE RC2004A-LLH-JSV RC2004A-LLR-JSVE DEM 08171 SYH-LY DEM 08172 SYH-PY DEM16101SYH-LY DEM 16209 SBH-PW-N DEM 16210 SGH DEM 16214 FGH-PW DEM 16216 FDH-P(RGB)-N DEM16216SYH-LY DEM 16217 SYH-PY DEM 16221 SYH DEM 16227 FGH-PW DEM 16227 SBH-PW-N DEM 16227 SYH-LY DEM 20231 SYH-PY-CYR22 DEM 20232 SBH-PW-N DEM 20233 SYH-LY