

Model No: WT ÙPË Í JÎ T ÖËH VÁÇÜÒXÖD

Approved By						

Tel: 1 (888) 499-8477

Fax: (407) 273-0771

E-mail: mtusainfo@microtipsusa.com

Web: www.microtipsusa.com

		Revision record	
Document	Model No.	D ' ' '	Revision
Revision	Version No.	Description	by
	UMSH-8596MD-T		Y.D. Shie
0	(UFSH-K106EY-FT)	7.0" TFT.(backlight luminance 300cd/m²)	Zi Xin Ou
	Version No. 0	,	25-May-2011
1	UMSH-8596MD-1T		Y.D. Shie
1		Modify the backlight luminance from $300cd/m^2$ to $500cd/m^2$.	Danny Jhuang
	Version No. 0		11-Aug-2014
		1.Add the touch panel IC to ST1633i.	H.F. Kuo
2	UMSH-8596MD-34T	2.Modify the module number from	Zi Xin Ou
_	Version No. 0	UMSH-8596MD-1T to UMSH-8596MD-34T.	30-Apr-2015
			50 11p1 2010
		1. Change the TFT panel and backlight.	H.F. Kuo
3	UMSH-8596MD-34T(REVD)	Modify the module number from	Zi Xin Ou
	Version No. 0	UMSH-8596MD-34T to UMSH-8596MD-34T(REVD).	11-Dec-2015
		, ,	
	<u> </u>		
////// Mic	erotips Davidian 2 . In.	1011 0506MD 24T/DEVD) V 0 . D 11 0015	Dogg. 2
TECHNO	$\frac{1}{0 \log Y}$ Revision 3; UN	ISH-8596MD-34T(REVD) Ver. 0; December-11-2015	Page: 2
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1. BASIC SPECIFICATION

1.1 Mechanical specifications

Items	Nominal Dimension	Unit
Active screen size	7.0" Diagonal	-
Dot Matrix	800 x RGB x 480	Pixel
Module Size (W x H x T)	165.0 x 106.4 x 8.2	mm.
Active Area (W x H)	152.4 x 91.44	mm.
Pixel Size (W×H)	0.1905 x 0.1905	mm.
Color depth	262K	color
Interface	Parallel 18-bit RGB	-
Driving IC Package	COG	-
Module weight	195±10%	g

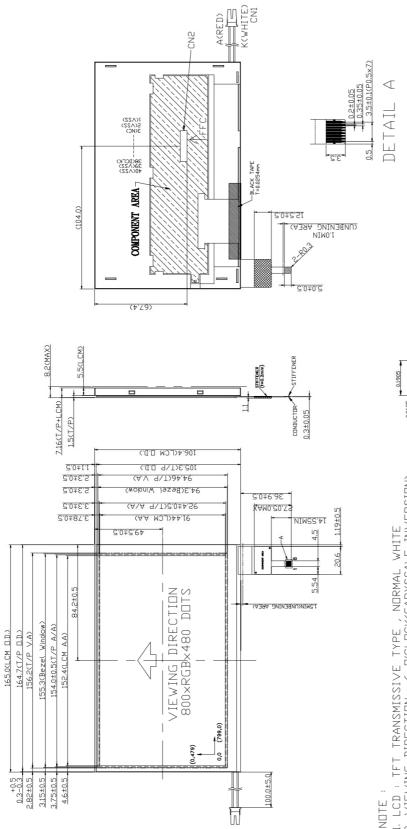
1.2 Display specification

Display	Descriptions	Note
LCD Type	a-Si TFT	-
LCD Mode	TN / Normal white	-
Polarizer Mode	Transmissive	-
Polarizer Surface	Normal	-
Pixel arrangement	RGB-stripe	-
Backlight Type	LED	-
Viewing Direction(Gray inversion)	6 O'clock Direction	1

Color tone is slightly changed by temperature and driving voltage.

Note 1: The viewing direction defined in this specification is according to the rubbing direction of its TFT surface treatment by the TFT glass manufacturer. The grayscale inversion is at this direction as well. However, the optimal viewing direction for human view is normally where the color does NOT change to grayscale inversion, and this would be the opposite site of the specified viewing direction in this specification. In any case we advise customers to judge by themselves, and be aware of this phenomenon.

1.3 Outline dimension



1. LCD : TFT TRANSMISSIVE TYPE , NDRMAL WHITE
2. VIEWING DIRECTION : 6 D'CLOCK(GARYSCALE INVERSION)
3. TOP : -20-70°C , Tst : -30-80°C
4. LED COLOR : WHITE , 27 PCS DICE
CONSTANT CURRENT : 210mA ; Vled=9.9V(TYP)

Touch panel PIN No. | PIN Name

VDD GND GND

RST SCL GND SDA

DOTS DETAIL

K C B S

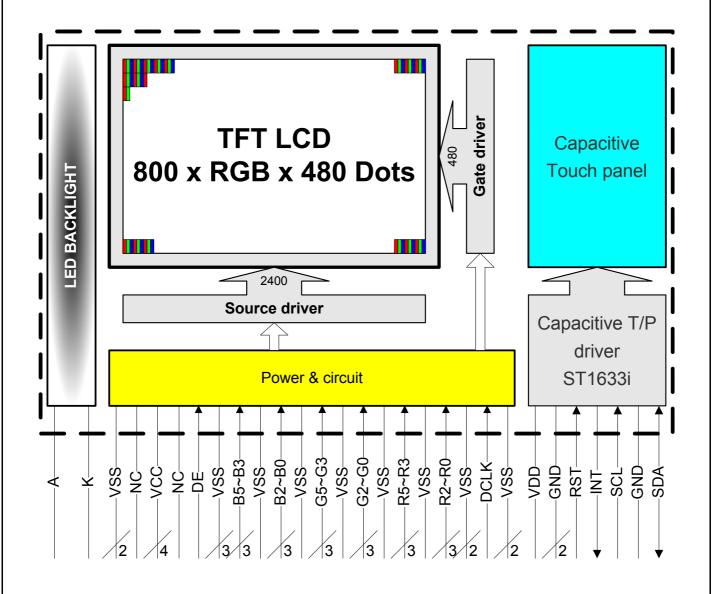
GENERAL TOLERANCE:±0.3 CN1:BHSR-02VS-1(JST) OR EQUIVALENT

CN2:IMSA-9681S-40Y901(IRISD) DR EQUIVALENT PROJECTIVE CAPACITIVE TYPE TOUCH PANNEL 9.01

THE MINIMUM BENDABLE RADIUS(INNER) OF THE FPC IS 1.0 mm T/P INTERFACE: I2C; IC : ST1633; ; FIVE PDINT TDUCH THE METAL FRAME OF LCM DO NOT NEED COMMON-GROUNDED

COMPONENT AREA CAN NOT BENDING,

1.4 Block diagram:



1.5 Interface Pin Connection:

Pin No.	Pin Symbol	1/0	Description
1~2	VŠS	Р	GND
3	NC	-	No connection
4~7	VCC	Р	Power supply for Module (+3.3V)
8	NC	-	No connection
9	DE		Data enable
10~12	VSS	Р	GND
13~15	B5~B3		Blue data input
16	VSS	Р	GND
17~19	B2~B0		Blue data input
20	VSS	Р	GND
21~23	G5~G3		Green data input
24	VSS	Р	GND
25~27	G2~G0		Green data input
28	VSS	Р	GND
29~31	R5~R3		Red data input
32	VSS	Р	GND
33~35	R2~R0		Red data input
36~37	VSS	Р	GND
38	DCLK		Dot clock
39~40	VSS	Р	GND

B/L interface pin:

Pin No.	Pin Symbol	1/0	Description			
1	Α	Р	Power supply for LED+			
2	K	Ρ	Power supply for LED-			

Capacitive touch panel (I2C) Interface:

Pin No.	Pin Symbol	1/0	Description
1	VDD	Р	Power supply. (+3.3V)
2~3	GND	Р	Ground.
4	RST		System reset signal input, active low. Note (1)
5	INT	0	Active low when data output from touch panel.
6	SCL		Serial Clock.
7	GND	Ρ	Ground.
8	SDA	1/0	Serial data access.

Note(1): Power On/Off Sequence.

Reset pin should be held low before power on and power off. During power on, after VDD reach normal voltage, Reset pin needs to be held low for 5ms to ensure internal block stable.

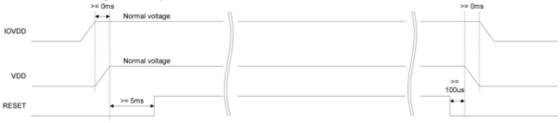


Figure - Power On/Off Sequence

Note(2): SDA / SCL pins must connect to 4.7Kohm(Pull-up resistor) at host



2. ELECTRICAL CHARACTERISTICS

2.1 Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit
Power supply voltage	VCC	-0.3	7.0	V
rower suppry voltage	VDD	-0.3	6.0	V
Input voltage	Vin	-0.3	VCC+0.3	V
Operate temperature range	Тор	-20	70	.C
Storage temperature range	Тѕт	-30	80	.C



2.2 DC Characteristics

 $T_a=25^{\circ}C$

Items	Symbol	Min.	Typ.	Max.	Unit	Condition
Overal versities as	VCC	-	3.3	-	٧	-
Supply voltage	VDD	-	3.3	-	٧	-
Input Voltage	VIL	0	-	0.3VCC	٧	L level
(LCM)	ViH	0.7VCC	-	VCC	٧	H level
Input Voltage	VIL	-	-	0.15VDD	٧	L level
(CTP)	Viн	0.85VDD	-	-	٧	H level
0	lvcc	-	160	250	mΑ	Note 1
Current consumption	lvdd	-	14	28	mΑ	-

*Note1:

Measuring Condition:

Standard Value MAX.

 $Ta = 25^{\circ}C$

VCC - GND = 3.3V

Display Pattern



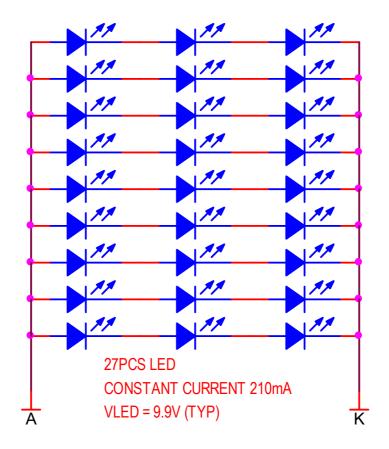
0 gray black pattern

2.3 Back-light only Specification:

PARAMETER	SYMBOL	MIN	TYP	MAX	Unit	Test Condition	NOTE
Supply Current	If	-	210	-	mA	Ta=25°C	-
Supply Voltage	Vf	-	9.9	-	V	Ta=25°C	-
Half-Life Time	Lf	-	50000	-	hrs	Ta=25°C	1

Note 1: The "Half-Life Time" is defined as the LED chip brightness decreases to 50% than original brightness, Based on Ta 25±2°C,60±10% RH condition.

Note 2: LED backlight is 27 LEDs.





2.4 AC Characteristics

Switching characteristics

PARAMETER	Symbol		Unit		
TANAMETER	Symbol	Min.	Тур.	Max.	Onic
Data setup time	T_{dsu}	6			ns
Data hold time	T_{dhd}	6	1	<u> 2001</u>	ns
DE setup time	T _{esu}	6	-	5 	ns
Source output settling time	T _{ST}	-	-	15	μs
Source output loading R	R_{SL}	1	2		K ohm
Source output loading C	C_{SL}	-	60		рF

Parallel RGB Input Timing Requirement

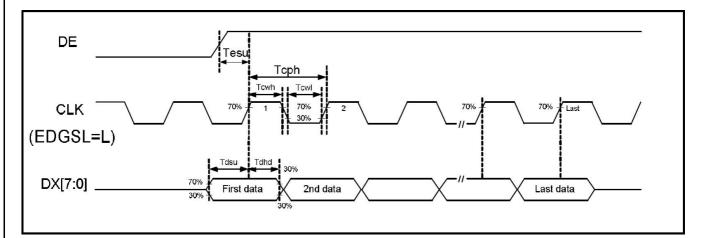
DE mode

PARAMETER	Cymphol		Spec.		11:4
PARAMETER	Symbol	Min.	Тур.	Max.	Unit
CLK frequency	F _{CPH}	-	33.26	₩.	MHz
CLK period	T _{CPH}	= ()	30.06		ns
CLK pulse duty	T_CWH	40	50	60	%
DE period	T _{DEH} +T _{DEL}	1000	1056	1200	T _{CPH}
DE pulse width	T _{DEH}	-	800	=	T _{CPH}
DE frame blanking	T _{DEB}	10	45	110	T _{DEH} +T _{DEL}
DE frame width	T_DE	Î	480	-	T _{DEH} +T _{DEL}

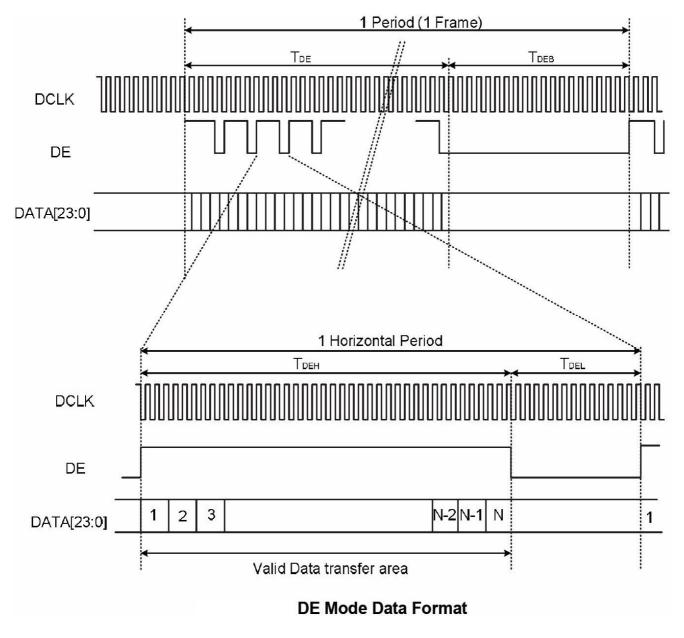
PARAMETER	Symbol			Unit	
PARAMETER	Symbol	Min.	Тур.	Max.	Offic
OEV pulse width	T _{OEV}		150	-	T_{CPH}
CKV pulse width	T _{CKV}	-	133	-	T _{CPH}
DE(internal)-STV time	T ₁	-	4	-	T _{CPH}
DE(internal)-CKV time	T_2	9225 9227	40	=	T _{CPH}
DE(internal)-OEV time	T ₃	3	23	<u> </u>	T _{CPH}
DE(internal)-POL time	T_4	.=	157	-	T_{CPH}
STV pulse width	=	<u> </u>	1	=	T_H

(i). T_{HS}+T_{HA}<T_H

Timing Controller Timing Chart



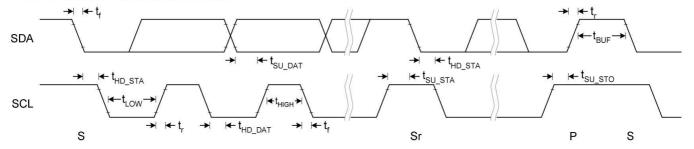
Clock and Data input waveforms



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2-5 Capacitive touch panel controller AC Characteristics

AC Electrical Characteristics



I2C Fast Mode Timing

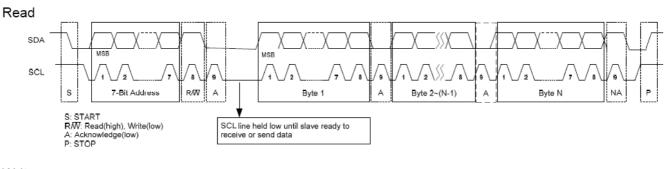
I2C Fast Mode Timing Characteristic

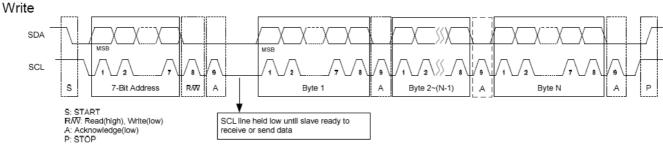
Conditions: VDD = IOVDD = 3.3V, GND = 0V, $T_A = 25$ °C

Symbol	Parameter		Rating		Unit
Cyllibol	T didifficter	Min.	Тур.	Max.	Oint
f_{SCL}	SCL clock frequency	0	-	400	kHz
t_{LOW}	Low period of the SCL clock	1.3			us
t _{HIGH}	High period of the SCL clock	0.6	(=)	-	us
t_f	Signal falling time	-	a==	300	ns
t _r	Signal rising time	1-	i - i	300	ns
t _{SU_STA}	Set up time for a repeated START condition	0.6		-	us
t _{HD_STA}	Hold time (repeated) START condition. After this period, the first clock pulse is generated	0.6	-	-	us
t _{SU DAT}	Data set up time	100	-	-	ns
t _{HD DAT}	Data hold time	0	:=:	0.9	us
t _{su_sto}	Set up time for STOP condition	0.6	1-1	-	us
t _{BUF}	Bus free time between a STOP and START condition	1.3	-	-	us
C _b	Capacitive load for each bus line	-	-	400	pF

2-6 I2C Host Interface Protocol

2.6.1 I2C Slave Interface





Note: I2C address is default to 0x55 (7-bits address)

2.6.2 Register Read

For reading register value from I2C device, host has to tell I2C device the *Start Register Address* before reading corresponding register value.

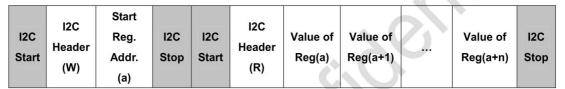


Figure 1 - Register Read Format.

Sitronix Touch IC I2C host interface protocol supports Repeated Register Read. That is, once the Start Register Address has been set by host, consequent I2C Read(R) transactions will directly read register values starting from the Start Register Address without setting address first, as shown in Figure 2.

	I2C	Value			1			I2C	Value			
I2C	Header	of	Value of	•	Value of	I2C	I2C	Header	of	Value of	Value of	I2C
Start	пеацеі		Reg(a+1)		Reg(a+n)	Stop	Start	пеацеі		Reg(a+1)	 Reg(a+n)	Stop
	(R)	Reg(a)	3, ,		,			(R)	Reg(a)	J. ,	3, ,	

Figure 2 - Repeated Register Read.

Header Value(R): 0xab

2.6.3 Register Write

For writing register to I2C device, host has to tell I2C device the Start Register Address in each I2C Register Write transaction. Register values to the I2C device will be written to the address starting from the Start Register Address described in Register Write I2C transaction as shown in Figure 3.

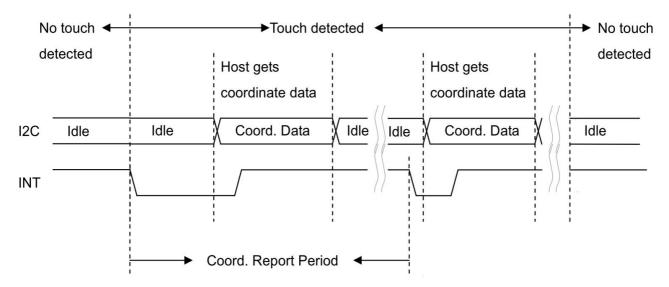
	I2C	Start					
I2C	Header	Reg.	Value to	Value to		Value to	I2C
Start	(W)	Addr.	Reg(a)	Reg(a+1)	•••	Reg(a+n)	Stop
	(**)	(a)					

Figure 3 - Register Write Format.

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Header Value(W): 0xaa

2.6.4 I2C Electrical Waveform



2.6.5 Registers

Sitronix Touch IC provides a register set for host to configure device attributes and retrieve information about Device Control, XY Coordinates through device host interface. Host interface registers are listed below.

			Host Inter	face Registe	rs (Report F	age)			
Reg Addr.	Name	Bit 7	9it 7 Bit 6 Bit 5 Bit 4 Bit 3 Bit 2 Bit 1						
0x00 0x01			Reserved						
0x01	Device Control Reg								Reset (R/W)
0x03	Timeout to Idle Register				Timeout to (R/	Idle (Sec) W)		(,	
0x04 ~ 0x11					Rese	erved			
0x12	XY0 Coord (High Byte)	Valid 0 (RO)		X0_H(R0)		Reserved		Y0_H (R0)	
0x13	X0 Coord (Low Byte)				X0_L	.(R0)			
0x14	Y0 Coord (Low Byte)				Y0_L	.(R0)			
0x15					Rese	erved			
0x16	XY1 Coord (High Byte)	Valid 1 (RO)		X1_H(R0)		Reserved		Y1_H (R0)	
0x17	X1 Coord (Low Byte)		X1_L(R0)						
0x18	Y1 Coord (Low Byte)		Y1_L(R0)						
0x19					Rese	erved			
0x1A	XY2 Coord (High Byte)	Valid 2 (RO)		X2_H(R0)		Reserved		Y2_H (R0)	
0x1B	X2 Coord (Low Byte)				X2_L	.(R0)			
0x1 C	Y2 Coord (Low Byte)				Y2_L	.(R0)			
0x1 D					Rese	erved			
0x1E	XY3 Coord (High Byte)	Valid 3 (RO)		X3_H(R0)		Reserved		Y3_H (R0)	
0x1F	X3 Coord (Low Byte)				X3_L	.(R0)			
0x20	Y3 Coord (Low Byte)		Y3_L(R0)						
0x21			Reserved						
0x22	XY4 Coord (High Byte)	Valid 4 (RO)							
0x23	X4 Coord (Low Byte)	X4_L(R0)							
0x24	Y4 Coord (Low Byte)				Y4_L	.(R0)			

2.6.6 Device Control Register

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0	
0x02	Device Control Reg		Rese	erved		Reserved	Reserved	Power Down (RMV)	Reset (R/W)	

Device Control Register provides device control bits for host to reset the device, power down the device.

2.6.7 Timeout to Idle Register

Reg Addr.	Name	Bit 7	Bit 6	Bit 5	Bit 4	Bit 3	Bit 2	Bit 1	Bit 0
0x03	Timeout to Idle Register		Timeout to Idle (Sec) (RM)						

Timeout to Idle Register provides timeout control to entering Idle Mode for host.

The touch controller will enter Idle Mode after the number of seconds specified in Timeout to Idle Register if there is no touch detected in this period.

Set the field to 0xFF will disable Idle Mode. Set the field to 0 will entering Idle Mode immediately.

The default value of Timeout to Idle Register is set to 0x08 for 8 seconds to Idle Mode.

2.6.8 XY Coordinate Registers

0x12	XY0 Coord (High Byte)	Valid 0 (RO)	X0_H(R0)	Reserved	Y0_H (R0)				
0x13	X0 Coord (Low Byte)	X0_L(R0)							
0x14	Y0 Coord (Low Byte)		Y0_L(RO)						
0x15			Reserved						
0x16 ~ 0x21									
0x22	XY4 Coord (High Byte)	Valid 4 (RO)	X4_H(RO)	Reserved	Y4_H (R0)				
0x23	X4 Coord (Low Byte)	X4_L(R0)							
0x24	Y4 Coord (Low Byte)		Y4_L	_(R0)					

XY Coordinate Registers represent the XY coordinates for each touch point ID.

Valid bit field tells that this point ID is valid and the XY information represents a real touch point on touch sensor.

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3. OPTICAL CHARACTERISTICS

3.1 Characteristics

Electrical and Optical Characteristics

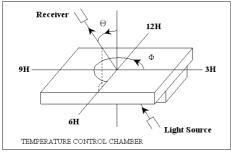
No.	Item	•		symb	ool / temp.	Min.	Тур.	Max.	Unit	Note
1	Response	Time		Tr	25 ℃	-	5	10	ms	2
			Tf	25 ℃	-	15	20	1115	2	
		Hor.		Θ_{2^+}	$\Phi = 0$ °	60	85	-		
2	Viewing	1101.	Center	Θ2-	Φ= 180°	60	85	-	degree	3
2	Angle	Ver.	CR≥10	Θ_{1+}	Ф= 270°	60	85	-	degree]
		V CI.		Θ1-	Ф= 90°	60	85	-		
3	Contrast R	atio		Cr	25 ℃	700	1000	-	-	4
	Red x-cod	e		Rx		0.59	0.64	0.69	<u> </u>	
	Red y-cod	e		Ry		0.29	0.34	0.39	<u></u>	
	Green x-co	ode		Gx		0.28	0.33	0.38		
	Green y-co	ode		Gy		0.58	0.63	0.68		5
4	Blue x-coo	de		Bx	25 ℃	0.10	0.15	0.20	_	
	Blue y-coo	de		Ву		0.00	0.05	0.10	<u></u>	
	White x-co	ode		Wx		0.25	0.30	0.35	<u></u>	
	White y-co	ode		Wy		0.27	0.32	0.37		
	Brightness	3		Y		320	450	-	cd/m ²	
5	Brightness Uniformit				25 ℃	80	-	-	%	6

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3.2 Definition of optical characteristics

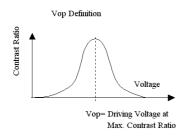
Measurement condition:

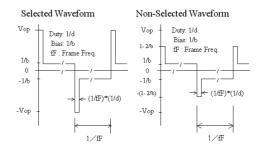
Transmissive and Transflective type



PHOTAL LCD-5000

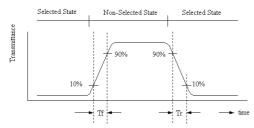
[Note 1] Definition of LCD Driving Vop and Waveform :





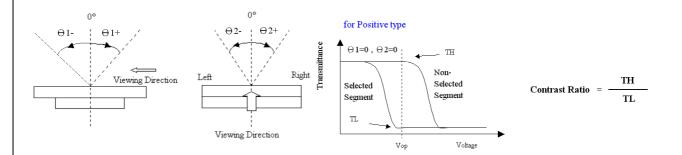
[Note 2] Definition of Response Time

for Positive type

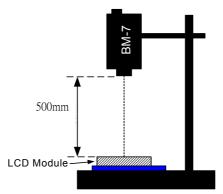


[Note 3] Definition of Viewing Angle:

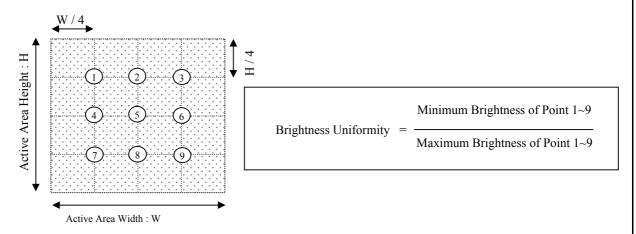
[Note 4] Definition of Contrast Ratio:



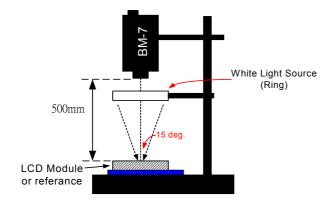
[Note 5] Definition of measurement of Color Chromaticity and Brightness



[Note 6] Definition of Brightness Uniformity



[Note 7] Definition of Measurement of Reflectance





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4. RELIABILITY:

Item No	Items	Condition	Note
1	High temperature operating	70 °C , 200 hours	1
2	Low temperature operating	-20 °C , 200 hours	1
3	High temperature storage	$80~^{\circ}\text{C}$, $200~\text{hours}$	1
4	Low temperature storage	-30 °C , 200 hours	1
5	High temperature & humidity	60℃, 90%RH, 100 hours	2
6	Thermal Shock storage	-30°C, 30min.<=> 80°C, 30min. 10 Cycles	1
7	Vibration test	10 => 55 => 10 => 55 => 10 Hz, within 1 minute Amplitude: 1.5mm. 15 minutes for each Direction (X,Y,Z)	
8	Drop test	Packed, 100CM free fall, 6 sides, 1 corner, 3edges	
9	Life time	50,000 hours 25°C, 60%RH, specification condition driving	

- Note 1: The product move into the room temperature for at least 2 hours with no condensation.
- Note 2: The product move into the room temperature for at least 24 hours with no condensation.
- Note 3: Please change the display picture (autorun) during operating mode. Avoid displaying static images to avoid image sticking, and the image sticking is accelerated by temperature.
 - * One single product test for only one item.
 - * Judgment after test: keep in room temperature for more than 2 hours.
 - Current consumption < 2 times of initial value
 - Contrast > 1/2 initial value
 - Function : work normally



5. PRODUCT HANDLING AND APPLICATION

PRECAUTION FOR HANDLING LCM

- The LCD module contains a C-MOS LSI. People who operate the LCM should wear ESD protection eguipement to prevent ESD hurt on products.
- Do not input any signal before power is turned on.
- Do not take LCM from its packaging bag until it is assembled.
- Peel off the LCM protective film slowly since static electricity may be generated.
- Pay attention to the humidity of the work shop, 50~60%RH is satisfactory.
- Use a non-leak iron for soldering LCM.
- Do not touch the display surface or connection terminals area with bare hands. Smudges on the display surface reduce the insulation between terminals.
- Cautions for soldering to LCM:

Condition for soldering I/O terminals:

Temperature at iron tip :350°C±15°C. Soldering time : $3\sim4$ sec./ terminals.

Type of solder: Eutectic solder(rosin flux filled).

PRECAUTION IN USE OF LCM

- Do not contact or scratch the front surface and the contact pads of a LCM with hard materials such as metal or glass or with one's nail.
- To clean the surface, wipe it gently with soft cloth dampened by alcohol.
- Do not attempt to wiped off the contact pads.
- Keep LCM panels away from direct sunlight, also avoid them in high-temperature & high humidity environment for a long period.
- Do not drive LCM by DC voltage.
- Do not expose LCM to organic solvent.
- Liquid in LCM is hazardous substance. In case a contact with liquid crystal material is occured, be sure to immediately wash such material away by soap and water.
- The polarizer is easily damaged and should be handle with special care. Don't press or rub it with hard objects.

PRECAUTION FOR STORING AND USE OF LCM

- To avoid degradation of the device, do not store the module under the conditions of direct sunlight, high temperature or high humidity. Keep the module in bags designed to prevent static electricity charging under low temperature / normal humidity conditions(avoid high temperature / high humidity and low temperature below 0° C)
- Never use the LCD, LCM under 45 Hz, the liquid crystal will decomposition and cause permently damage on display!!

☐ USING ON MEDICAL CARE, SAFETY OR HAZARDOUS APPLICATION OR SYSTEM

- For the application in medical care, safety and hazardous products or systems, an authorization from MT is required. MT will not responsible for any damage or loss which caused by the products without any authorization given by URT.
- This product is not allowed to be designed and used for military application and/or purpose.
- The delivery of this product to the countries and/or regions where the embargoes are imposed by U.N. is prohibited.
- The application and delivery of this product must comply with Startegic High-Tech Commodities (SHTC) export control and the sales to the embargoed and/or sanctioned countries or regions are strictly prohibited.



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☐ USING ON MEDICAL CARE, SAFETY OR HAZARDOUS APPLICATION OR SYSTEM

- For the application in medical care, safety and hazardous products or systems, an authorization from MT is required. MT will not responsible for any damage or loss which caused by the products without any authorization given by MT.
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- The delivery of this product to the countries and/or regions where the embargoes are imposed by U.N. is prohibited.
- The application and delivery of this product must comply with Startegic High-Tech Commodities (SHTC) export control and the sales to the embargoed and/or sanctioned countries or regions are strictly prohibited.

□ RoHS COMPLIANCE

• This product is RoHS compliance. Follow RoHS Directive 2011/65/EU.

Pb	< 1000 ppm	Pb	: Lead
Cd	< 100 ppm	Cd	: Cadmium
Hg	< 1000 ppm	Hg	: Mercury
Cr ⁶⁺	< 1000 ppm	Cr^{6+}	: Chromium VI

PBB < 1000 ppm PBB : Polybrominated biphenyls
PBDE < 1000 ppm PBDE : Polybrominated biphenyl ethers



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6. DATE CODE OF PRODUCTS

• Date code will be shown on each product :

• YY MM DD - XXXX

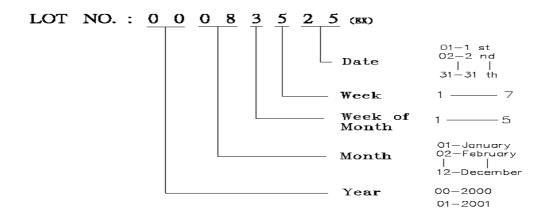
Year Month Day - Production lots no.

• Example: 141108 - 0 0 0 3 ==> Year 2014, November,8th, Production lots no.0003

Note: The lot no. attached on the packing box will be used for tracking once the part is too small to print the date code.

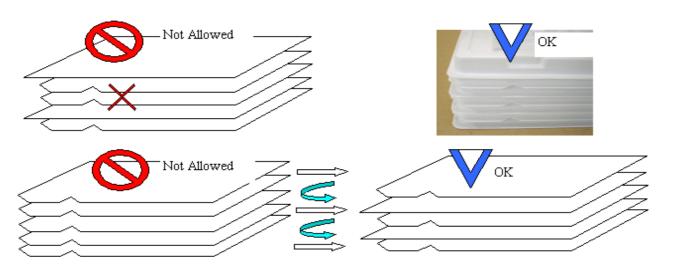
7. PACKING

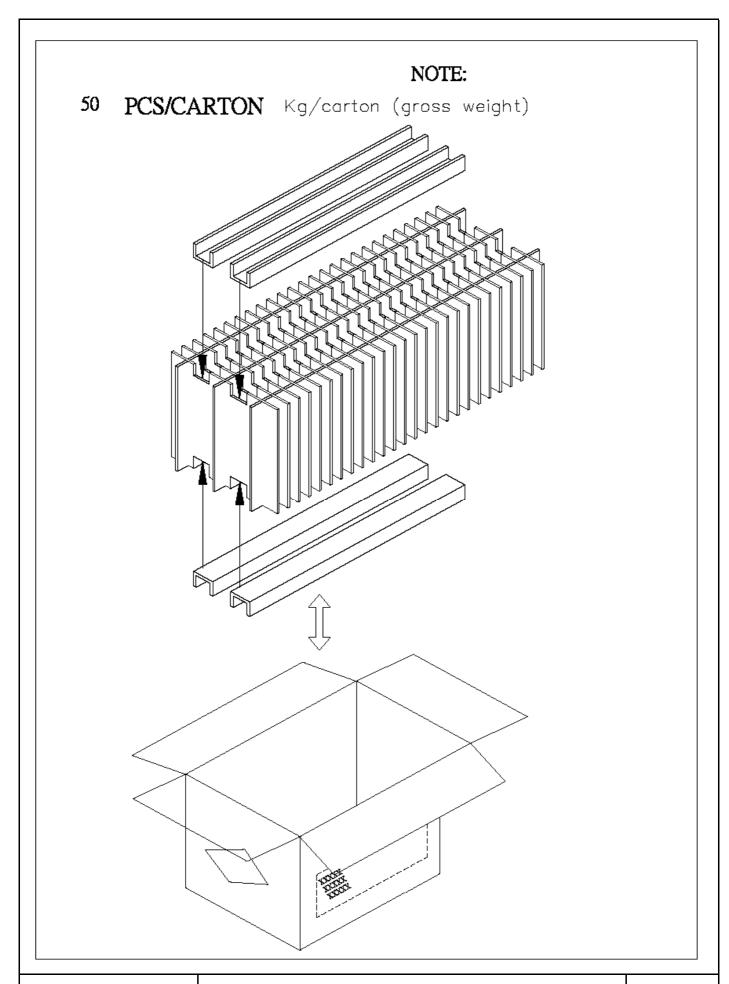
Instruction of lot number:



Lable of carton:

Packing tray must be stacked with alternated direction to each others. To tacks packing trays in same direction will cause product damaged.





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8. INSPECTION STANDARD

8.1. QUALITY:

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

8.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM MT TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 $^{\circ}$ C TO 40 $^{\circ}$ C ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

8.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION, A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E), LEVEL II SINGLE PLAN.

CLASS	AQL(%)
CRITICAL	0.4 %
MAJOR	0.65 %
MINOR	1.5 %
TOTAL	1.5 %

EVERY ITEM SHALL BE INSPECTED ACCORDING TO THE CLASS.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION, A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

8.1.3. WARRANTY POLICY

MT WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION

OPERATING CONDITIONS. MT WILL REPLACE NEW PRODUCTS FOR THESE DEFECT PRODUCT WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF MT.

8.2. CHECKING CONDITION

- **8.2.1.** CHECKING DIRECTION SHALL BE IN THE 45 DEGREE AREA FROM VIEWING DIRECTION.
- **8.2.2.** CHECKER SHALL SEE OVER 300±25 mm. WITH BARE EYES FAR FROM SAMPLE AND USING 2 PCS. OF 20W FLUORESCENT LAMP.

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8.3. INSPECTION PLAN:

	1		
CLASS	ITEM	JUDGEMENT	CLASS
PACKING &	1. OUTSIDE AND INSIDE PACKAGE	"MODEL NO.", "LOT NO." AND "QUANTITY" SHOULD INDICATE ON THE PACKAGE.	Minor
INDICATE	2. MODEL MIXED AND QUANTITY	OTHER MODEL MIXEDREJECTED QUANTITY SHORT OR OVERREJECTED	Critical
	3. PRODUCT INDICATION	"MODEL NO." SHOULD INDICATE ON THE PRODUCT	Major
		ACCORDING TO SPECIFICATION OR DRAWING.	Major
	5. VIEWING AREA	POLARIZER EDGE OR LCD'S SEALING LINE IS VISABLE IN THE VIEWING AREAREJECTED	Minor
APPEARANCE	6. BLEMISH、BLACK SPOT、 WHITE SPOT IN THE LCD AND LCD GLASS CRACKS	ACCORDING TO STANDARD OF VISUAL INSPECTION (INSIDE VIEWING AREA)	Minor
	7. BLEMISH、BLACK SPOT WHITE SPOT AND SCRATCH ON THE POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION (INSIDE VIEWING AREA)	Minor
	8. BUBBLE IN POLARIZER	ACCORDING TO STANDARD OF VISUAL INSPECTION (INSIDE VIEWING AREA)	Minor
	9. LCD'S RAINBOW COLOR	STRONG DEVIATION COLOR (OR NEWTON RING) OF LCDREJECTED. OR ACCORDING TO LIMITED SAMPLE (IF NEEDED, AND INSIDE VIEWING AREA)	Minor
	10. ELECTRICAL AND OPTICAL CHARACTERISTICS (CONTRAST \ VOP \ CHROMATICITY ETC)	ACCORDING TO SPECIFICATION OR DRAWING . (INSIDE VIEWING AREA)	Critical
ELECTRICAL	11.MISSING LINE	MISSING DOT · LINE · CHARACTERREJECTED	Critical
	12.SHORT CIRCUIT、 WRONG PATTERN DISPLAY	NO DISPLAY · WRONG PATTERN DISPLAY · CURRENT CONSUMPTION OUT OF SPECIFICATION REJECTED	Critical
	13. DOT DEFECT (FOR COLOR AND TFT)	ACCORDING TO STANDARD OF VISUAL INSPECTION	Minor

NO.	CLASS	ITEM	JUDGI	EMENT		
			(A) ROUND TYPE:	unit : mm.		
			DIAMETER (mm.)	ACCEPTABLE Q'TY		
			$\Phi \leq 0.1$	DISREGARD		
			$0.1 < \Phi \leq 0.25$	3(Distance>5mm)		
		BLACK AND WHITE SPOT FOREIGN MATERIEL	0.25 < Ф	0		
3.4.1	MINOR	DUST IN THE CELL	NOTE: $\Phi = (LENGTH + WIDTH)/2$			
		BLEMISH	(B) LINEAR TYPE:	unit : mm.		
		SCRATCH	LENGTH WIDTH	ACCEPTABLE Q'TY		
				≦0.03 DISREGARD		
				≤ 0.07 3(Distance>5mm)		
			0.07< W	FOLLOW ROUND TYPI		
				•.		
			DIAMETER	unit : mm.		
	MINOR	BUBBLE IN POLARIZER	DIAMETER <0.2	ACCEPTABLE Q'TY		
8.4.2		DENT ON POLARIZER	$ \begin{array}{ccc} \Phi & \leq 0.2 \\ \hline 0.2 < \Phi & \leq 0.5 \end{array} $	DISREGARD 2(Distance>5mm)		
				2(Distance>3mm) 0		
			<u>0.5 < Φ</u>	Ü		
8.4.3	MINOR	Dot Defect	Pixel Define: Pixel Pixel	B ze of a defective dot over as one defective dot. at and unchanged in size aying under black pattern.		
TEC	Microtips	Revision 3; UMSH-8596MD-3	Note 3: Dark dot: Dots appear dark a which LCD panel is displayi ,blue pattern. 4T(REVD) Ver. 0; December-11-2015	_		

NO.	CLASS	ITEM	JUDGEMENT	Γ
8.4.4	MINOR	LCD GLASS CHIPPING	S	Y > S Reject
8.4.5	MINOR	LCD GLASS CHIPPING	SX	X or Y > S Reject
8.4.6	MAJOR	LCD GLASS GLASS CRACK	T	Y > (1/2) T Reject
8.4.7	MAJOR	LCD GLASS SCRIBE DEFECT	$A_{\tau}^{\pm} = A_{\tau}^{\pm} B$	 a> L/3 , A>1.5mm. Reject B: ACCORDING TO DIMENSION
8.4.8	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL AREA)	T	$\Phi = (x+y)/2 > 2.5 \text{ mm}$ Reject
8.4.9	MINOR	LCD GLASS CHIPPING (ON THE TERMINAL SURFACE)	TZX	Y > (1/3) T Reject
8.4.10	MINOR	LCD GLASS CHIPPING	T Z	Y > T Reject

8.5 INSPECTION STANDARD OF TOUCH PANEL

NO.	CLASS	ITEMS		JUDGEMENT				
8.5.1	MAJOR	Touch Panel Crack				Reje	Reject	
8.5.2	MINOR	R Touch Panel Chipping	Corner	X Y T	Not CNC Products CNC Products	X≤2mm, Y≤2mm, Z<1/2T For CNC Outline Dimension	Accept Accept	
			Edge	X Y	Not CNC Products CNC Products	X≤3mm, Y≤3mm, Z<1/2T For CNC Outline Dimension	Accept Accept	
			W≤0.05, L≤10mm		Acce	ept		
8.5.3	MINOR	Scratch Dust and Foreign material (Linear Type)	0.05 mm< $W \le 0.07$ mm ; $L \le 5.0$ mm Distance between seratch > 5.0 mm			Accept 3 ea Max.		
			W>0.07mm		Reje	Reject		
8.5.4	Scratch 5.4 MINOR Dust and Foreign mate (Round Type: Φ =(Length+)		l Foreign material	$\Phi \leq 0.15$ mm 0.15 mm $\Phi \leq 0.25$ mm Distance between seratch $\Phi \leq 0.25$ mm		Accept Accept 5 ea Max.		
				$\Phi > 0.25$ mm		Reje	ct	
		Touch Panel MINOR Dent / Fish Eyes (Φ=(Length+Width)/2)	Φ ≤ 0.35mm		Acce	ept		
8.5.5	MINOR		$0.35 \text{mm} < \Phi \leq 1.0 \text{mm}$ Distance $> 5.0 \text{mm}$		Acce 3 ea	ept Max.		
			$\Phi > 1$.0mm	Reje	ct		
		Touch Panel Air Bubble (Φ=(Length+Width)/2)	$\Phi \leq 0$.	15mm	Acce	ept		
8.5.6	MINOR		0.15mm < Φ		Acce 3 ea	ept Max.		
			$\Phi > 0.2$	25mm	Reje	ct		
	MINOR	MINOR Touch Panel Printing area Scratch	$W \leq 0.03, 1$	L≦10mm	Acce	ept		
8.5.7			$0.03 mm < W \le 0.05 mm, L \le 5 mm$		Acce 3 ea	ept Max.		
			W>0.05m (W>0.05 Follow	um or L>5mm v 8.5.4 Round t	Reje	ect		
8.5.8	MINOR		ouch Panel Jaze Mark / Dust	Can not be removed		Reje	ct	

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