## Features

- Robust package
- Uniform light disbursement
- Ideal for backlighting logos or icons
- Excellent for flush mounting
- Standard configuration: Gray face w/ white segments
- RoHS compliant



## Package Schematics





Notes:

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25(0.01$ ") unless otherwise noted. 2. Specifications are subject to change without notice.

| Absolute Maximum Ratings <br> $\mathbf{( T _ { \mathrm { A } } = \mathbf { 2 5 } ^ { \circ } \mathbf { C } )}$ | Red <br> (GaAsP/ <br> GaP) | Green <br> (GaP) | Unit |  |
| :--- | :---: | :---: | :---: | :---: |
| Reverse Voltage | $\mathrm{V}_{\mathrm{R}}$ | 5 | 5 | V |
| Forward Current | $\mathrm{I}_{\mathrm{F}}$ | 30 | 25 | mA |
| Forward Current (Peak) <br> $1 / 10$ Duty Cycle <br> 0.1 ms Pulse Width | $\mathrm{i}_{\mathrm{FS}}$ | 160 | 140 | mA |
| Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | 75 | 62.5 | mW |
| Operating Temperature | $\mathrm{T}_{\mathrm{A}}$ | $-40 \sim+85$ | ${ }^{\circ} \mathrm{C}$ |  |
| Storage Temperature | $\mathrm{Tstg}^{2}$ | $-40 \sim+85$ |  |  |
| Lead Solder Temperature <br> Below Package Base] | $260^{\circ} \mathrm{C}$ For $3 \sim 5$ Seconds |  |  |  |

A Relative Humidity between $40 \%$ and $60 \%$ is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

| Operating Characteristics ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ) |  | $\begin{gathered} \text { Red } \\ \text { (GaAsP/GaP) } \end{gathered}$ | $\begin{aligned} & \text { Green } \\ & \text { (GaP) } \end{aligned}$ | $\begin{aligned} & \text { Un } \\ & \text { it } \end{aligned}$ |
| :---: | :---: | :---: | :---: | :---: |
| Forward Voltage (Typ.) ( $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ ) | $\mathrm{V}_{\mathrm{F}}$ | 1.9 | 2 | V |
| Forward Voltage (Max.) ( $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ ) | $\mathrm{V}_{\mathrm{F}}$ | 2.3 | 2.4 | V |
| Reverse Current (Max.) ( $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ ) | $\mathrm{I}_{\mathrm{R}}$ | 10 | 10 | uA |
| Wavelength of Peak <br> Emission CIE127-2007* (Typ.) $\left(\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}\right)$ | $\lambda \mathrm{P}$ | 627* | 565* | nm |
| Wavelength of Dominant Emission CIE127-2007* (Typ.) ( $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ ) | 入D | 617* | 568* | nm |
| Spectral Line Full Width At Half-Maximum (Typ.) ( $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ ) | $\triangle \lambda$ | 45 | 30 | nm |
| Capacitance (Typ.) ( $\mathrm{V}_{\mathrm{F}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ ) | C | 15 | 15 | pF |

Luminous Intensity
CIE127-2007*
(IF=10mA) ucd

Wavelength
CIE127-2007*
CIE127-2007* nm $\lambda$ P

Description

|  |  |  | min. | typ. |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| XGURUGX10D | Red | GaAsP/GaP | $\begin{aligned} & 3600 \\ & 900^{*} \end{aligned}$ | $\begin{aligned} & 8990 \\ & 1990^{*} \end{aligned}$ | 627* | 10 Segments <br> Bar graph-Display |
|  | Green | GaP | $\begin{aligned} & 5600 \\ & 1400^{*} \end{aligned}$ | $\begin{aligned} & 11990 \\ & 3990^{*} \end{aligned}$ | 565* |  |

[^0]

* Red

Relative Intensity Vs. CIE Wavelength


## * Green






Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)


Remarks:
If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength), the typical accuracy of the sorting process is as follows:

1. Wavelength: +/-1nm
2. Luminous Intensity / Luminous Flux: $+/-15 \%$
3. Forward Voltage: $+/-0.1 \mathrm{~V}$

Note: Accuracy may depend on the sorting parameters.

## PACKING \& LABEL SPECIFICATIONS




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[^0]:    *Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

