Ceramic

Bandpass Filter

BFCN-7331+

 50Ω 6850 to 7850 MHz

The Big Deal

- LTCC construction
- Temperature stable from -55 to +100°C
- Small size (0.126 x .063 X .037")



Product Overview

The BFCN-7331+ LTCC bandpass filter covers the 6850 to 7850 MHz passband with 1.4 dB passband insertion loss, 23 dB lower stop band rejection, and 20 dB upper stopband rejection. This model handles up to 2.5W RF input power and provides a wide operating temperature range from -55 to +100°C. Utilizing LTCC multi-layer construction, the filter achieves excellent repeatability of performance and comes in a tiny 1206 ceramic package with wraparound terminations, minimizing performance variations due to parasitics and saving space in dense PCB layouts.

Key Features

Feature	Advantages		
LTCC Construction	Provides a rugged package well suited for tough environments such as high humidity and temperature extremes.		
Tiny size (0.126 x .063 x .037")	Saves space in dense circuit boards and minimizes the effects of parasitics.		
Wrap-around terminations	Provides excellent solderability and easy visual inspection		
Wide operating temperature range, -55 to +100°C	Enables reliable performance in extreme environments		

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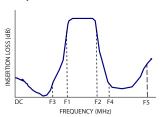
Features

- Small size(0.126 x .063 x .037)
- Temperature stable
- LTCC construction

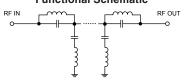
Applications

- · Harmonic rejection
- Transmitters / Receivers
- UWB impulse radar
- · Emission masking

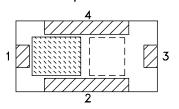
Specification Definition



Functional Schematic



Top View



Pad Connections

Input	1					
Output	3					
Ground	2,4					

BFCN-7331+



Generic photo used for illustration purposes only

CASE STYLE: FV1206-4

+RoHS Compliant

The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications



Electrical Specifications^{1,2} at 25°C

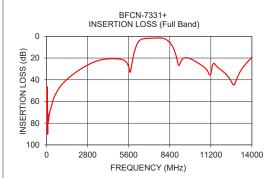
Paran	neter	F#	Frequency (MHz)	Min.	Тур. Мах.		Unit
	Center Frequency	_			7330		MHz
Pass Band	Insertion Loss	F1 - F2	6850 - 7850	_	1.4	3.5	dB
	VSWR	F1 - F2	6850 - 7850	_	1.45	_	:1
Cton Bond Lower	Insertion Loss	DC - F3	5800	10	23	_	dB
Stop Band, Lower	VSWR	DC - F3	5800	_	19	_	:1
Stop Bond Upper	Insertion Loss	F4 - F5	9300 - 13300	10	20	_	dB
Stop Band, Upper	VSWR	F4 - F5	9300 - 13300	_	20	_	:1

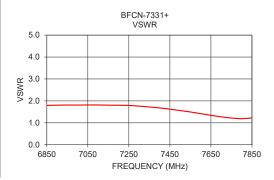
- 1. Measured on Mini-Circuits Characterization Test Board TB-824+.
 2. This filter is not intended for use as a DC Blocking circuit element. In Application where DC voltage is present at either input or output ports, blocking capacitors are required at the corresponding RF port

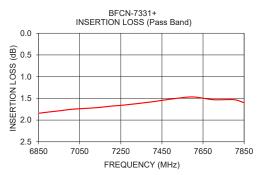
Maximum Ratings

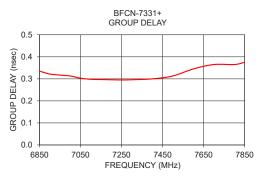
Operating Temperature	-55°C to +100°C
Storage Temperature	-55°C to +100°C
RF Power Input*	2.5 W at 25°C

*Passband rating, derate linearly to 0.7 W at 100°C ambient Permanent damage may occur if any of these limits are exceeded.









Full Band Performance

Pass Band Performance

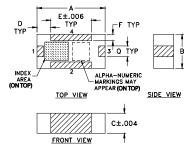
Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)	Frequency (MHz)	Insertion Loss (dB)	Group Delay (nsec)
10	46.84	28.76	6850	1.84	0.34
50	46.46	58.17	6900	1.81	0.32
100	79.43	145.73	6950	1.79	0.32
500	56.33	103.82	7000	1.76	0.31
1000	45.00	82.08	7050	1.74	0.30
2000	33.01	73.35	7100	1.73	0.30
3000	25.04	77.98	7150	1.71	0.30
4000	21.00	77.98	7200	1.68	0.30
5800	26.32	21.94	7250	1.66	0.30
6850	1.84	1.80	7300	1.64	0.30
7350	1.61	1.72	7400	1.58	0.30
7850	1.60	1.23	7500	1.51	0.31
9300	20.59	11.64	7600	1.47	0.35
10500	26.05	37.56	7700	1.53	0.37
13300	30.42	21.10	7800	1.53	0.37
14000	19.49	13.58	7850	1.60	0.38

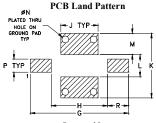
Pad Connections

Input	1
Output	3
Ground	2,4

Product Marking: GS

Outline Drawing



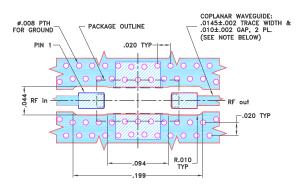


Suggested Layout, Tolerance to be within ±.002

Outline Dimensions (inch)

D E F G H .026 .075 .012 .182 .104 0.66 1.91 0.30 4.62 2.64	.182	.012	.075	.026	.037	.063	A .126 3.20
N P Q R .013 .024 .020 .039 0.33 0.61 0.51 0.99	.039	.020	.024	.013	.039	.041	.119

Demo Board MCL P/N: TB-824+ Suggested PCB Layout (PL-454)



- NOTES:

 1. TRACE WIDTH PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .0066*±.0007*. COPPER: 1/2 0Z. EACH SIDE. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.

 2. BOTTOM SIDE OF THE PCB IS CONTINUOUS GROUND PLANE.
 - - DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER). DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK.

Additional Notes

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp



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