

# Monolithic Amplifier

MNA-3A+

50Ω 0.5 to 2.5 GHz

### THE BIG DEAL

- · Integrated matching, DC Blocks and bias circuits
- · High directivity, 16-25 dB typ.
- Operates over 2.8-5V
- Choice of supply voltage, 2.8V to 5V
- Micro-miniature size .120"X.120"
- Internal DC blocking at RF input and output
- Output power, up to +11.6 dBm typ.
- · Aqueous washable



Generic photo used for illustration purposes only CASE STYLE: DQ849

+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

### **APPLICATIONS**

- Buffer amplifier
- Cellular
- PCN
- · Communications satellite
- Defense

### **PRODUCT OVERVIEW**

MNA-3A+ is a wideband PHEMT based MMIC amplifier with high active Directivity. MNA integrates the entire matching network and majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the MNA amplifier extremely straightforward to use. This design operates on a single 2.8 to 5V supply, is well matched for  $50\Omega$  and comes in a tiny, low profile 3x3mm 8-lead MCLP package accommodating dense circuit board layouts.

### **KEY FEATURES**

Feature	Advantages
Excellent Active Directivity (Isolation- Gain) 16-25 dB	Ideal for use as a buffer amplifier minimizing interaction of adjacent circuits
Integrates DC blocks and RF choke	Minimizes external components, component count and circuit area.
Single 2.8 to +5V operation	Amplifier can be used at low voltage such as +3V or standard +5V. +5V operation results in higher P1dB and OIP3.
3 x 3mm 8-lead MCLP package	Tiny footprint saves space in dense layouts while providing low inductance, repeatable transitions, and excellent thermal contact to the PCB.

REV. A ECO-011187 MNA-3A+ RS/CP 211217





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## MNA-3A+

### **ELECTRICAL SPECIFICATIONS<sup>1</sup> AT 25°C**

Parameter	Condition (GHz)	Min.	Vs=5V Typ.	Max.	Vs=2.8V Typ.	Units
Frequency Range	(G.1.2)	0.5	, , , , , , , , , , , , , , , , , , ,	2.5	0.5-2.5	GHz
requeries runge	0.5	-	12.9		12.0	GITE
	0.75	_	15.5	_	14.4	
	1.0	_	16.4	_	15.2	
Gain	1.5	_	16.8	_	15.5	dB
	2.0	14.9	16.5	18.2	15.2	
	2.5	_	15.5	_	14.2	
	0.5		4.4		4.7	
	0.75		14.1		14.2	
	1.0		24.4		21.5	
Input Return Loss	1.5		16.7		17.0	dB
	2.0		19.4		18.6	
	2.5		14.2		13.8	
	0.5		15.0		13.9	
	0.75		22.3		18.8	
	1.0		30.7		22.4	
Output Return Loss	1.5		25.1		22.9	dB
	2.0		20.2		18.0	
	2.5		18.5		15.0	
	0.5		11.6		10.0	
	0.75		11.6		10.4	
	1.0		11.0		10.4	dBm
Output Power at P1dB	1.5		10.3		9.3	
	2.0		9.5		8.6	
	2.5		9.5		8.4	
	0.5		23.3		21.0	
	0.75		24.1		21.7	
	1.0		22.9		21.0	
Output IP3	1.5		22.0		20.2	dBm
	2.0		21.0		19.4	
	2.5		21.0		19.2	
	0.5		4.5		4.5	
	0.75		4.1		4.2	
	1.0		3.9		4.0	dB
Noise Figure	1.5		3.9		4.0	
	2.0		4.0		4.1	
	2.5		4.1		4.2	
	0.5		25.4		25.9	
	0.75		19.3		20.0	
Directivity	1.0		17.3		18.0	
(Isolation-Gain)	1.5		16.3		16.7	dB
·	2.0		16.8		16.7	
	2.5		18.7		17.7	
DC Current		_	34.3	43	32.6	mA
Device Current Variation vs. Temperature(2)			16		6	μΑ/°C
Device Current Variation vs Voltage			0.0004 <sup>3</sup>		0.00124	mA/mV
Thermal resitance at 85°C			69.9		69.9	°C/W
mermanesitance at 05 C			00.0		J 05.5	C/ VV

<sup>1.</sup> Measured on Mini-Circuits Characterization test board TB-186-3A+. See Characterization Test Circuit (Fig. 1)

<sup>2. (</sup>Current at 85°C -Current at -45°C)/130 3. (Current at 5.25V-Current at 3.9V)/1.35

<sup>4. (</sup>Current at 3.9V-Current at 2.66V)/1.24

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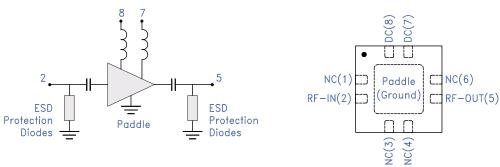
MNA-3A+

### **MAXIMUM RATINGS**<sup>5</sup>

Parameter	Ratings			
Operating Temperature	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			
DC Voltage	7V at pad 7 (on TB-186-3A+) 1V at pads 2 & 5			
Power Dissipation	650 mW			
Input Power	6 dBm (continuous operation) 28 dBm (5 minutes max)			

<sup>5.</sup>Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation

### SIMPLIFIED SCHEMATIC AND PAD DESCRIPTION



Function	Pad Number	Description (See Fig 1)	
RF IN	2	RF input pin	
RF-OUT	5	RF output pin	
DC	7,8	DC Bias pads 7,8. Pad 7 connected to ground via 1000 pF. Pad 8 connected to pad 7 via 33 ohms.	
NC	1,3,4,6	Not Connected, connect pad 3 and 4 to ground externally	
GND	Paddle	Ground	
OPTIONAL	1,6	No internal connection; recommended use: per PCB Layout PL-078	



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MNA-3A+

### **CHARACTERIZATION & APPLICATION TEST CIRCUIT**

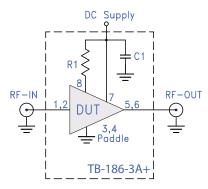


Fig 1. Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization test board TB-313-3A+)

Gain, Return loss, Output power at 1dB compression (P1 dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

#### Conditions:

- 1. Gain and Return loss: Pin= -25dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, -5dBm/tone at output.

Component	Size	Value	Units
R1	0805	33.2	Ω
C1	0402	1000	ρF

### **RECOMMENDED APPLICATION CIRCUIT**

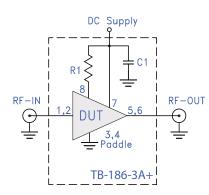


Fig 2. Test Board includes case, connectors, and components soldered to PCB

Component	Size	Value	Units
R1	0805	33.2	Ω
C1	0402	1000	ρF

### **PRODUCT MARKING**



Marking may contain other features or characters for internal lot control

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### ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASH BOARD. TO ACCESS

**CLICK HERE** 

Performance Data	Data Table Swept Graphs S-Parameter (S2P Files) Data Set (.zip file)
Case Style  DQ849 3x3x0.9 mm MCLP Plastic package, exposed paddle lead finish: Matte-Tin	
Tape & Reel Standard quantities available on reel	F104 7" reels with 20, 50, 100, 200, 500, 1K, or 2K devices
Suggested Layout for PCB Design	PL-078
Evaluation Board	TB-186-3A+
Environmental Ratings	ENV08T1

### **ESD RATING**

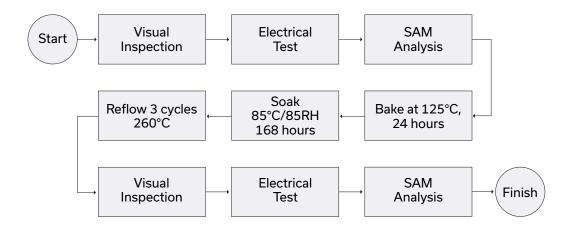
Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (>25V) in accordance with ANSI/ESD STM5.2-1999

### **MSL RATING**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

### **MSL TEST FLOW CHART**



### NOTES

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
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