

Description

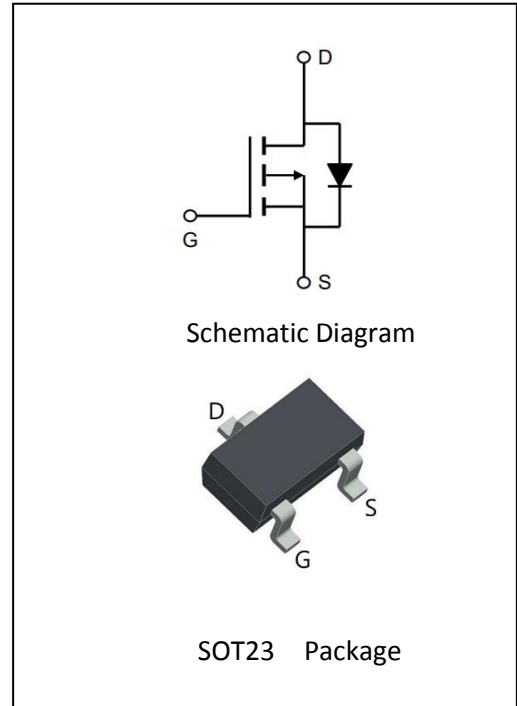
The AO3401S combines advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltage as low as 2.5V. This device is suitable for use as a load switch or other general applications.

Features

- ① $V_{DS} = -30V$, $I_D = -4.2A$
 $R_{DS(ON)typ.} = 45m\Omega @ V_{DS} = -10V$
 $R_{DS(ON)typ.} = 51m\Omega @ V_{DS} = -4.5V$
 $R_{DS(ON)typ.} = 65m\Omega @ V_{DS} = -2.5V$
- ② Low gate charge
- ③ High power and current handling capability
- ④ Termination is Lead-free and RoHS Compliant

Applications

- ① PWM applications
- ② Load switch
- ③ Power Management



Maximum Ratings($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	-4.2	A
Pulsed Drain Current ^B	I_{DM}	-20	A
Maximum Power Dissipation ^A	P_D	1.2	W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	104	$^\circ C/W$
---	-----------------	-----	--------------

Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30			V
Gate-Threshold Voltage	$V_{th(GS)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.7	-9	-1.3	V
Gate-body Leakage	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 10V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-24V, V_{GS}=0V$			-1	μA
Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-4.2A$		45	55	m Ω
		$V_{GS}=-4.5V, I_D=-4A$		51	68	m Ω
		$V_{GS}=-2.5V, I_D=-1A$		65	88	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=-5V, I_D=-4.2A$		10		s
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V, F=1MHz$		880		pF
Output Capacitance	C_{oss}			105		
Reverse Transfer Capacitance	C_{rss}			65		
Switching Capacitance						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-15V, I_D=-4.2A$ $V_{GS}=-10V, R_{GEN}=6\Omega$		7		nS
Turn-on Rise Time	t_r			3		nS
Turn-off Delay Time	$t_{d(off)}$			30		nS
Turn-off Fall Time	t_f			12		nS
Total Gate Charge	Q_g	$V_{DS}=-15V, I_D=-4.2A,$ $V_{GS}=-4.5V$		8.5		nC
Gate-Source Charge	Q_{gs}			1.8		nC
Gate-Drain Charge	Q_{gd}			2.7		nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_D=-4.2A$			-1.2	V
Diode Forward Current	I_s				-4.2	A

Notes:

- A. The Power dissipation PD is based on $T_J(MAX)=150^\circ C$, using $\leq 10s$ junction-to ambient thermal resistance.
- B. Repetitive rating, pulse width limited by junction temperature $T_J(MAX)=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ C$.
- C. The Static characteristics in Figures are obtained using $<300\mu s$ pulses, duty cycle 2% max.

Typical Electrical and Thermal Characteristics

Figure 1: On-region Characteristics

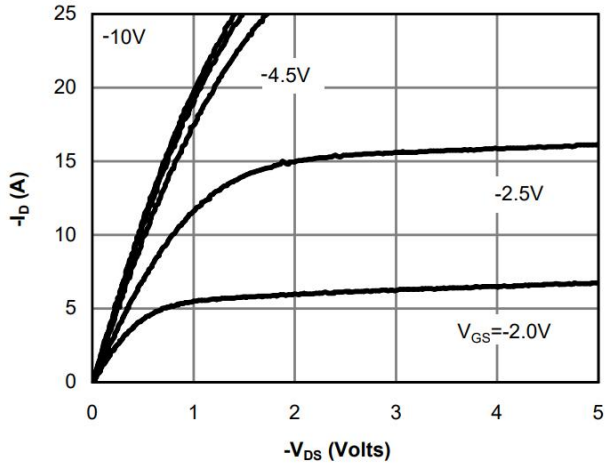


Figure 2: Transfer Characteristics

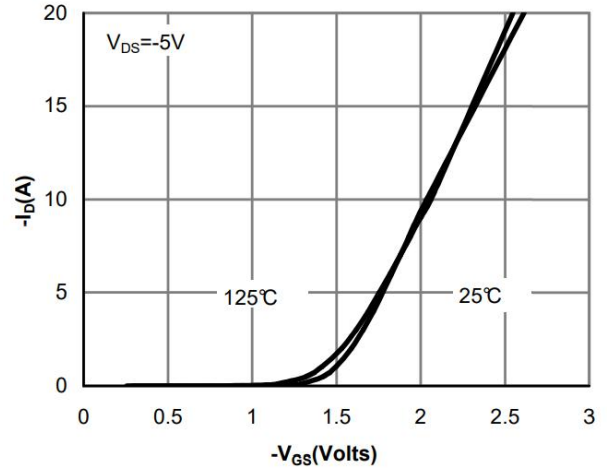


Figure 3: Drain-Source On-Resistance

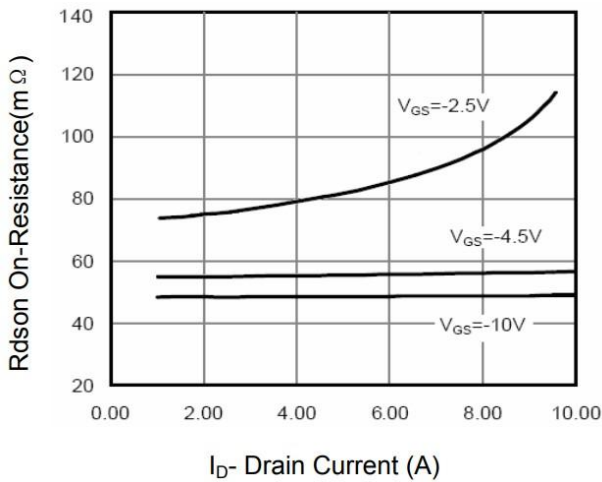


Figure 4: On-Resistance vs. Junction Temperature

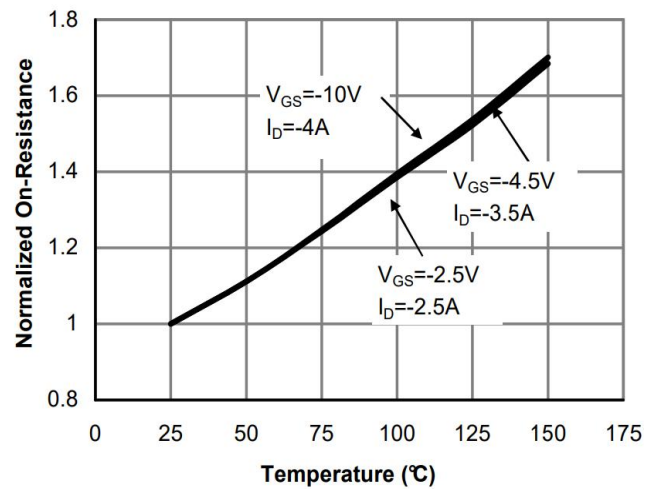


Figure 5: On-Resistance vs. Gate-Source Voltage

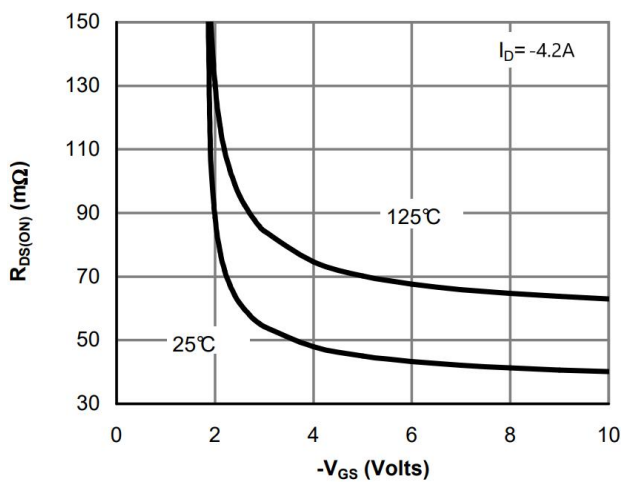


Figure 6: Body-Diode Characteristics

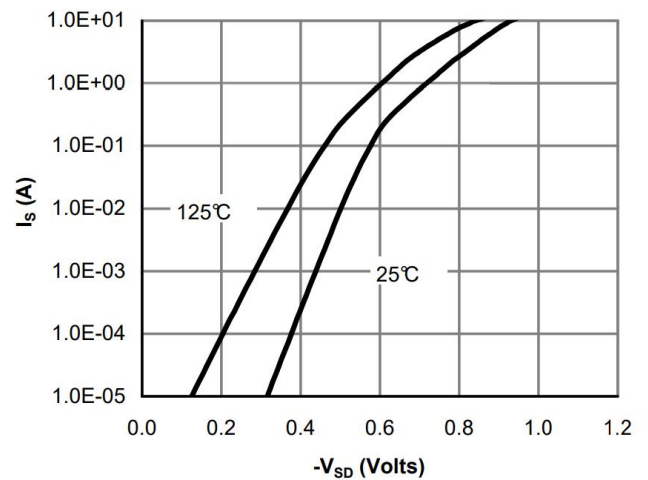


Figure 7: Gate-Charge Characteristics

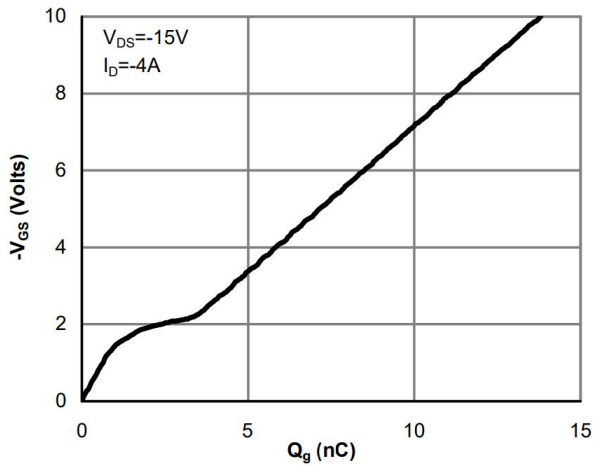


Figure 8: Capacitance Characteristics

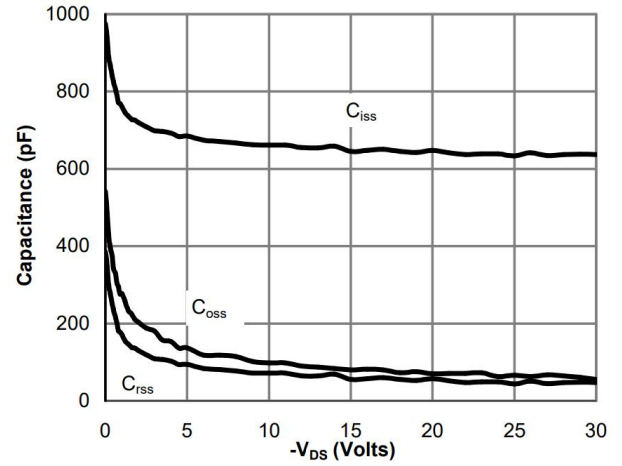


Figure 9: Power Dissipation

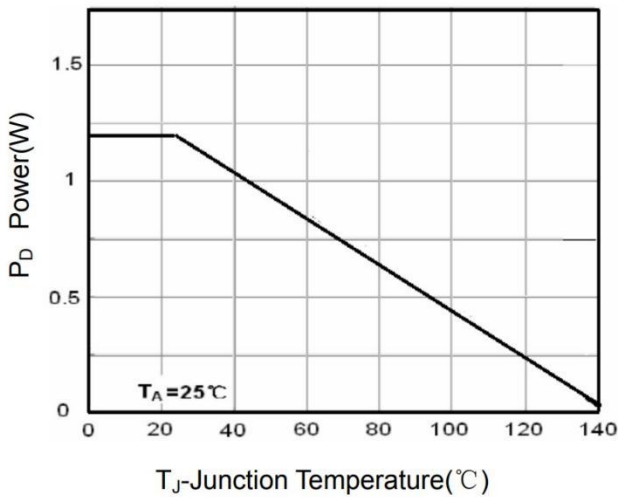


Figure 10: Drain Current

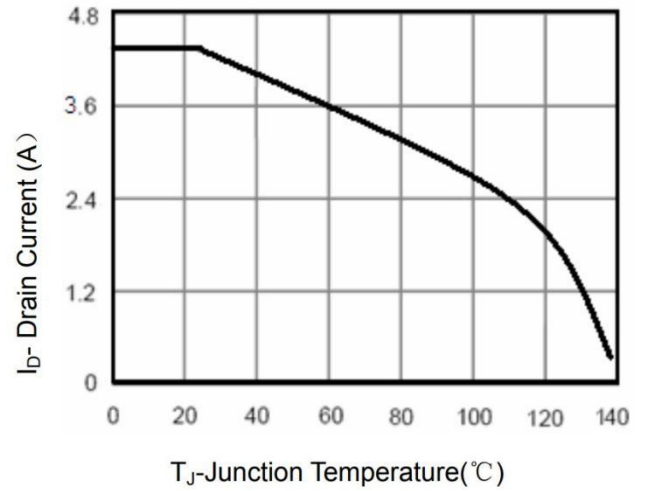


Figure 11: Switching Test Circuit

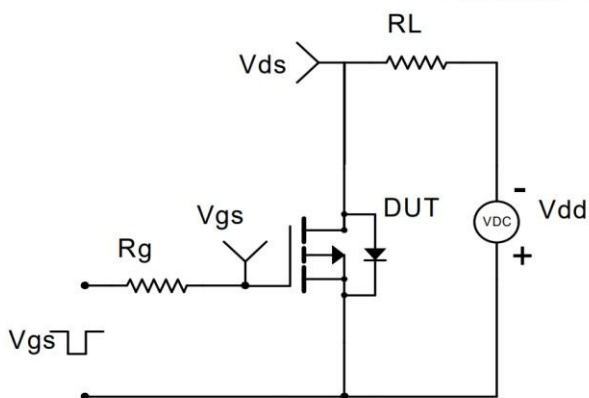


Figure 12: Switching Waveform

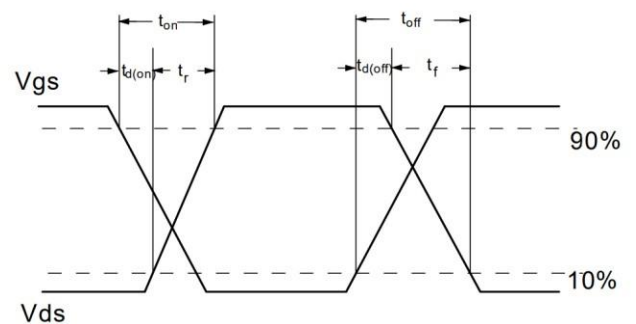


Figure 13: Safe Operation Area

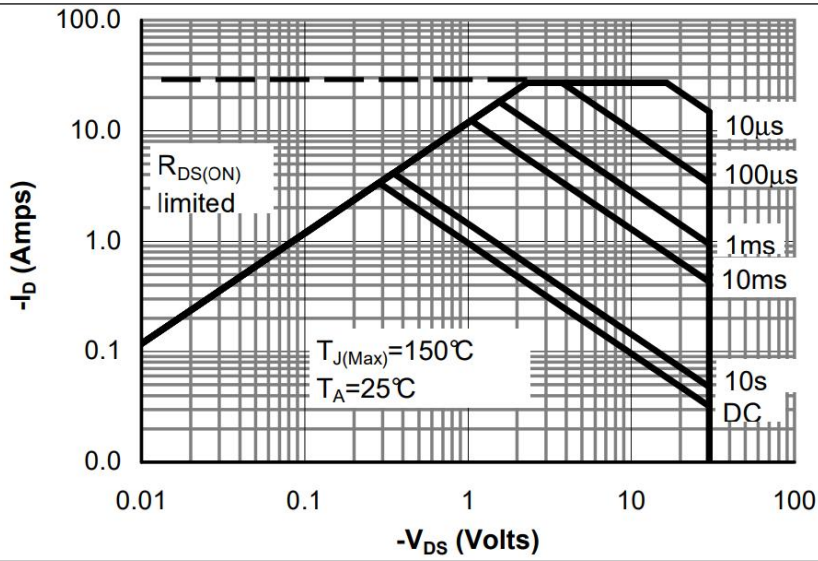
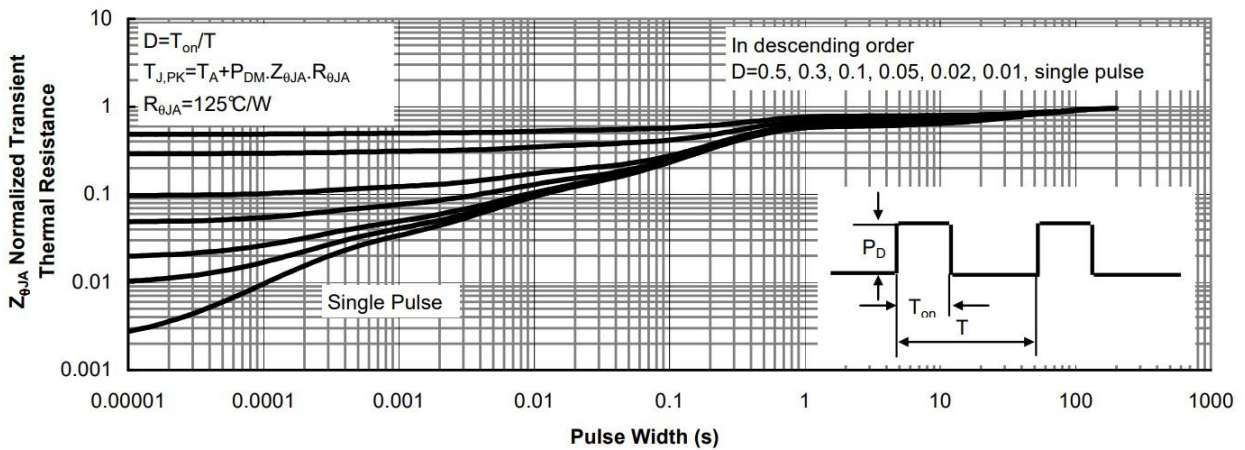
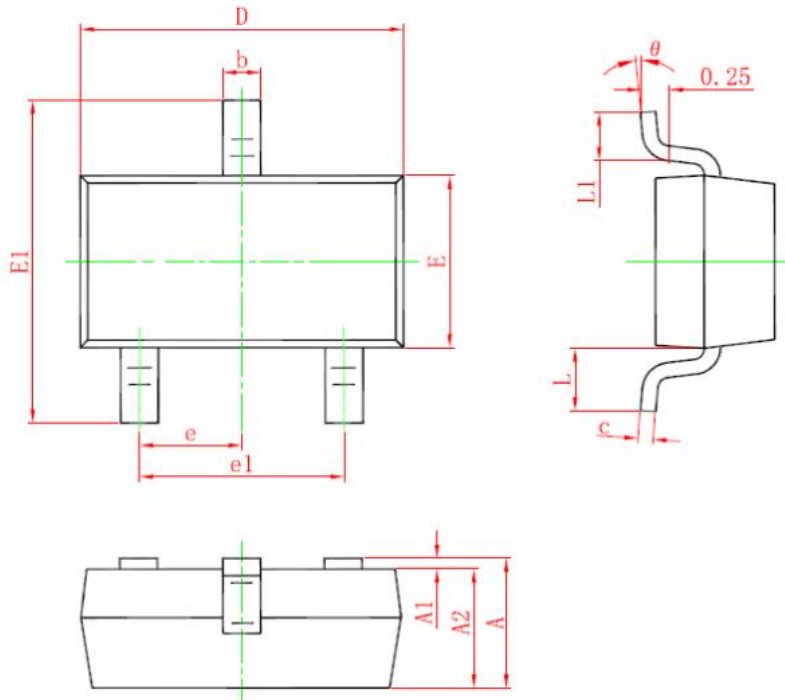


Figure 14: Normalized Maximum transient Thermal Impedance



SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°



NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

CONTACT:

深圳市迈诺斯科技有限公司 (总部)

地址：深圳市福田区华富街道田面社区深南中路4026号田面城市大厦22B-22C

邮编：518025

电话：0755-83273777

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [MOSFET](#) category:

Click to view products by [Minos](#) manufacturer:

Other Similar products are found below :

[IRFD120](#) [IRFY240C](#) [JANTX2N5237](#) [2SK2267\(Q\)](#) [BUK455-60A/B](#) [MIC4420CM-TR](#) [VN1206L](#) [NDP4060](#) [SI4482DY](#)
[IPS70R2K0CEAKMA1](#) [SQD23N06-31L-GE3](#) [TK16J60W,S1VQ\(O](#) [2SK2614\(TE16L1,Q\)](#) [DMN1017UCP3-7](#) [EFC2J004NUZTDG](#)
[DMN1053UCP4-7](#) [SQJ469EP-T1-GE3](#) [NTE2384](#) [DMC2700UDMQ-7](#) [DMN2080UCB4-7](#) [DMN61D9UWQ-13](#) [US6M2GTR](#)
[DMN31D5UDJ-7](#) [DMP22D4UFO-7B](#) [DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#) [IRF40H233XTMA1](#) [STU5N65M6](#)
[DMN6022SSD-13](#) [DMN13M9UCA6-7](#) [DMTH10H4M6SPS-13](#) [DMN2990UFB-7B](#) [IPB80P04P405ATMA2](#) [2N7002W-G](#) [MCAC30N06Y-](#)
[TP](#) [MCQ7328-TP](#) [NTMC083NP10M5L](#) [NVMFS2D3P04M8LT1G](#) [BXP7N65D](#) [BXP4N65F](#) [AOL1454G](#) [WMJ80N60C4](#) [BXP2N20L](#)
[BXP2N65D](#) [BXT1150N10J](#) [BXT1700P06M](#) [TSM60NB380CP](#) [ROG](#) [RQ7L055BGTCR](#) [DMNH15H110SK3-13](#)