

## 100V N-Channel Power MOSFET

### DESCRIPTION

The MDT13N10D uses advanced trench technology to provide excellent RDS(ON), low gate charge. It can be used in a wide variety of applications.

### Application

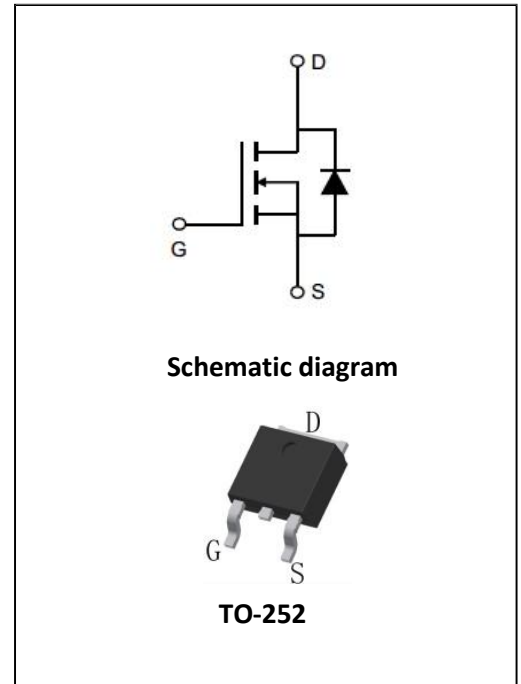
- ① Power switching application
- ② Hard switched and High frequency circuits
- ③ Uninterruptible power supply

### KEY CHARACTERISTICS

- ①  $V_{DS} = 100V, I_D = 13A$   
 $R_{DS(ON)} < 120m\Omega @ V_{GS}=10V$
- ② High density cell design for lower Rdson
- ③ Fully characterized avalanche voltage and current
- ④ Good stability and uniformity with high EAS
- ⑤ Excellent package for good heat dissipation

**100% UIS TESTED !**

**100% DVDS TESTED !**



### Package Marking And Ordering Information

Ordering Codes	Package	Product Code	Packing
MDT13N10D	TO-252	MDT13N10D	Reel

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	13	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	40	A
Maximum Power Dissipation( $T_c=25^\circ C$ )	$P_D$	31	W
Single pulse avalanche energy(Note 2)	EAS	21	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ C$

### Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	4.8	$^\circ C/W$
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**Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.8	2.4	V
Drain-Source On-State Resistance <sup>(Note 3)</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	110	120	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =25V, I <sub>D</sub> =3.6A	-	5	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	680	-	pF
Output Capacitance	C <sub>oss</sub>		-	110	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	85	-	pF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V, R <sub>GEN</sub> =2.5Ω	-	10	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	7	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	34	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	9	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =80V, I <sub>D</sub> =3A V <sub>GS</sub> =10V	-	16	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	4	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =15A	-	-	1.2	V

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. EAS condition :T j=25°C, V<sub>DD</sub>=50V, V<sub>GS</sub>=10V, L=0.5mH, R<sub>g</sub>=25Ω
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production.

Characteristics Curves

Figure 1 Output Characteristics

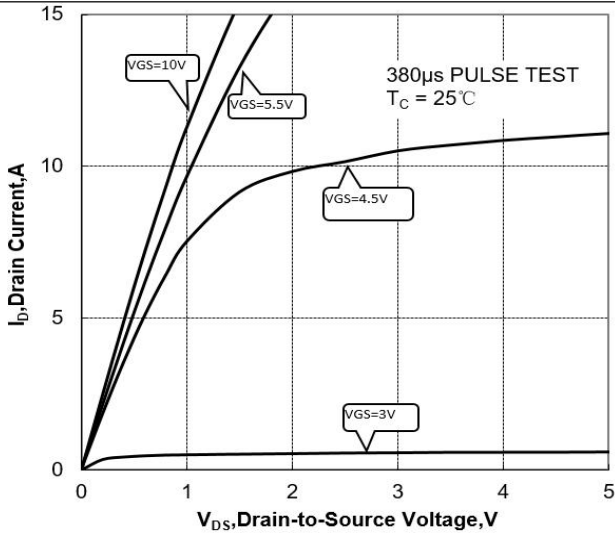


Figure 2 Transfer Characteristics

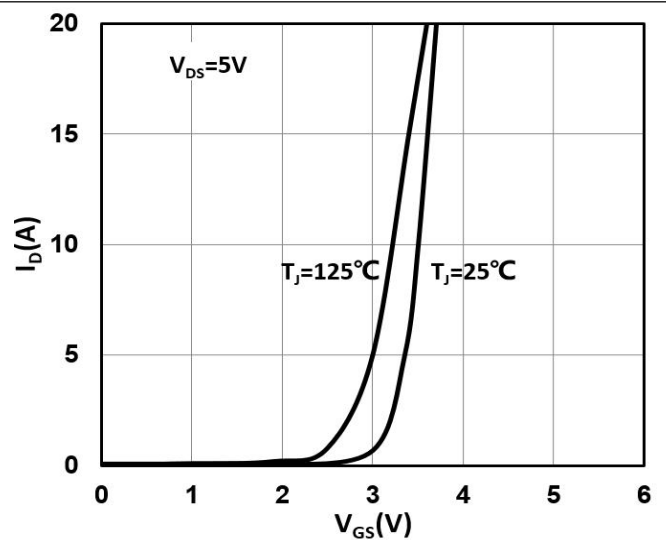


Figure 3 On-Resistance vs. ID and VGS

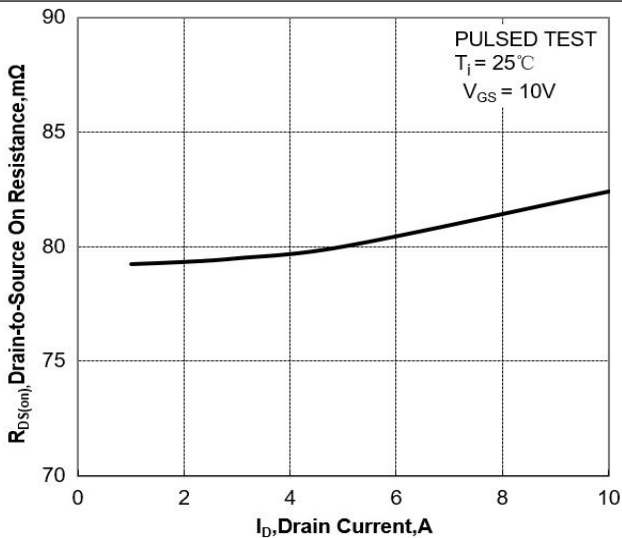


Figure 4 On-Resistance vs. Junction Temperature

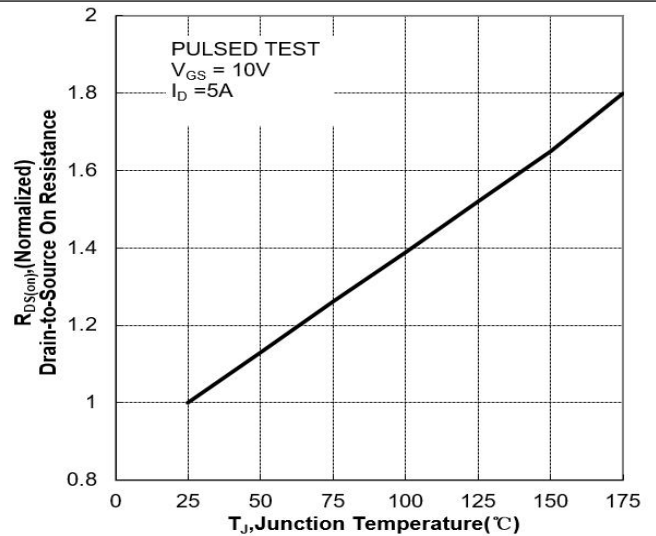


Figure 5 On-Resistance vs. VGS

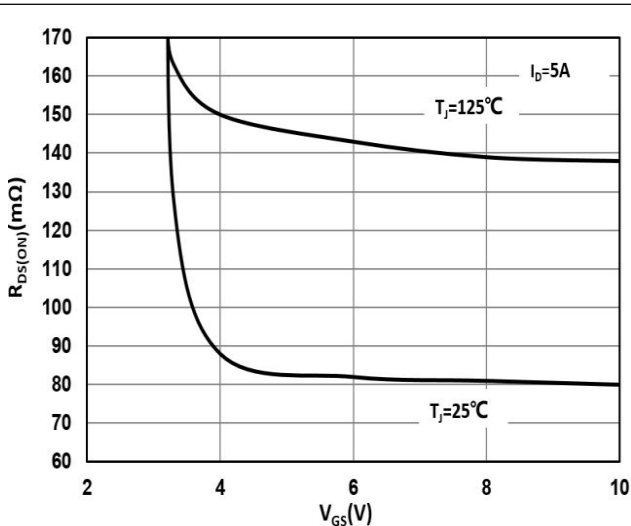


Figure 6 Body Diode Forward Voltage

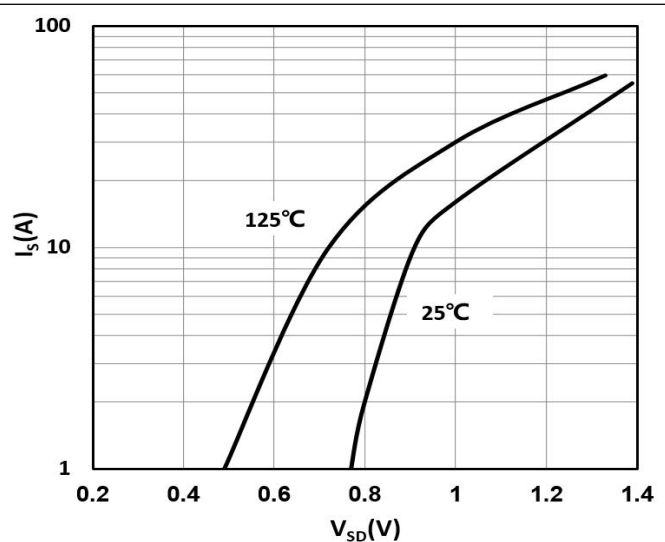


Figure 7 Gate-Charge Characteristics

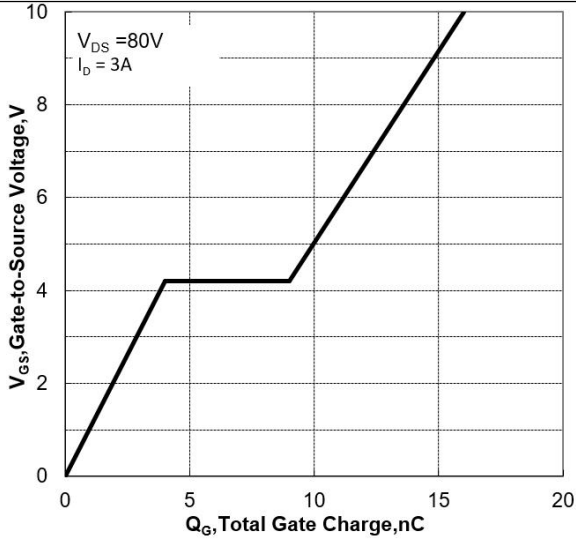


Figure 8 Capacitance Characteristics

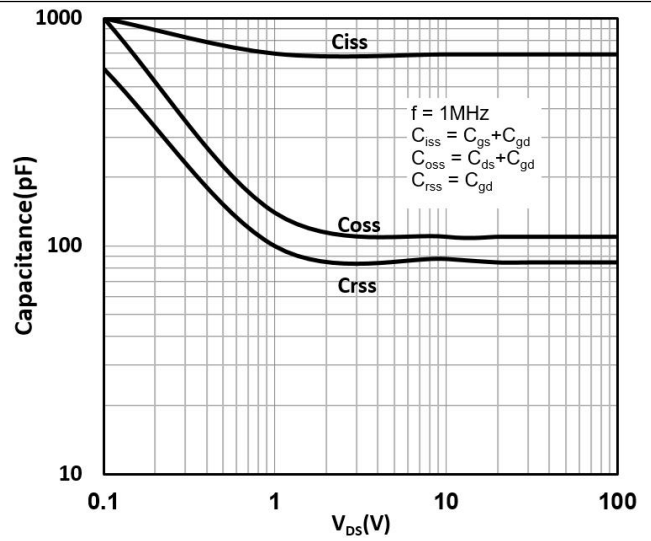


Figure 9 Maximum Forward Biased Safe Operation Area

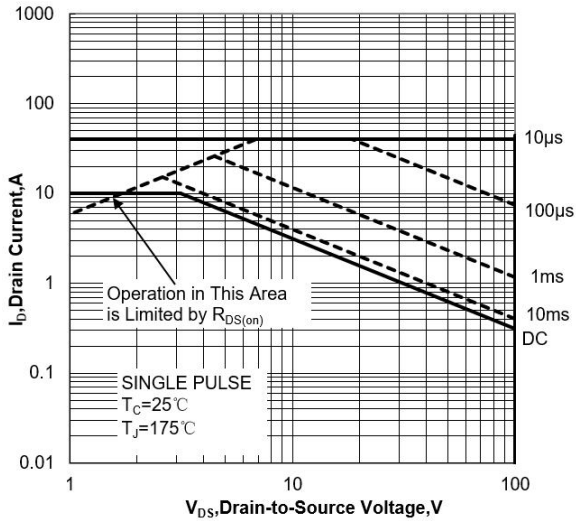


Figure 10 Single Pulse Power Rating Junction-to-Ambient

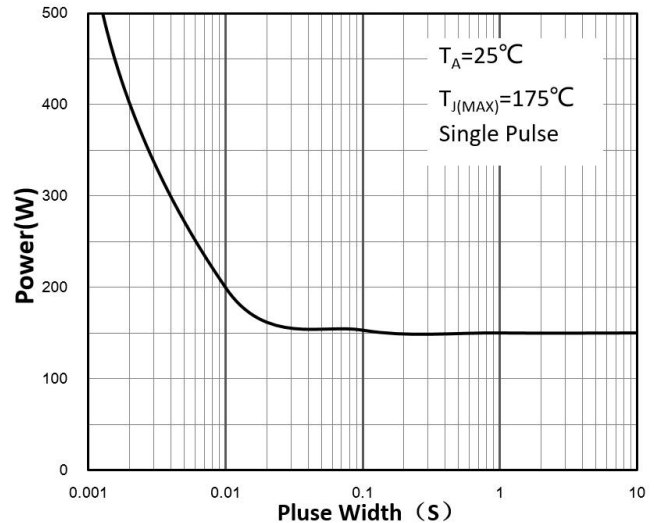
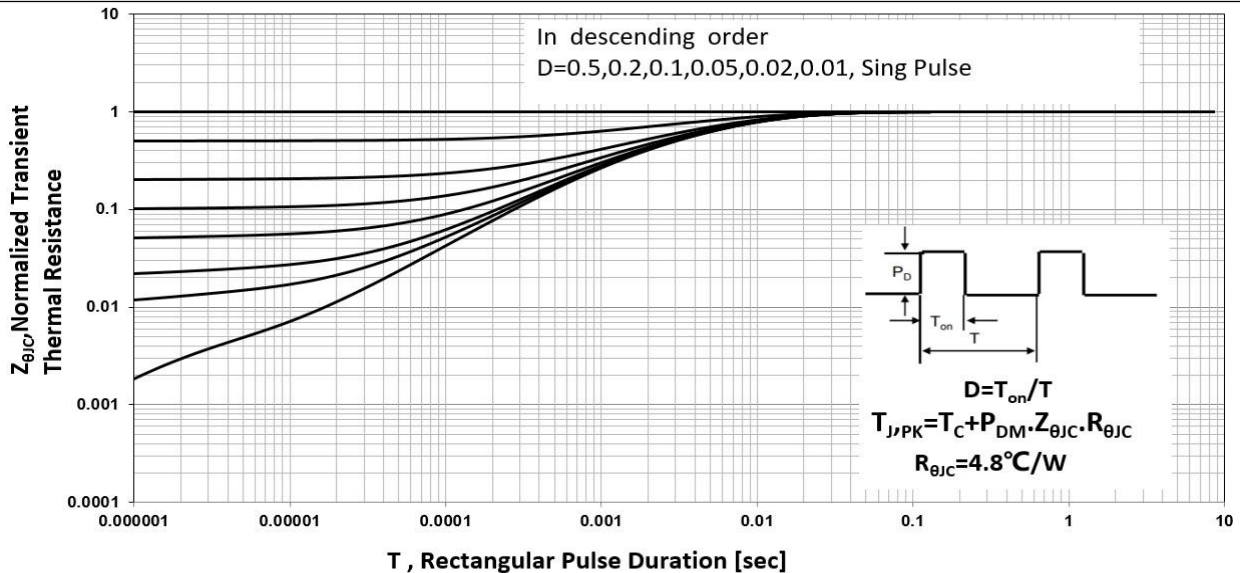
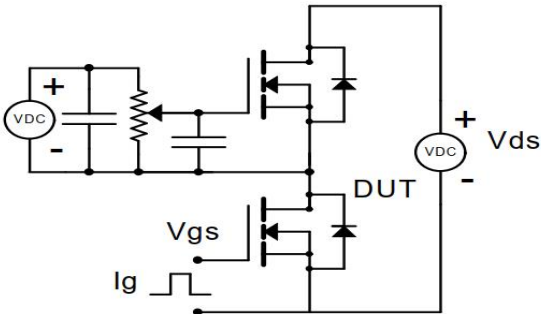
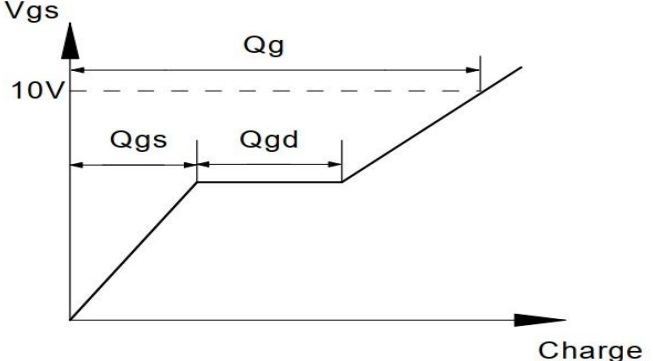
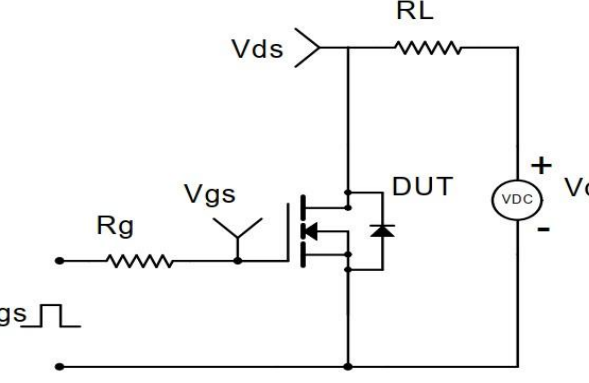
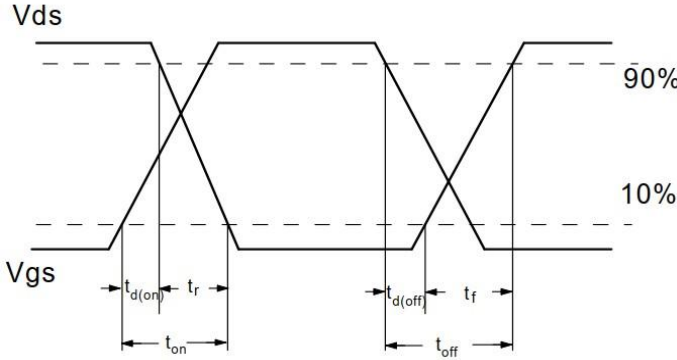
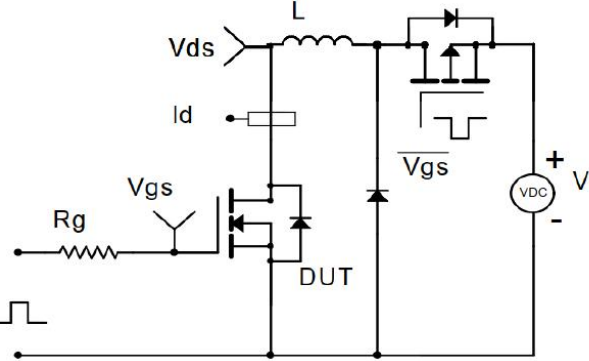
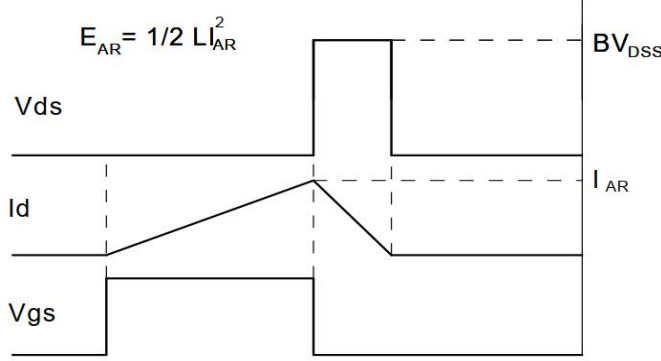
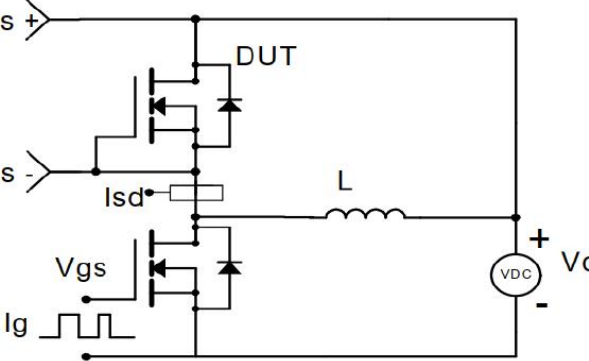
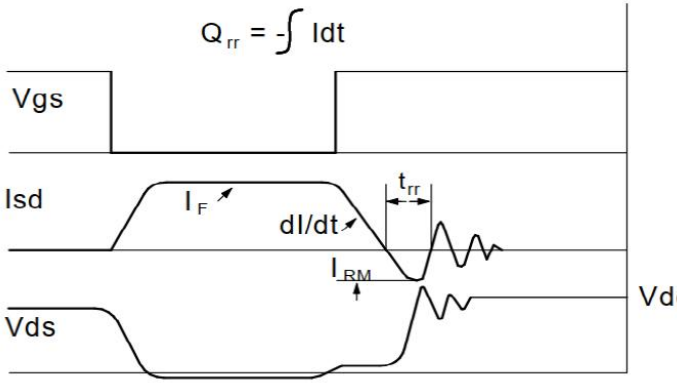


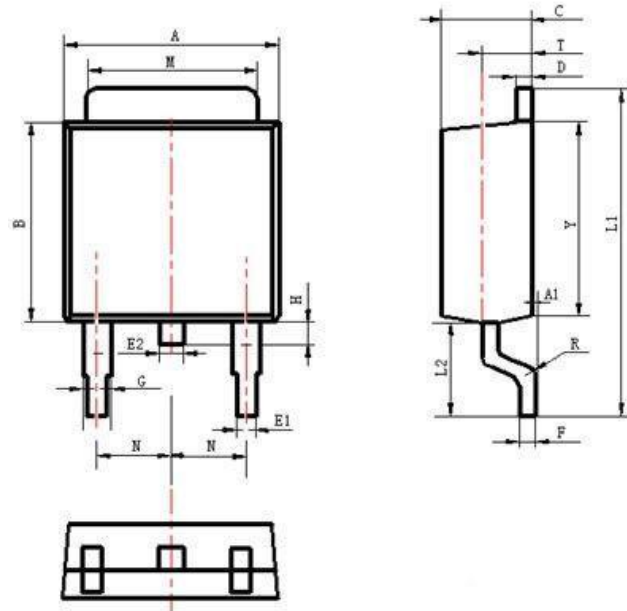
Figure 11 Normalized Maximum Transient Thermal Impedance



Test Circuit and Waveform

<p style="text-align: center;"><b>Gate Charge Test Circuit</b></p> 	<p style="text-align: center;"><b>Gate Charge Test Waveform</b></p> 
<p style="text-align: center;"><b>Resistive Switching Test Circuit</b></p> 	<p style="text-align: center;"><b>Resistive Switching Test Waveforms</b></p> 
<p style="text-align: center;"><b>Unclamped Inductive Switching (UIS) Test Circuit</b></p> 	<p style="text-align: center;"><b>Unclamped Inductive Switching (UIS) Test Waveforms</b></p> 
<p style="text-align: center;"><b>Diode Recovery Test Circuit</b></p> 	<p style="text-align: center;"><b>Diode Recovery Test Waveforms</b></p> 

Package Description



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.60
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package



**NOTE:**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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