

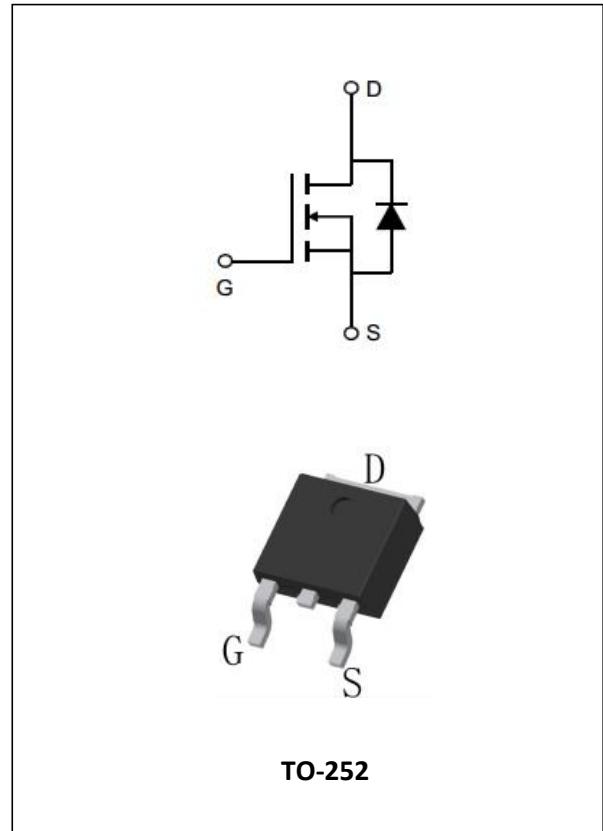
## Silicon N-Channel Power MOSFET

### Description

MDT4N65, This Power MOSFET is produced using Wisdom's advanced planar stripe, DMOS technology. This latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics. These devices are well suited for high efficiency switch mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

### KEY CHARACTERISTICS

- ①  $V_{DS} = 650V, I_D = 4A, R_{DS(ON)} < 2.5\Omega @ V_{GS}=10V$
- ② Gate Charge (Typical 15nC)
- ③ Improved dv/dt Capability, High Ruggedness
- ④ 100% Avalanche Tested
- ⑤ Maximum Junction Temperature Range (150°C)



TO-252

### ORDERING INFORMATION

Ordering Codes	Package	Product Code	Packing
MDT4N65	TO-252	MDT4N65	Tube

### ABSOLUTE RATINGS at $TC=25^{\circ}C$ ,unless otherwise specified

Symbol	Parameter	Rating	Units
$V_{DSS}$	Drain-to-Source Voltage	650	V
$I_D$	Continuous Drain Current	4	A
	Continuous Drain Current $TC = 100^{\circ}C$	2.5	A
$I_{DM}$	Pulsed Drain Current(Note1)	16	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulse Avalanche Energy(Note2)	240	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note3)	4.5	V/ns
$P_D$	Power Dissipation TO-252	50	W
	Derating Factor above 25°C	0.18	W/ $^{\circ}C$



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T <sub>L</sub> T <sub>J</sub> , T <sub>stg</sub>	Operating Junction and Storage Temperature Range	150, -55 to 150	°C
	Maximum Temperature for Soldering	300	°C

### Thermal characteristics

#### Thermal characteristics (No FullPAK) TO-252

Symbol	Parameter	RATINGS	Units
R <sub>θJC</sub>	Junction-to-Case	5.5	°C/W
R <sub>θJA</sub>	Junction-to-Ambient	62.5	°C/W

### Electrical Characteristics At TC=25°C,unless otherwise specified

OFF Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
V <sub>DSS</sub>	Drain to Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	650	--	--	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Bvdss Temperature Coefficient	I <sub>D</sub> =250uA, Reference25°C	--	0.6	--	V/°C
I <sub>DSS</sub>	Drain to Source Leakage Current	V <sub>DS</sub> =600V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 25 °C	--	--	1	μA
		V <sub>DS</sub> =480V, V <sub>GS</sub> = 0V, T <sub>j</sub> = 125 °C	--	--	100	μA
I <sub>GSS(F)</sub>	Gate to Source Forward Leakage	V <sub>GS</sub> =+30V	--	--	100	nA
I <sub>GSS(R)</sub>	Gate to Source Reverse Leakage	V <sub>GS</sub> =-30V	--	--	-100	nA

ON Characteristics						
Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R <sub>D(S)</sub>	Drain-to-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =1.0A(Note4)	--	2	2.5	Ω
V <sub>GS(TH)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA(Note4)	2.0	--	4.0	V

#### Dynamic Characteristics



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Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
R <sub>g</sub>	Gate resistance	f = 1.0MHz	--	25	--	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V V <sub>DS</sub> = 25V F = 1.0MHz	--	545	710	PF
C <sub>oss</sub>	Output Capacitance		--	60	80	
C <sub>rss</sub>	Reverse Transfer Capacitance		--	8	11	

#### Switching Characteristics

Symbol	Parameter	Test Conditions	Values			Units
			Min.	Typ.	Max.	
t <sub>d(ON)</sub>	Turn-on Delay Time	ID = 2.0A VDD = 300V VGS = 10V RG = 25Ω	--	10	30	ns
T <sub>r</sub>	Rise Time		--	30	80	
t <sub>d(OFF)</sub>	Turn-Off Delay Time		--	45	11	
t <sub>f</sub>	Fall Time		--	40	90	
Q <sub>g</sub>	Total Gate Charge	ID = 2.0A VDS = 480V VGS = 10V	--	15	20	nC
Q <sub>gs</sub>	Gate to Source Charge		--	2.8	--	
Q <sub>gd</sub>	Gate to Drain ("Miller")Charge		--	6	--	

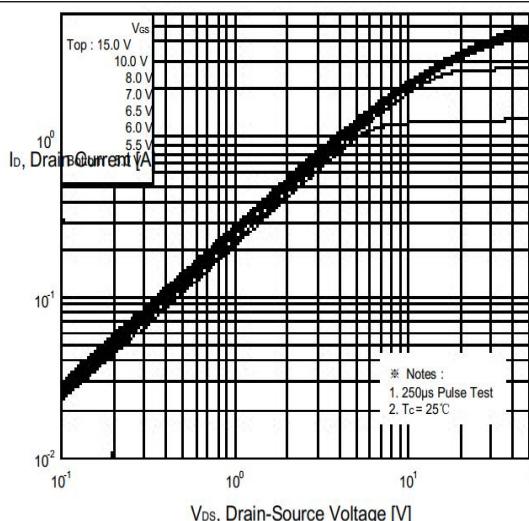
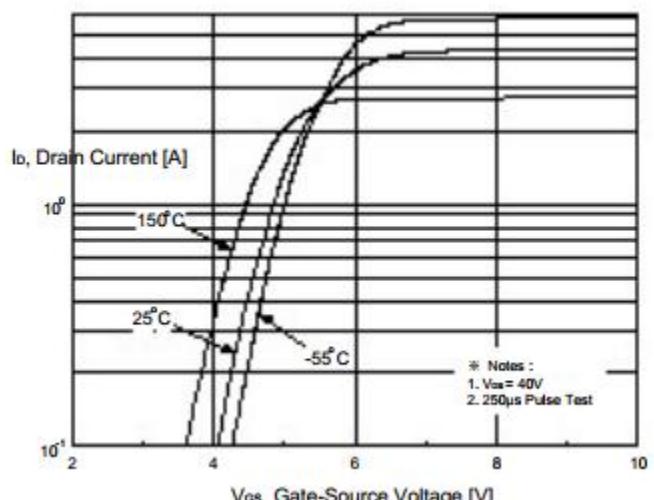
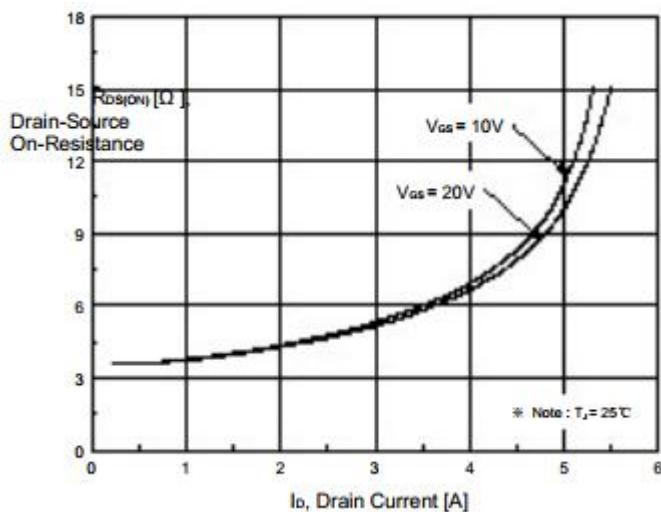
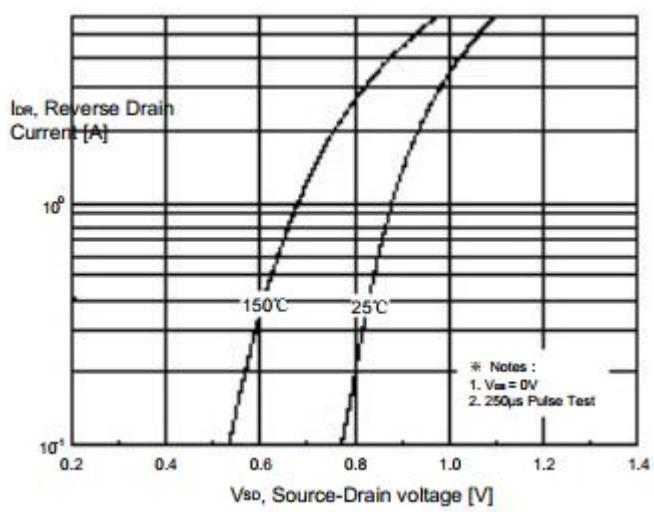
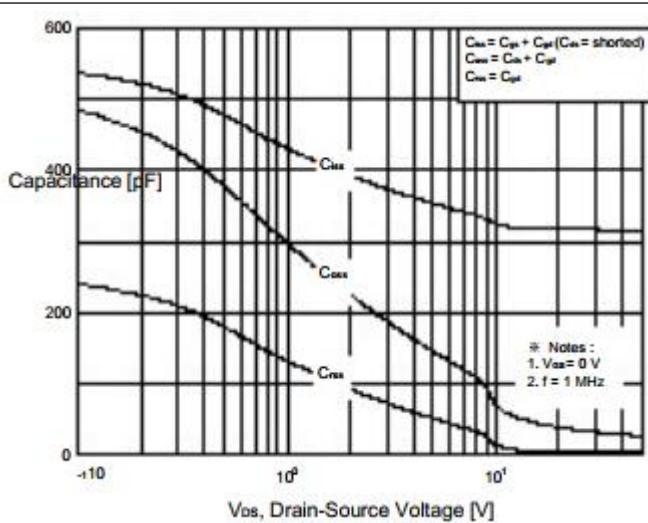
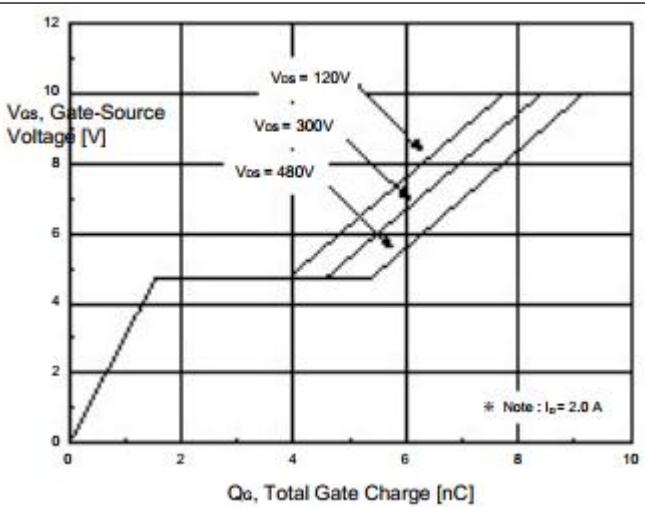
#### Source-Drain Diode Characteristics

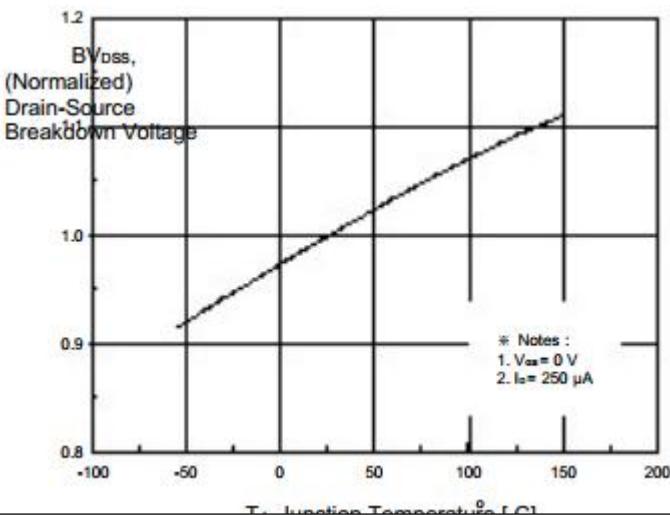
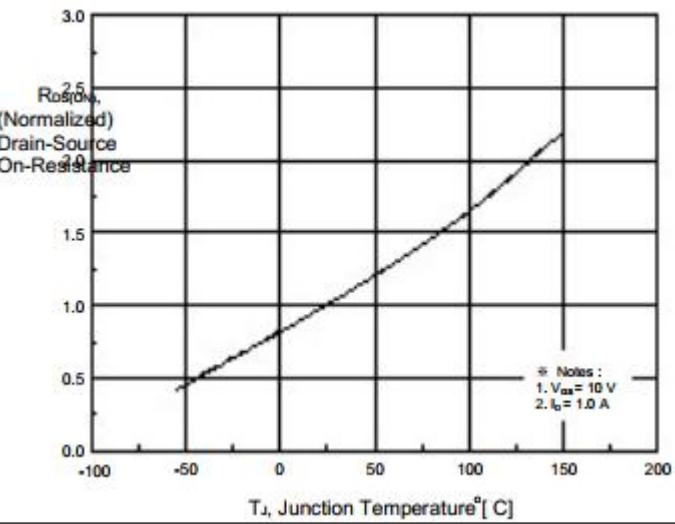
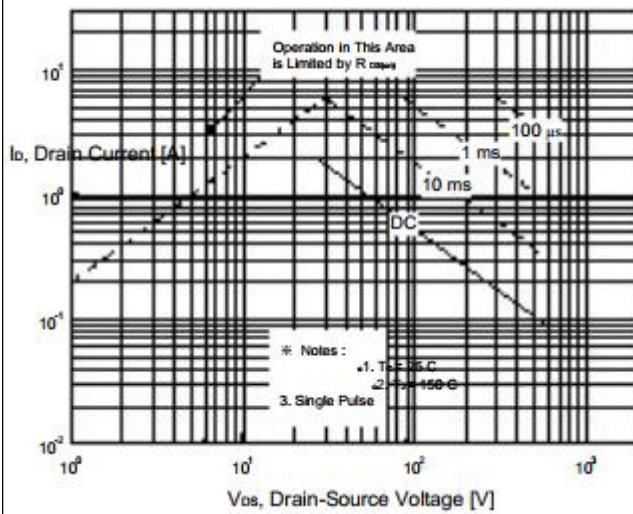
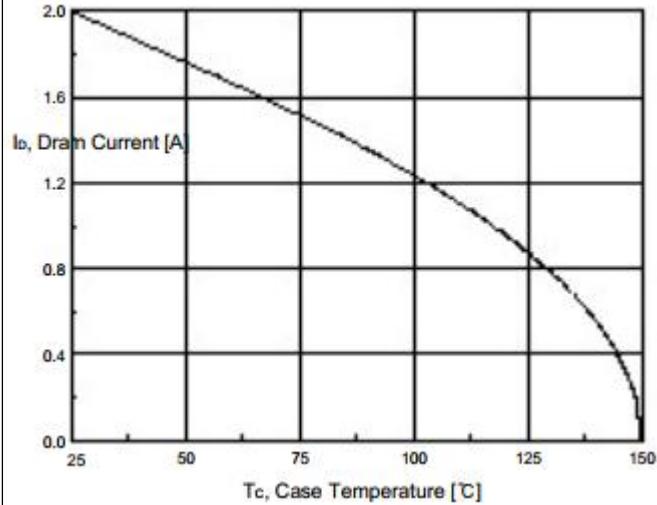
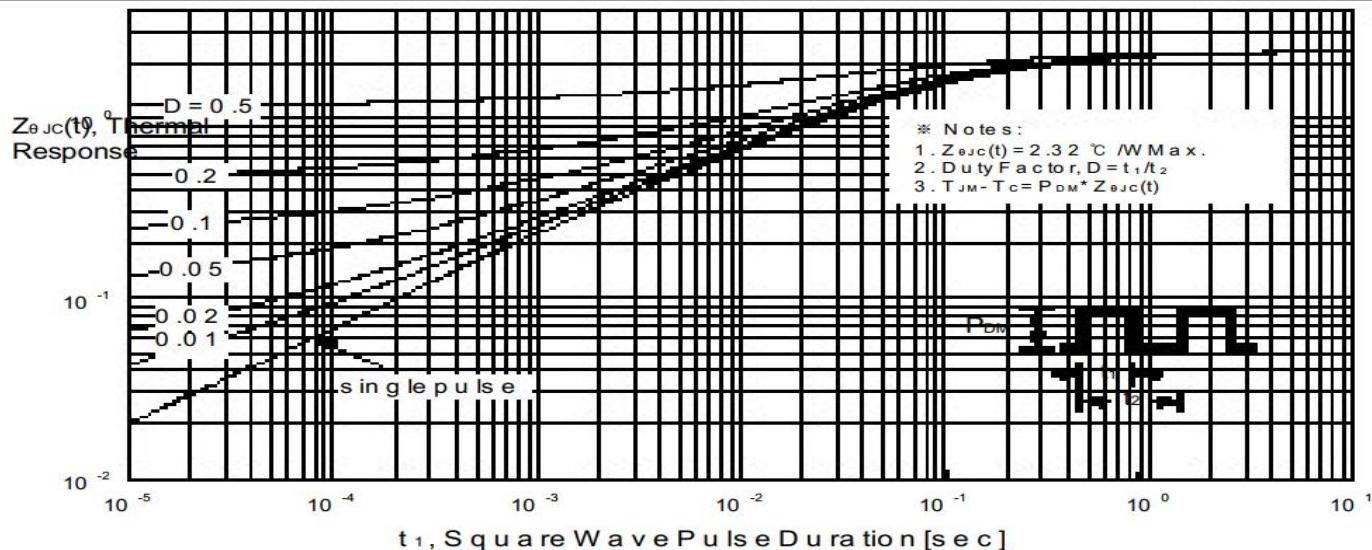
Symbol	Parameter	Test Conditions	Values			Units
			Min	Typ.	Max.	
I <sub>s</sub>	Continuous Source Current (Body Diode)	T <sub>c</sub> =25 °C	--	--	4	A
I <sub>SM</sub>	Maximum Pulsed Current (Body Diode)		--	--	16	A
V <sub>SD</sub>	Diode Forward Voltage	I <sub>s</sub> =2A, V <sub>GS</sub> =0V	--	--	1.4	V
T <sub>rr</sub>	Reverse Recovery Time	I <sub>s</sub> =2A, dI <sub>F</sub> /dt=100A/us, V <sub>GS</sub> =0V	--	350	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge		--	2.2	--	nC

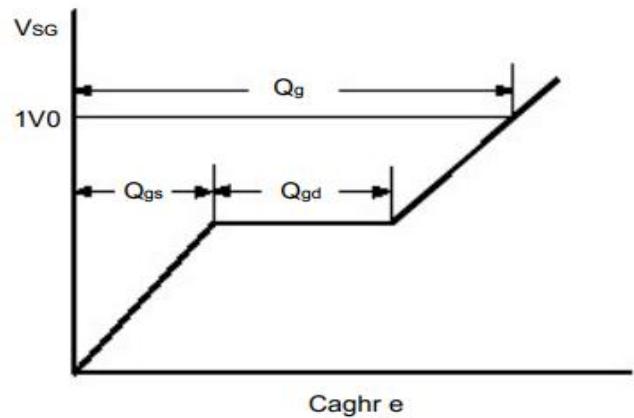
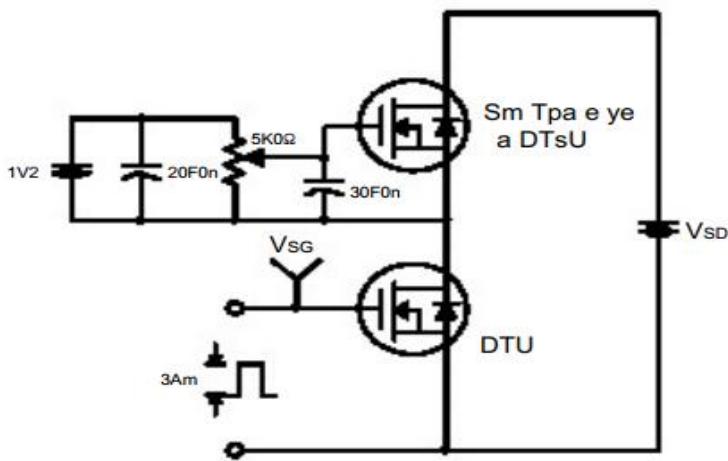
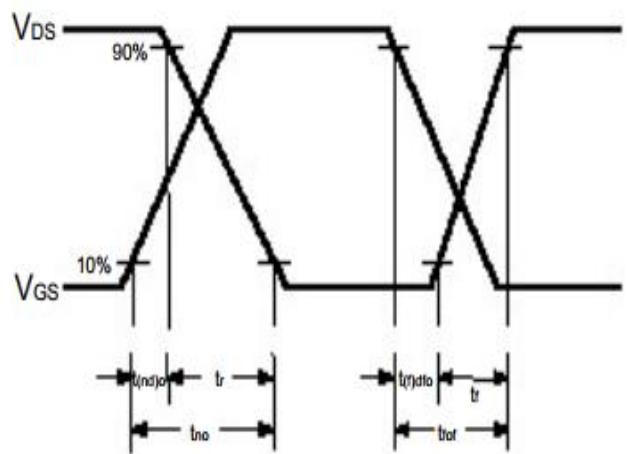
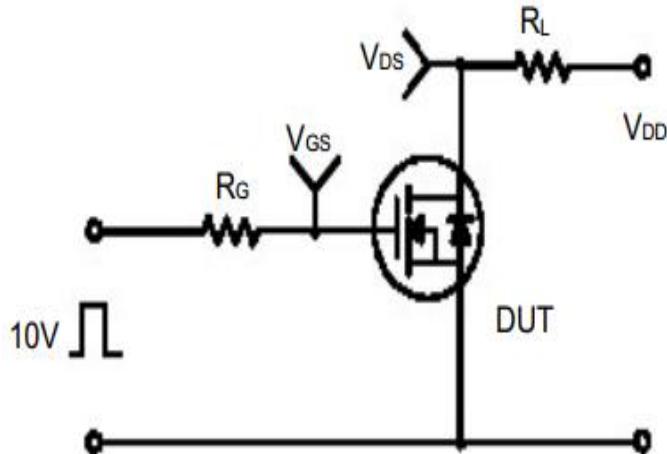
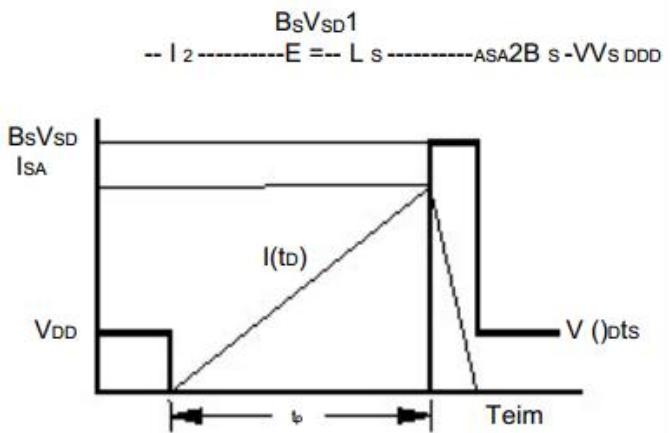
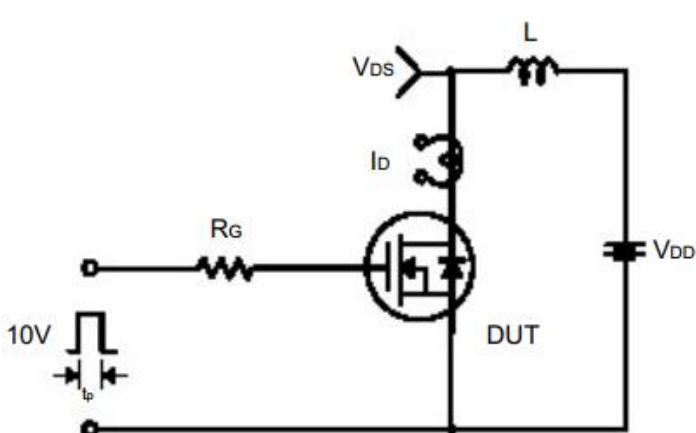
#### NOTES

- 1.Repeativity rating : pulse width limited by junction temperature
- 2.L = 55mH, IAS = 4.0A, VDD = 50V, RG = 25Ω , Starting TJ = 25°C
- 3.ISD ≤ 4.0A, di/dt ≤ 200A/us, VDD ≤ BVDSS, Starting TJ = 25°C
- 4.Pulse Test : Pulse Width ≤ 300us, Duty Cycle ≤ 2%
- 5.Essentially independent of operating temperature.

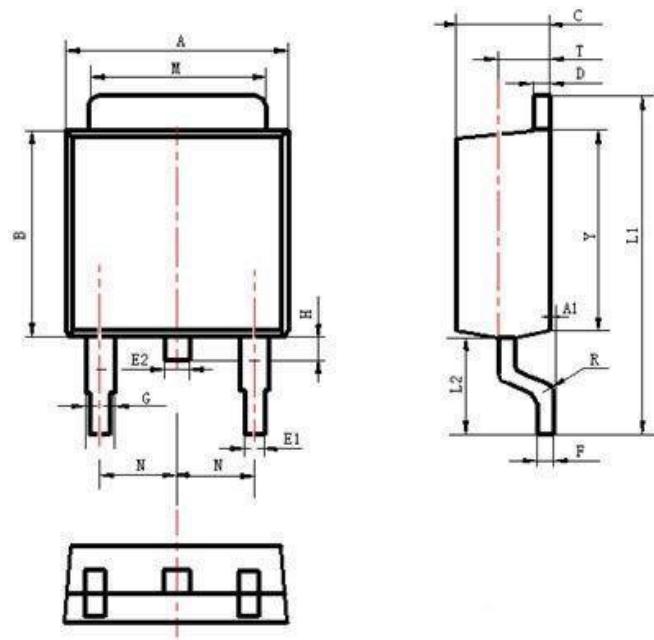
## Characteristics Curves

**Figure 1. On-Region Characteristics**

**Figure 2. Transfer Characteristics**

**Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage**

**Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature**

**Figure 5. Capacitance Characteristics**

**Figure 6. Gate Charge Characteristics**


**Figure7.BreakdownVoltageVariation**

**Figure8.On-ResistanceVariation**

**Figure9.Maximum Safe Operating Area**

**Figure10.Maximum Drain Current Vs Case Temperature**

**Figure11.TransientThermalResponseCurve**


**Gate Charge Test Circuit & Waveform**

**Resistive Switching Test Circuit & Waveforms**

**Unclamped Inductive Switching Test Circuit & Waveforms**


## Package Description



Items	Values(mm)	
	MIN	MAX
A	6.30	6.90
A1	0	0.13
B	5.70	6.30
C	2.10	2.50
D	0.30	0.60
E1	0.60	0.90
E2	0.70	1.00
F	0.30	0.60
G	0.70	1.20
L1	9.60	10.50
L2	2.70	3.10
H	0.60	1.00
M	5.10	5.50
N	2.09	2.49
R	0.3	
T	1.40	1.60
Y	5.10	6.30

TO-252 Package



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**NOTE:**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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