

Silicon N-Channel Power MOSFET

Description

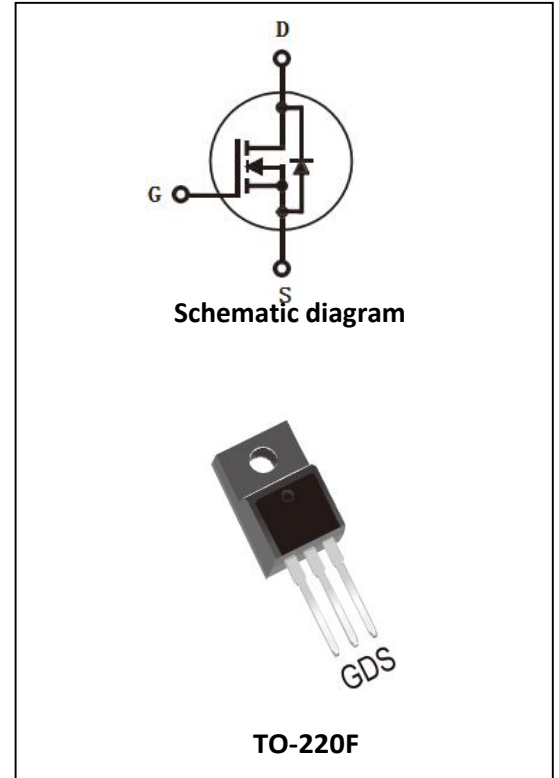
The MPF10N65 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

General Features

- ① VDS=10A, ID=650V
- ② RDS(on)(typ)=0.6Ω@VGS=10V, ID=4.0A
- ③ Low Crss:18.5pF@25V
- ④ Fast switching
- ⑤ Improved dv/dt capability

Application

- ① Switch Mode Power Supply (SMPS)
- ② Uninterruptible Power Supply (UPS)
- ③ Power Factor Correction (PFC)



Package Marking and Ordering Information

Ordering Codes	Package	Product Code	Packing
MPF10N65	TO-220F	MPF10N65	Tube

Electronic Characteristics (Tc=25℃)

PARAMETER	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
Drain-source Breakdown Voltage	BVDSS	VGS=0V, ID=250μA	650			V
Gate Threshold Voltage	VGS(TH)	VGS=VDS, ID=250μA	3.0		4.0	V
Drain-source Leakage Current	IDSS	VDS=650V, VGS=0V, Tj=25℃			1	μA
		VDS=520V, VGS=0V, Tj=125℃			100	μA
Gate-body Leakage Current(VDS=0)	IGSS	VGS=±30V			±100	nA
Static Drain-source On Resistance	RDS(ON)	VGS=10V, ID=4.0A		0.6	0.7	Ω
Input Capacitance	Ciss	VGS=0V, VDS=25V, F=1.0MHZ		1110		pF
Output Capacitance	Coss			106		pF
Reverse Transfer Capacitance	Crss			13		pF

Turn-On Delay Time	Td(on)	V _{DD} =325V, I _D =10A R _G =25Ω	39	ns
Turn-On Rise Time	Tr		10	ns
Turn-Off Delay Time	Td(off)		152	ns
Turn-Off Fall Time	Tf		42	ns
Total Gate Charge	Qg	I _D =10A, V _{DS} =520V V _{GS} =10V	37	nC
Gate-to-Source Charge	Qgs		5	nC
Gate-to-Drain Charge	Qgd		24	nC
Continuous Diode Forward Current	Is		10	A
Diode Forward Voltage	VSD	T _j =25°C, Is=12.0A VGS=,0V III	1.4	V
Reverse Recovery Time	Trr	T _j =25°C, If=12.0A di/dt=100A/μS III	601	ns
Reverse Recovery Charge	Qrr		2.3	uC

Notes:

1. Repetitive rating:Pulse width limited by maximum junction temperature
2. Starting T_j=25°C, VDD=50V, L=10mH, R_G=25Ω, IAS=9.0A
3. PulseTest:Pulse width≤300μs, Duty cycle≤2%

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

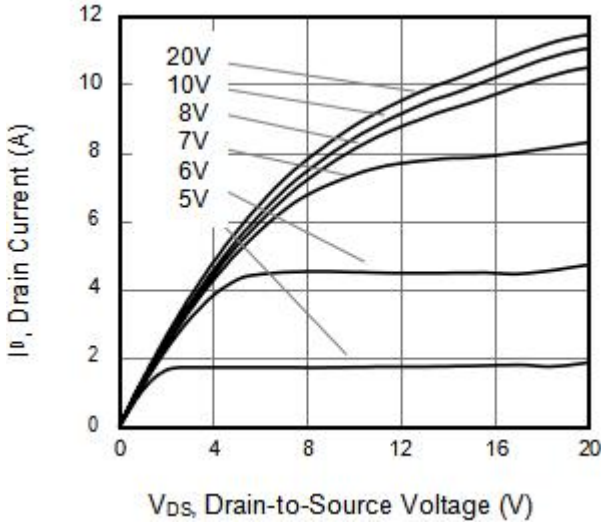


Figure 2. Body Diode Forward Voltage

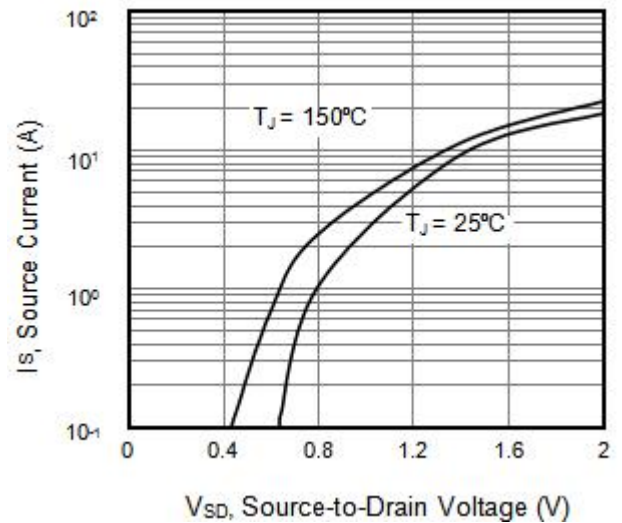


Figure 3. Drain Current vs. Temperature

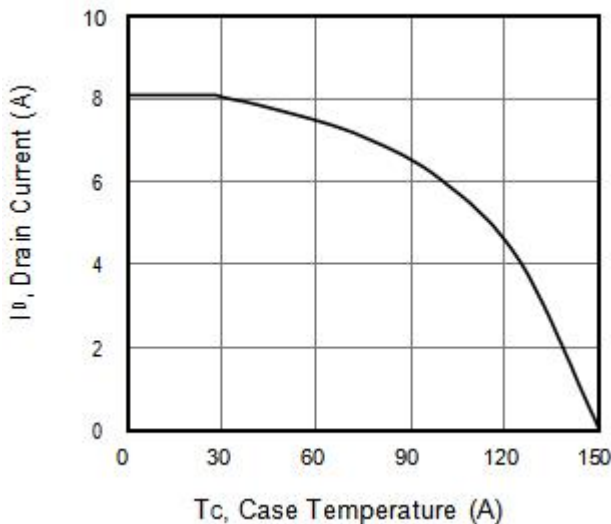


Figure 4. BV_{DSS} Variation vs. Temperature

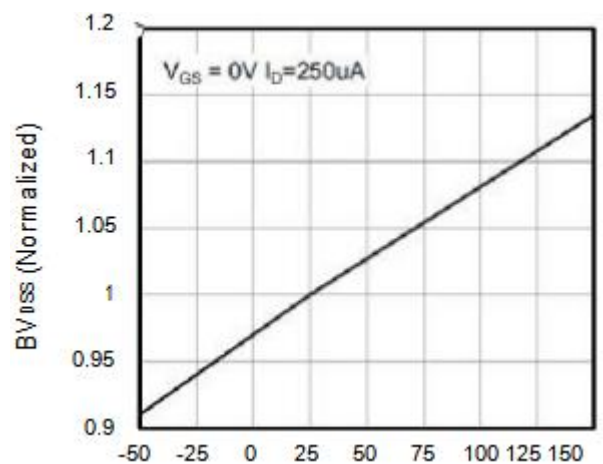


Figure 5. Transfer Characteristics

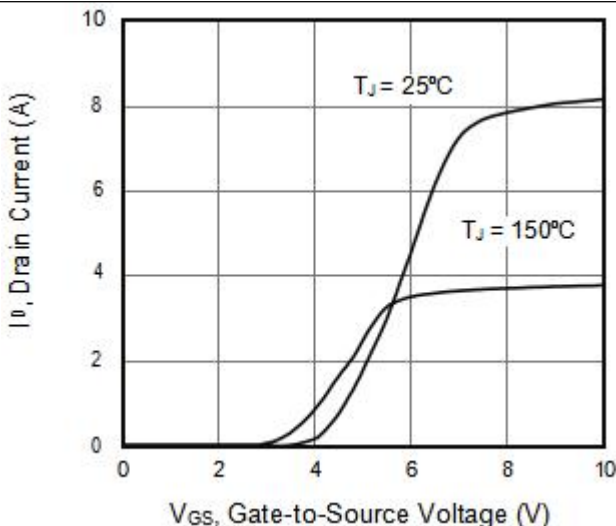


Figure 6. On-Resistance vs. Temperature

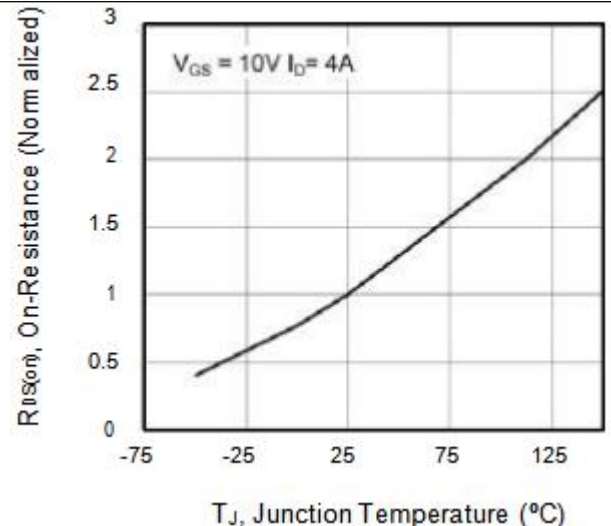


Figure 7. Capacitance

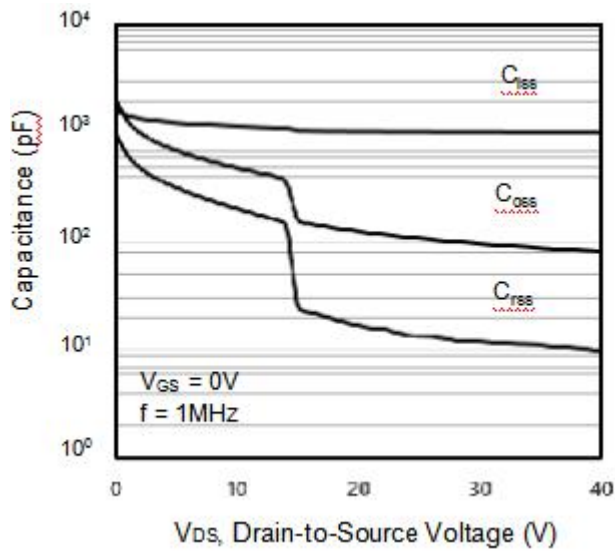


Figure 8. GateCharge

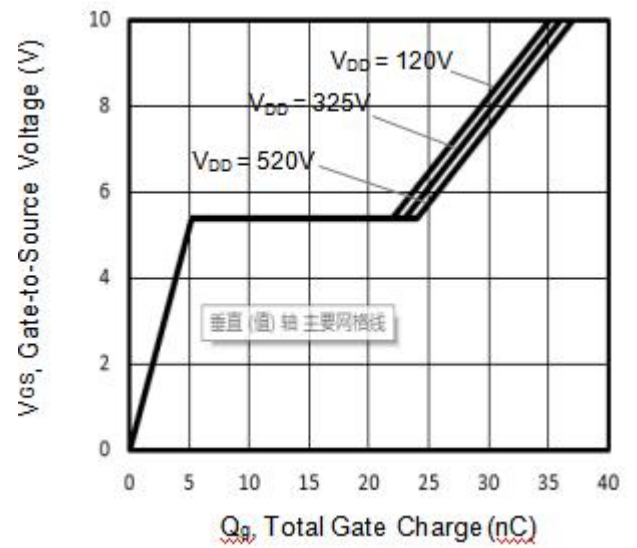


Figure 9. Transient Thermal Impedance

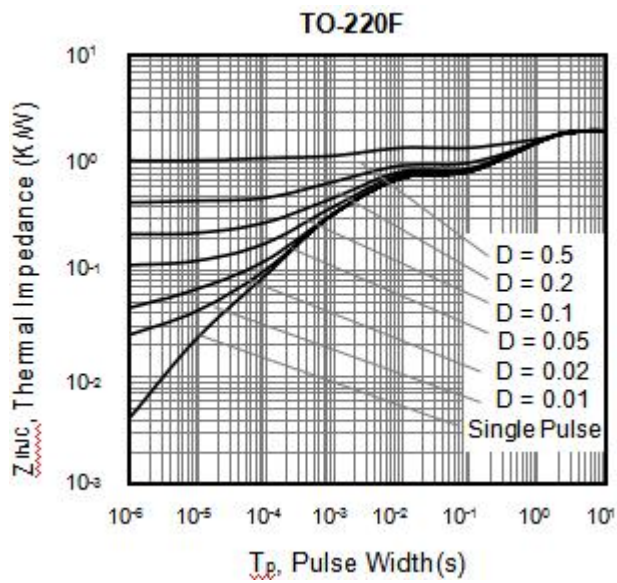


Figure A : Gate Charge Test Circuit and Waveform

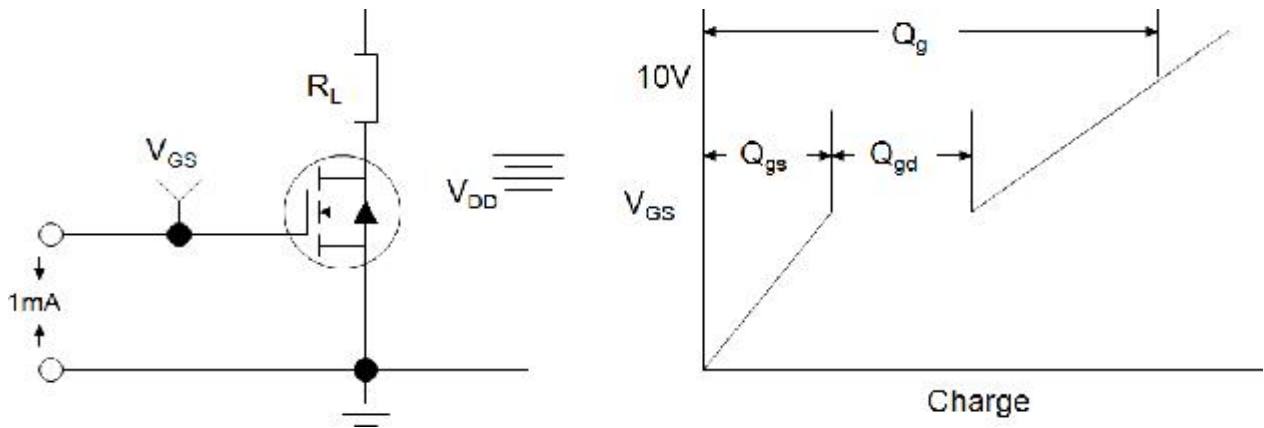


Figure B : Resistive Switching Test Circuit and Waveform

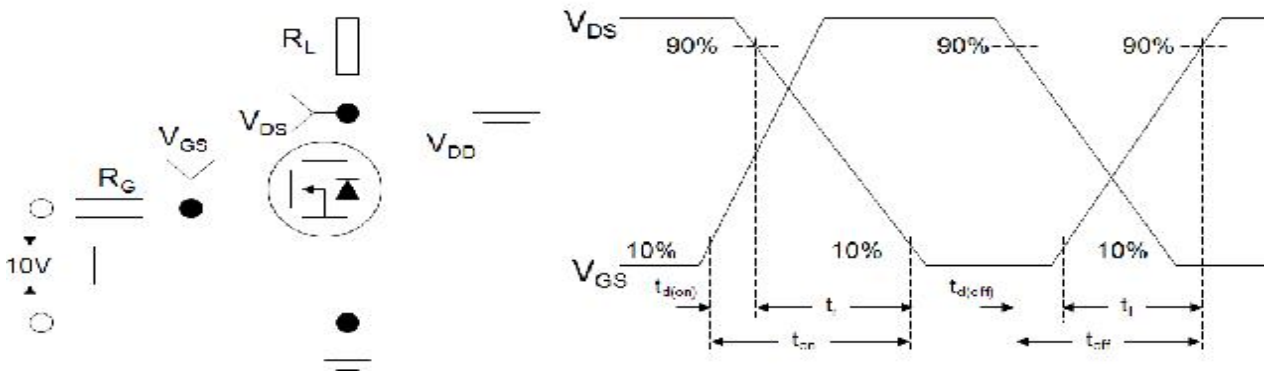
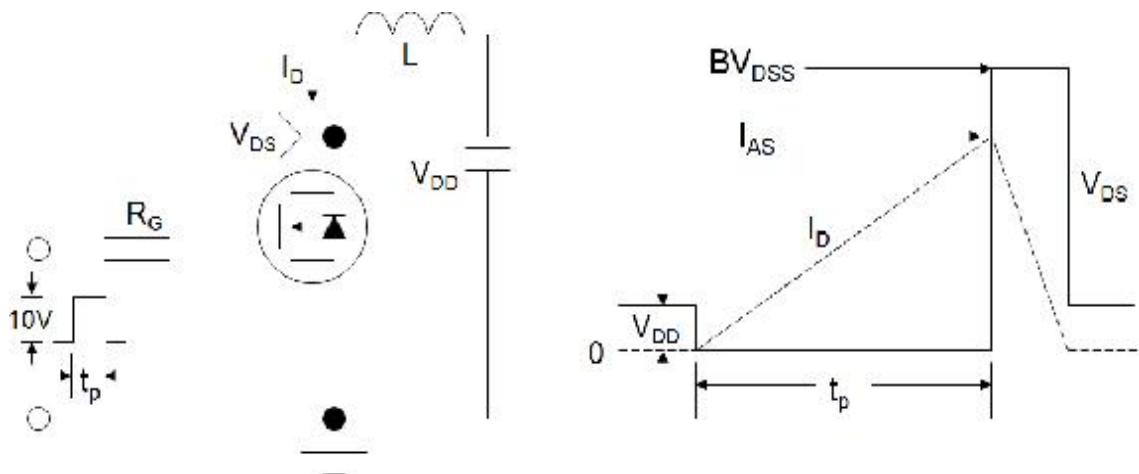
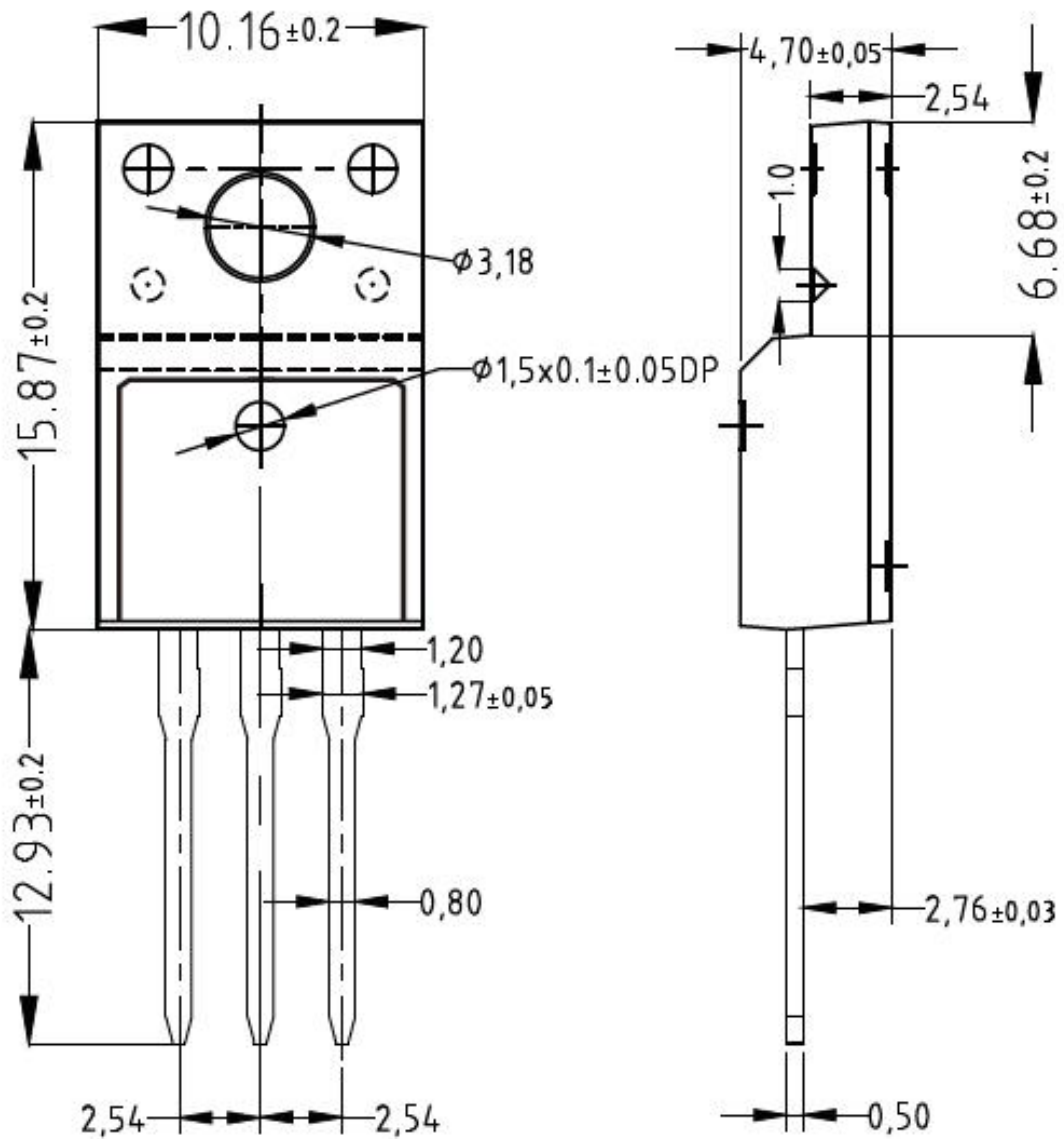
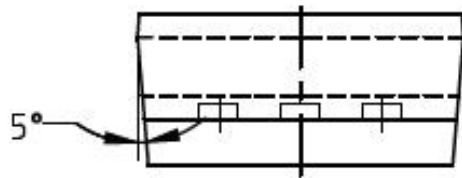


Figure C : Unclamped Inductive Switching Test Circuit and Waveform



Package Information





NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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