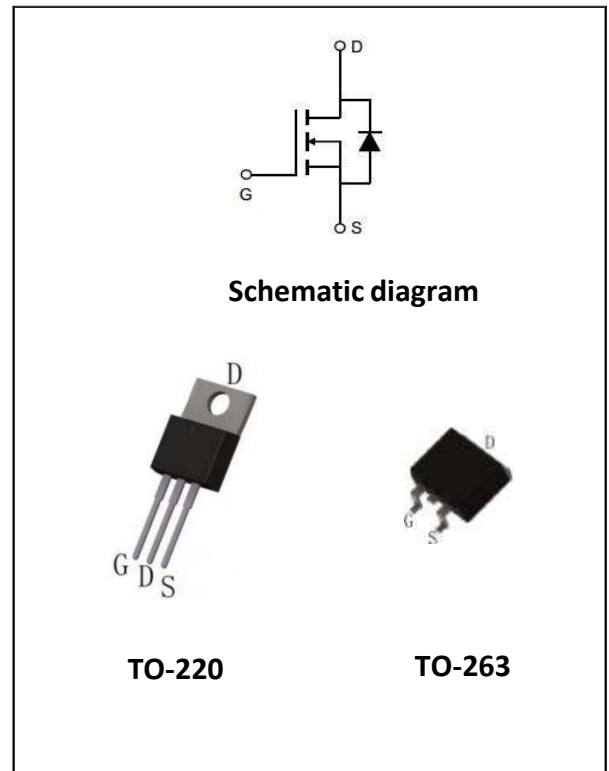


DESCRIPTION

The MPG100N07P uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

KEY CHARACTERISTICS

- ① $V_{DS} = 70V, I_D = 100A$
- ② $R_{DS(ON)} < 7.5m\Omega @ V_{GS}=10V$
- ③ Special process technology for high ESD capability
- ④ High density cell design for ultra low R_{dson}
- ⑤ Fully characterized avalanche voltage and current
- ⑥ Good stability and uniformity with high EAS
- ⑦ Excellent package for good heat dissipation



Application

- ① Power switching application
- ② Hard switched and High frequency circuits
- ③ Uninterruptible power supply

Package Marking And Ordering Information

Device Marking	Ordering Codes	Package	Product Code	Packing
100N07	MPG100N07-S	TO-263	MPG100N07S	Reel
100N07	MPG100N07-P	TO-220	MPG100N07P	Tube

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	70	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	100	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	340	A
Maximum Power Dissipation(Tc=25°C)	P_D	125	W
Single pulse avalanche energy ^(Note 2)	E_{AS}	370	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Case	$R_{\theta JC}$	1.2	$^{\circ}\text{C}/\text{W}$
-------------------------------------	-----------------	-----	-----------------------------

Electrical Characteristics (TA=25 $^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	$B_{V_{DS}}$	$V_{GS}=0\text{V}, I_D=250\mu\text{A}$	70	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=68\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20\text{V}, V_{DS}=0\text{V}$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2	3	4	V
Drain-Source On-State Resistance ^(Note 3)	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_D=45\text{A}$	-	6.5	7.5	m Ω
Forward Transconductance	G_{FS}	$V_{DS}=10\text{V}, I_D=20\text{A}$	-	20	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=30\text{V}, V_{GS}=0\text{V}, F=1.0\text{MHz}$	-	3200	-	pF
Output Capacitance	C_{oss}		-	440	-	pF
Reverse Transfer Capacitance	C_{rSS}		-	180	-	pF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=30\text{V}, I_D=30\text{A}, V_{GS}=10\text{V}, R_{GEN}=6\Omega$	-	16	-	nS
Turn-on Rise Time	t_r		-	95	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	47	-	nS
Turn-Off Fall Time	t_f		-	31	-	nS
Total Gate Charge	Q_g	$V_{DS}=30\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$	-	40	-	nC
Gate-Source Charge	Q_{gs}		-	11	-	nC
Gate-Drain Charge	Q_{gd}		-	15	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage(Note 3)	V_{SD}	$V_{GS}=0\text{V}, I_S=100\text{A}$	-	-	1.2	V

Notes:

- 1.Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2.EAS condition : Tj=25 $^{\circ}\text{C}$, VDD=30V, VG=10V, L=0.5mH, Rg=25 Ω
- 3.Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
- 4.Guaranteed by design, not subject to production.

Characteristics Curves

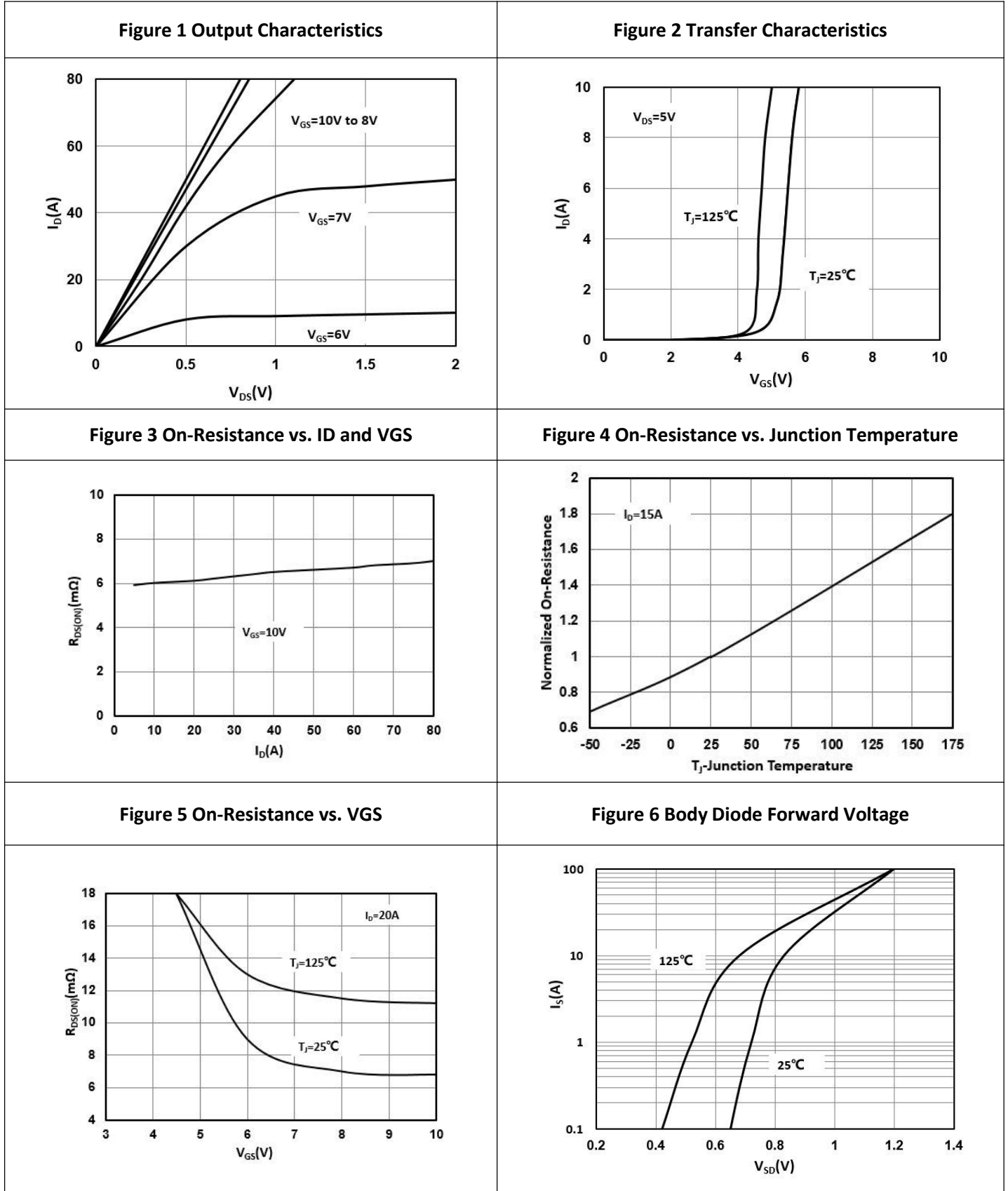


Figure 7 Gate-Charge Characteristics

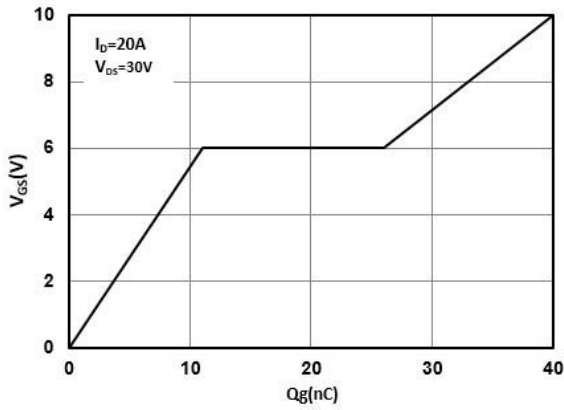


Figure 8 Capacitance Characteristics

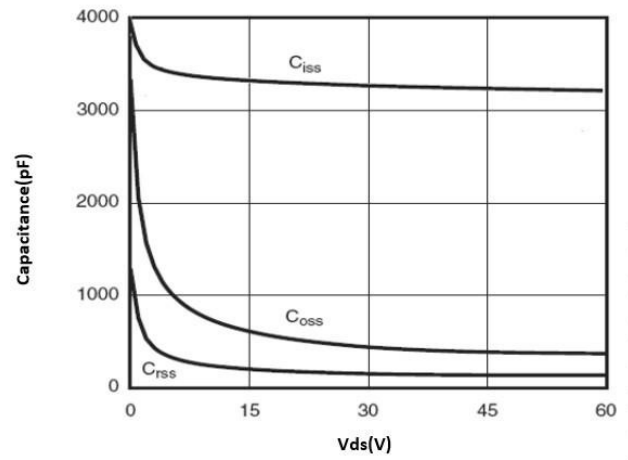


Figure 9 Maximum Forward Biased Safe Operation Area

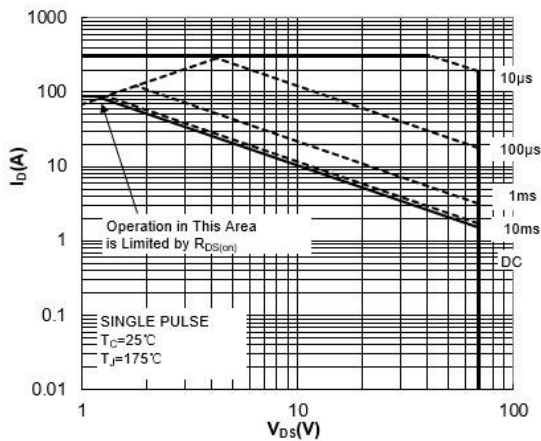


Figure 10 Single Pulse Power Rating Junction-to-Ambient

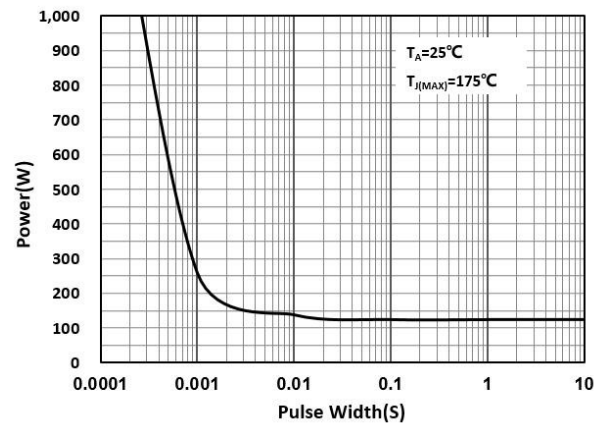
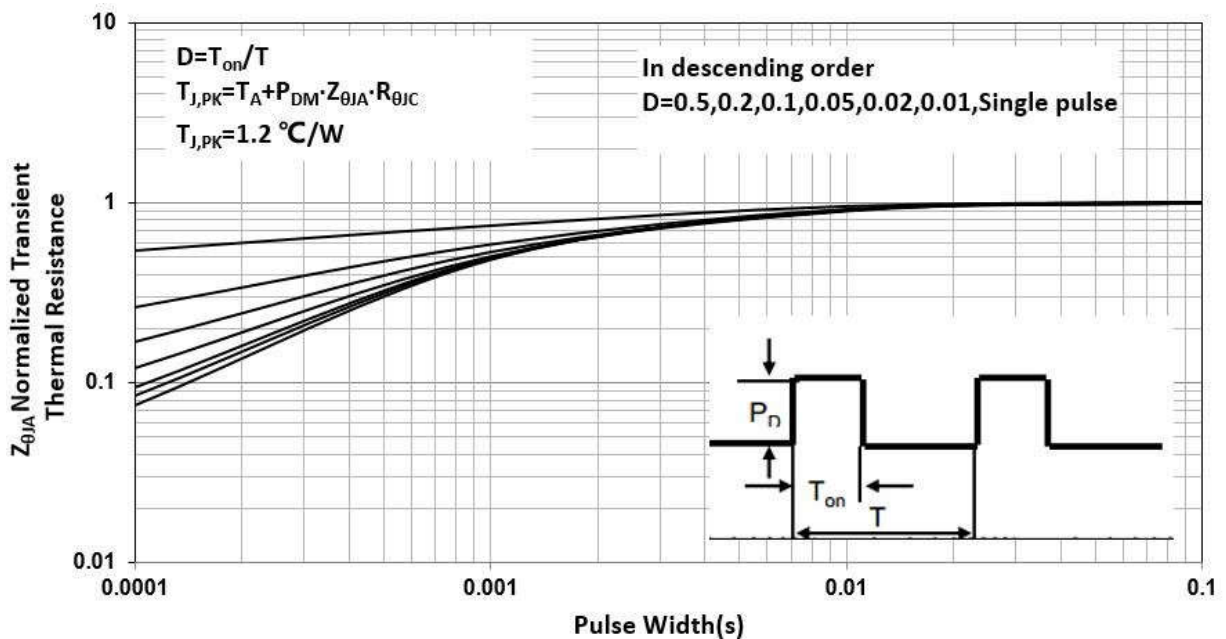
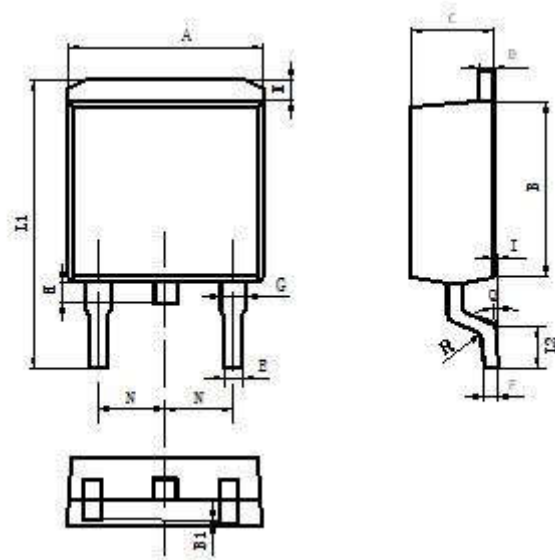


Figure 11 Normalized Maximum Transient Thermal Impedance

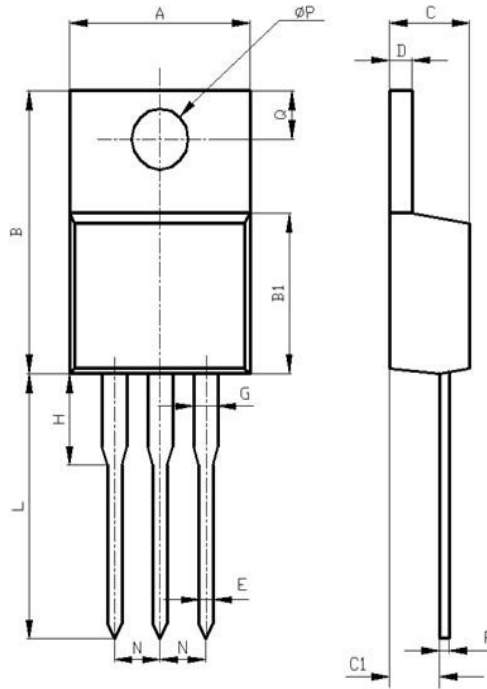


Package Description



Items	Values(mm)	
	MIN	MAX
A	9.80	10.40
B	8.90	9.50
B1	0	0.10
C	4.40	4.80
D	1.16	1.37
E	0.70	0.95
F	0.30	0.60
G	1.07	1.47
H	1.30	1.80
K	0.95	1.37
L1	14.50	16.50
L2	1.60	2.30
I	0	0.2
Q	0°	8°
R	0.4	0.4
N	2.39	2.69

TO-263 Package



Items	Values(mm)	
	MIN	MAX
A	9.60	10.6
B	15.0	16.0
B1	8.90	9.50
C	4.30	4.80
C1	2.30	3.10
D	1.20	1.40
E	0.70	0.90
F	0.30	0.60
G	1.17	1.37
H	2.70	3.80
L	12.6	14.8
N	2.34	2.74
Q	2.40	3.00
φp	3.50	3.90

TO-220 Package



NOTE:

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

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