

NPN SILICON TRANSISTOR

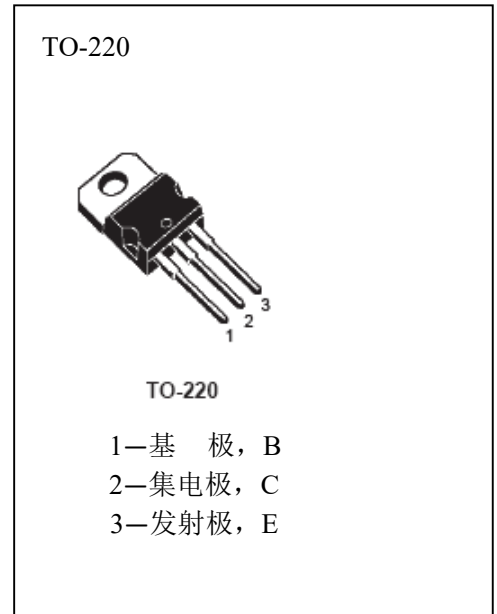
■ 主要用途

该器件为达林顿三极管内含阻尼二极管，用于高增益电路。

■ 外形图及引脚排列

■ 极限值 (Ta=25°C)

Tstg—贮存温度.....	-65~150°C
Tj—结温.....	150 °C
PC—集电极耗散功率 (Tc=25°C) .....	65W
PC—集电极耗散功率 (TA=25°C) .....	2W
VCBO—集电极—基极电压.....	100V
VCEO—集电极—发射极电压.....	100V
VEBO—发射极—基极电压.....	5V
IC—集电极电流.....	5A
ICP—集电极电流 (脉冲) .....	8A
IB—基极电流.....	120mA



■ 电参数 (Ta=25°C)

参数符号	符 号 说 明	最小值	典型值	最大值	单 位	测 试 条 件
BV <sub>CB0</sub>	集电极—基极击穿电压	100			V	IC=1mA, IE=0
BV <sub>CEO</sub>	集电极—发射极击穿电压	100			V	IC=5mA, IB=0
I <sub>CEO</sub>	集电极—发射极截止电流			0.5	mA	VCE=50V, IB=0
I <sub>CB0</sub>	集电极—基极截止电流			0.2	mA	VCB=100V, IE=0
I <sub>EBO</sub>	发射极—基极截止电流			2.0	mA	VEB=5V, IC=0
H <sub>FE</sub>	直流电流增益	1000				VCE=3V, IC=0.5A
V <sub>CE(sat1)</sub>	集电极—发射极饱和电压			2.0	V	IC=3A, IB=12mA
V <sub>CE(sat2)</sub>				4.0	V	IC=5A, IB=20mA
V <sub>BE(on)</sub>	基极—发射极导通电压			2.5	V	VCE=3V, IC=3A
C <sub>ob</sub>	共基极输出电容			200	pF	VCB=10V, IE=0, f=0.1MHz

■ 特性曲线

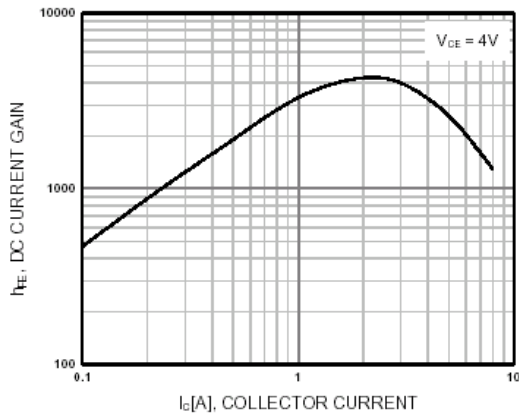


Figure 1. DC current Gain

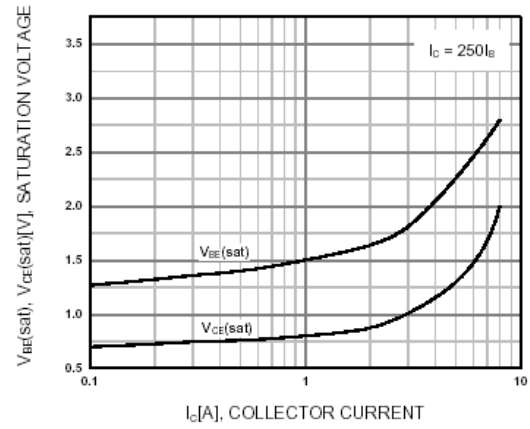


Figure 2. Base-Emitter Saturation Voltage  
Collector-Emitter Saturation Voltage

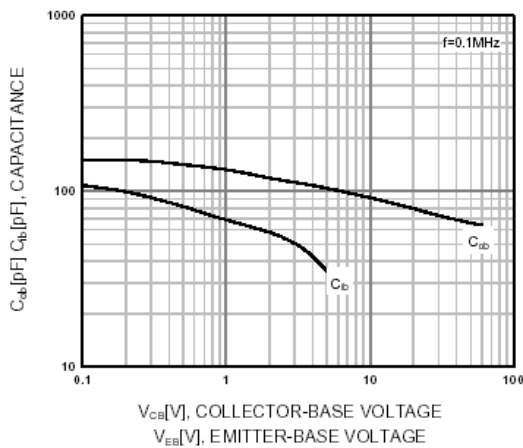


Figure 3. Output and Input Capacitance  
vs. Reverse Voltage

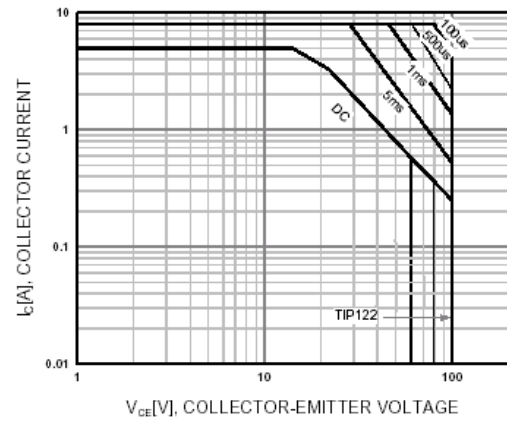


Figure 4. Safe Operating Area

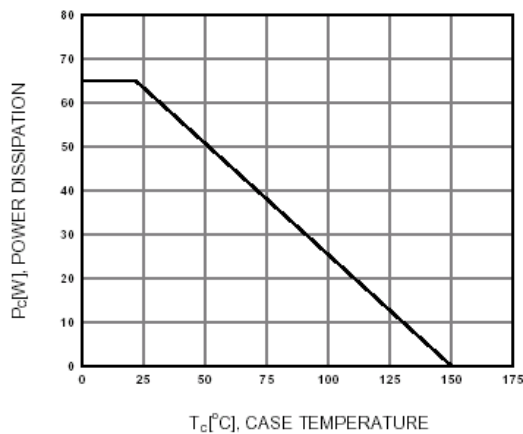


Figure 5. Power Derating

**NOTE:**

1. Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. Please do not exceed the absolute maximum ratings of the device when circuit designing.
2. When installing the heat sink, please pay attention to the torsional moment and the smoothness of the heat sink.
3. MOSFETs is the device which is sensitive to the static electricity, it is necessary to protect the device from being damaged by the static electricity when using it.
4. Shenzhen Minos reserves the right to make changes in this specification sheet and is subject to change without prior notice.

**CONTACT:**

**深圳市迈诺斯科技有限公司（总部）**

地址：深圳市福田区华富街道田面社区深南中路4026号田面城市大厦22B-22C

邮编：518025

电话：0755-83273777

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