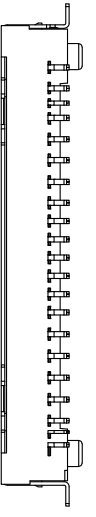
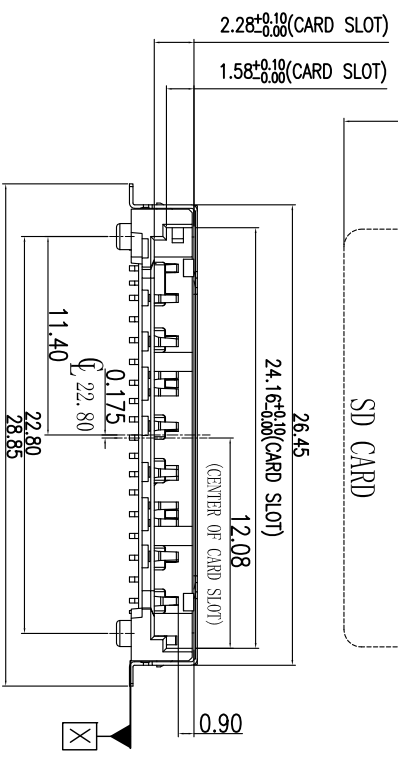
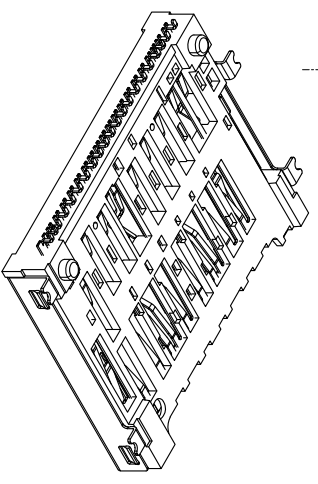
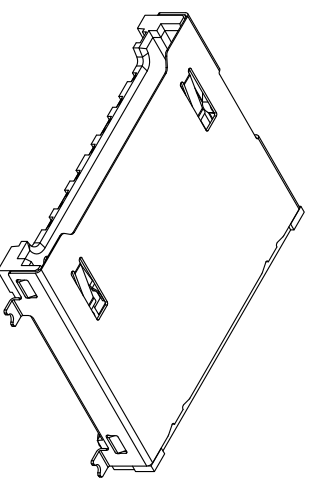
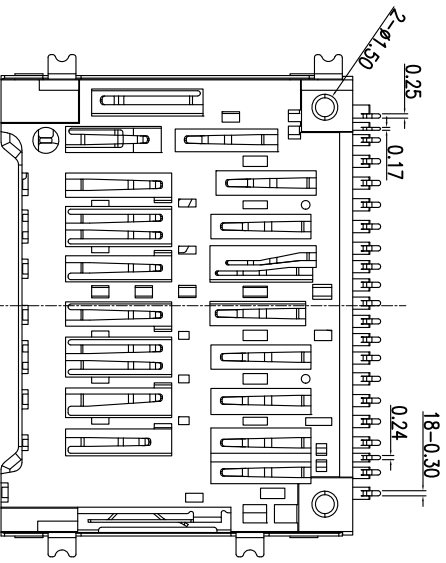
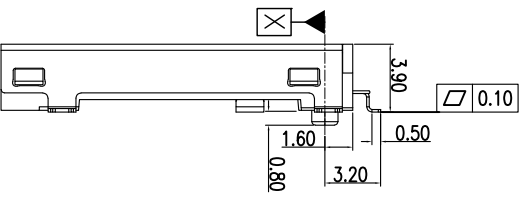
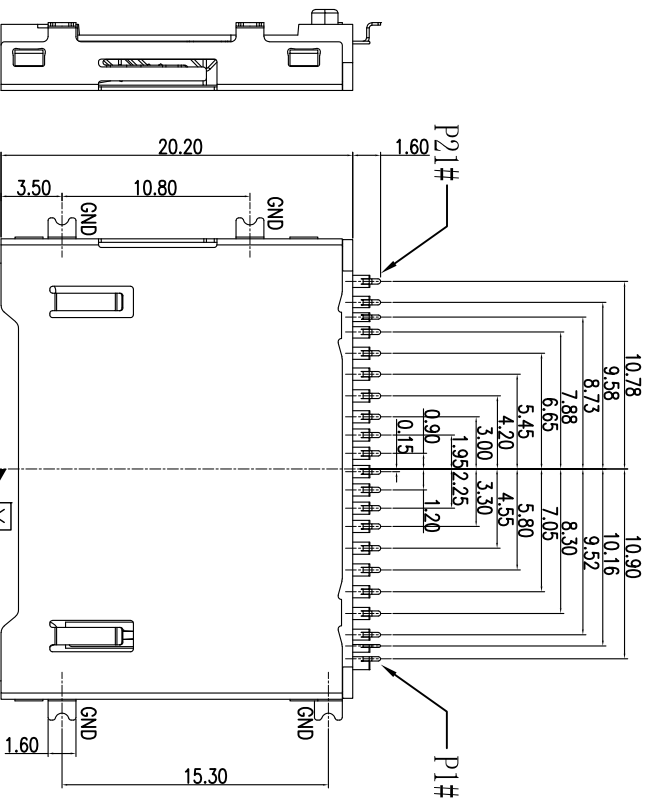


ROHS



REV.	EC#	DESCRIPTION	DATE	DRAWING	CHECKED	APPROVED
X0		初版发行	2019.12.24	JANYZ	WZX	ST

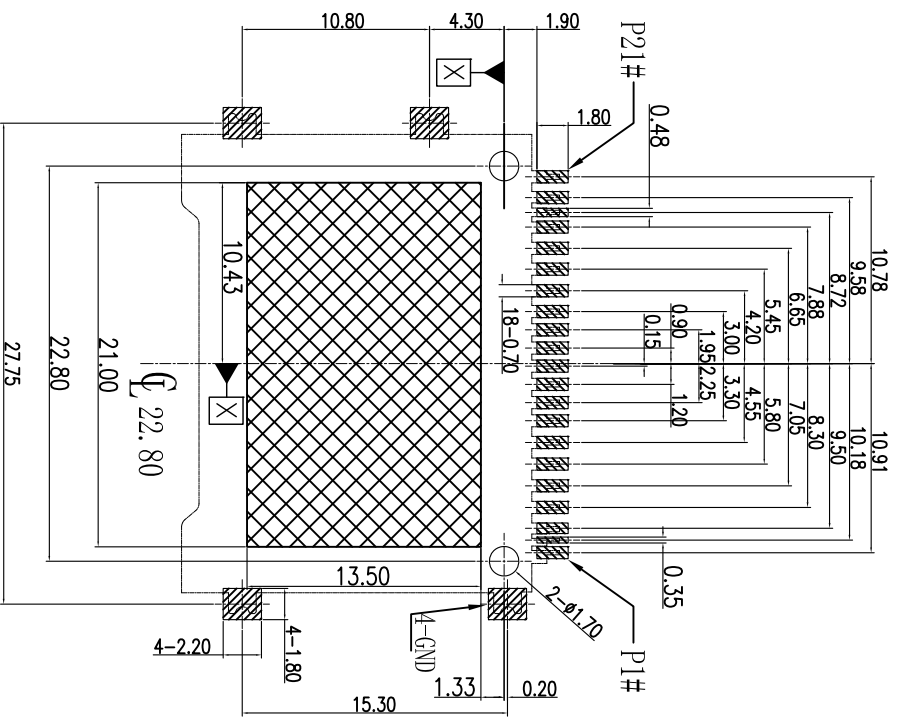


MILLIMETERS	INCH
X ± 2	X ± 2
X ± 0.35	.XX ± 0.015
.XX ± 0.25	.XXX ± 0.010
.XXX ± 0.10	.XXXX ± 0.004

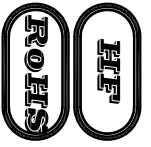
UNITS	MM
MATL	SEE NOTES
FINISH	SEE NOTES
QTY	SEE NOTES

MINTRON TECHNOLOGY CO.,LTD.	
PART NUMBER:	MT102013782
TITLE:	SD7.0 No Push H3.90 客户图
APPD:	
CHKD:	
DR:	
DWG NO.:	C-102013782-00

SCALE	1:1
SHEET	1/2
REV.	X0

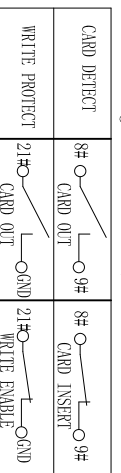


RECOMMEND P.C.B LAYOUT  
(General tolerance ±0.05)



- PAD AREA
- KEEP OUT AREA
- NO COPPER AREA
- (NO Trace&Via&GND)
- GND PATTERN ONLY

Circuit Diagram for CARD Detect/Write Protect Switch



- NOTES:
- 1.Operation Voltage :10V AC
  - 2.Current rating:1.0A Max
  - 3.Contact resistance:100mΩ MAX
  - 4.Dielectric withstanding voltage:500V AC/1minute
  - 5.Insulation resistance:1000MΩ min 500V DC
  - 6.Operation temperature:-20°C to 60°C
  - 7.Storage temperature:-20°C to 70°C
  - 8.Durability:10,000 CYCLES
  - 9.PLATING:
  - 9-1 CONTACT: 1u"OR30u" min.GOLD PLATED ON CONTACT AREA  
80u" min.TIN(MATTE) PLATING ON SOLDER TAIL AREA  
50u" min.NICKEL UNDERPLATING OVERALL
  - 9-2 SHELL :1u" min.GOLD PLATED ON SOLDER,30u" min. NICKEL PLATING OVERALL
  - 10.Recommending A Metal More Than 0.15mm Thick.  
Please Confirm Solderability, If Use A Metal Mask Less Than 0.15mm Thick.

No.	Pin Assignment	
	SD MODE	SD EXPRESS MODE
P1	VSS3 SUPPLY VOLTAGE GROUND	VSS3 SUPPLY VOLTAGE GROUND
P2		RESERVED
P3	DATA2 DATA LINE BIT2	CLKREQ# RESERVED AND FOR FUTURE PAD EXTENSION. ALSO USED BY L1 PM SUB-STATES
P4	DO+ DIFFERENTIAL DATA 0+	PCIE TX+
P5	CD/DATA3 CARD DETECT/DATA LINE BIT2	PERST# PE-RESET IS A FUNCTIONAL RESET
P6	DO- DIFFERENTIAL DATA 0-	PCIE TX-
P7	CMD COMMAND/RESPONSE	NOT USED
P8	CD CARD DETECT	CD CARD DETECT
P9	VSS1 SUPPLY VOLTAGE GROUND	VSS1 SUPPLY VOLTAGE GROUND
P10	VSS4 SUPPLY VOLTAGE GROUND	VSS4 SUPPLY VOLTAGE GROUND
P11	VDD2 SUPPLY VOLTAGE (1.8V)	VDD2 SUPPLY VOLTAGE (1.8V)
P12	VDD1 SUPPLY VOLTAGE (3.3V)	VDD1 SUPPLY VOLTAGE (3.3V)
P13	D1- DIFFERENTIAL DATA 1-	PCIE RX-
P14	CLK CLOCK	NOT USED
P15	D1+ DIFFERENTIAL DATA 1+	PCIE RX+
P16	VSS2 SUPPLY VOLTAGE GROUND	VSS2 SUPPLY VOLTAGE GROUND
P17	VSS5 SUPPLY VOLTAGE GROUND	VSS5 SUPPLY VOLTAGE GROUND
P18	DATA0 DATA LINE BIT0	REFCLK+
P19		REFCLK+
P20	DATA1 DATA LINE BIT1	VDD2 SUPPLY VOLTAGE (1.2V)-TBD
P21	WP WRITE PROTECT	REFCLK- PCIE REF CLOCK WRITE PROTECT

MINTRON TECHNOLOGY CO.,LTD.

PART NUMBER: MT102013782  
TITLE: SD7.0 No Push H3.90客户图

APPD: C-102013782-00  
CHKD: DWG NO.:  
QTY: SEE NOTES  
DR: SCALE: 1:1  
SHEET: 2/2  
REV: X0

MT102303902	Alt 30u"
MT102013782	Alt 1u"
PART NO	PART PLATED

1 2 3 4 5

A B C D E

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