**TENTATIVE** 

All information in this technical data sheet is tentative and subject to change without notice.

# 7.0" WVGA

## TECHNICAL SPECIFICATION

## AA070MC01-DA1

## MITSUBISHI ELECTRIC CORPORATION

Date: Nov.25,'15

AA070MC01-DA1\_02\_00

**MITSUBISHI ELECTRIC** 

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## **1. APPLICATION**

This specification applies to color TFT-LCD module, AA070MC01-DA1.

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MITSUBISHI's TFT-LCD module is designed and produced for "General Application" which is described as below.

#### <General Application>

Computers, office automation equipment, factory automation equipment, test and measurement equipment, communications, medical equipment which has no impact for human life and body, display equipment utilized in transportation system (automobiles, ships, trains, etc) which has no influence on its operation directly or indirectly.

Therefore, MITSUBISHI do not guarantee using our TFT-LCD for other application such as medical equipment, safety equipment, transportation system which might be influenced for its operation because of LCD-TFT, military systems, defense equipment, aerospace equipment, nuclear reactor control systems, and any other application in which there is a direct or indirect risk for human life and body or where high levels of reliability are required. Please confirm your application before using MITSUBISHI's TFT-LCD.

Furthermore, it is mandatory to make contract (document base) by both parties (customer and MITSUBISHI), in case if customers intend to use our TFT-LCD for such application as military systems, defense equipment, aerospace equipment and nuclear reactor control systems, "which has significant impact for human life and body". Otherwise, please do not use MITSUBISHI's TFT-LCD for any cases.

MITSUBISHI has been making continuous effort to improve the reliability of its products. However customers should implement sufficient reliability design of their application equipments such as redundant system design, fail-safe functions and anti-failure features.

MITSUBISHI assumes no responsibility for any damage, including but not limited to damage to human life and body, resulting from the use of the product for the application other than the above mentioned General Application or the use of the product that does not comply with the instructions and the precautions specified in this document.

## 2. OVERVIEW

AA070MC01-DA1 is 7.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs, control circuit, LED driver, backlight unit, touch panel, and cover glass.

By applying 6 bit or 8 bit digital data,  $800 \times 480$ , 262k-color or 16.7M-color images are displayed on the 7.0" diagonal screen. Input power voltages are 3.3 V for LCD driving, 12.0 V for backlight unit and 5.0 V for touch panel controller. The type of data and control signals are digital and transmitted via LVDS interface per Typ. 30.4 MHz clock cycle.

This product has LED driving circuit inside. The LED driving circuit has PWM light dimming function, which is controlled by PWM.

	ITEM	SPECIFICATION	
Display Area (mm	1)	152.4 (H) × 91.44 (V) (7.0-inch diagonal)	
Number of Dots		$800 \times 3 \text{ (H)} \times 480 \text{ (V)}$	
Pixel Pitch (mm)		$0.1905 (H) \times 0.1905 (V)$	
Color Pixel Arran	gement	RGB vertical stripe	
Display Mode		Normally black	
Number of Color		262k(6 bit/color), 16.7M(8 bit/color)	
Luminance (cd/m <sup>2</sup>	2)	800	
Viewing Angle (C	$R \ge 10)$	-85~85°(H), -85~85°(V)	
	Cover Glass Surface	Clear	
Comer Class	Thickness (mm)	1.1	
Cover Glass	Glass Type	Strengthened glass	
	Surface Hardness	$5\mathrm{H}$	
Electrical Interfac	ce	LVDS (6 bit/8 bit)	
Module Size (mm)	)	189.8 (W) × 129.7 (H) × 13.6 (D)	
Module Mass (g)		350	
Backlight Unit		LED, edge-light	
Touch Panel		Projective capacitive	
Touch Panel Inter	rface	UART / USB *1)	

General specifications are summarized in the following table:

Characteristic value without any note is typical value.

\*1) UART: Universal Asynchronous Receiver Transmitter UART and USB are used exclusively.

## **3. ABSOLUTE MAXIMUM RATINGS**

ITEM	SYMBOL	MIN.	MAX.	UNIT
Power Supply Voltage for LCD	VCC	-0.3	4.0	V
Logic Input Voltage	VI	-0.3	VCC+0.3	V
Backlight Power Supply Input Voltage	VL	-0.3	14.0	V
Backlight ON-OFF	BLEN	-0.3	14.0	V
Light Dimming Control (PWM) Input Voltage	V pdim	-0.3	14.0	V
Touch Panel Voltage	VDD5	0	6.0	V
Touch Panel Input Voltage	$VI_{TP}$	-0.3	VDD5+0.3	V
Operation Temperature Note 1,2)	$T_{op}$	-30	70	°C
Storage Temperature Note 2)	$\mathrm{T}_{\mathrm{stg}}$	-30	80	°C

[Note]

1) MIN.: Measured at the center of panel surface, MAX.: Measured at the center of panel back surface

2) Top,Tstg  $\leq 40^{\circ}$ C : 90%RH max. without condensation

Top, Tstg > 40°C : Absolute humidity shall be less than the value of 90% RH at 40°C without condensation.

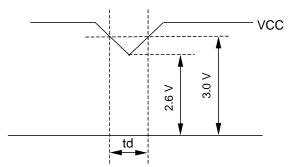
## 4. ELECTRICAL CHARACTERISTICS

(1) TFT-LCD Ambient temperature: Ta = 25°C						e: Ta = 25°C	
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks	
Power Supply Voltages	VCC	3.0	3.3	3.6	V	*1)	
Power Supply Currents	s for LCD	ICC		280	450	mA	*2)
Permissive Input Ripp	le Voltage	VRP			100	mVp-p	VCC = +3.3V
Logio Input Voltogo	High	VIH	0.8×VCC		VCC	V	MODE, SC
Logic Input Voltage	Low	VIL	0		0.2×VCC	V	MODE, SC
*1) Power and signals sequence: 0.1 ms $\leq$ t1 $\leq$ 10 ms 0 $\leq$ t2 $\leq$ 50 ms 0 $\leq$ t3 $\leq$ 50 ms 0 $\leq$ t6 LCD Power Supply Logic Signal 0.1VCC 11 t2 t3 t4 LCD Power Supply LCD Power							
Backlight Power Supply							

data: RGB DATA, DCLK, DENA, MODE, SC

VCC-dip conditions:

When VCC < 2.6V or VCC < 3.0V and 10 ms < td, this product may not work normally. Please reset power supply according to the power and signals sequence (see 4.(1)\*1)).



\*2) VCC = +3.3 V ,  $\rm f_{H}{=}31.7$  kHz,  $\rm f_{V}{=}60$  Hz,  $\rm f_{CLK}{=}30.4$  MHz

Display image at typical power supply current value is 256-gray-bar pattern (8 bit), 480 line mode.

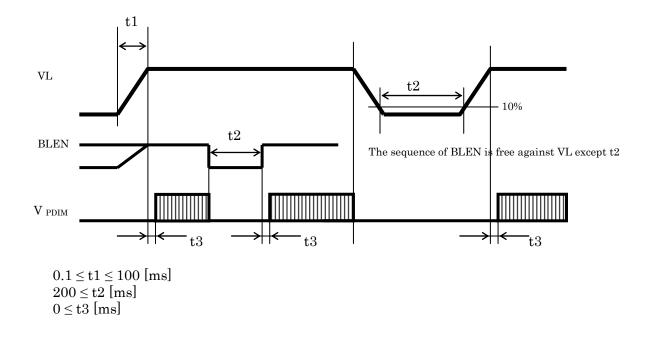
*3)	Fuse
-----	------

Parameter	Fuse Type Name	Supplier	Remark
VCC	FCC16162AB	Kamaya Electric Co., Ltd.	*)

\*) The power supply capacity should be designed to be more than the fusing current.

2) Backlight Ta=25°C							
ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Power Supply Input Vo	oltage	VL	10.8	12.0	13.2	V	*1)
Power Supply Input C	urrent	IL		350	430	mA	Dimming=100% VL=12.0V
Power Supply Input Cu (Rush Current)	ırrent	ILR			(900)	mA	*2), VL=12.0V
Backlight ON-OFF	High	BLEN	2.5		VL	V	*1), ON
backlight ON-OFF	Low	DLEN	0		0.4	V	*1), OFF
Light Dimming	2 0		2.5		5.5	V	*1), ON
Control (PWM) Input Voltage Low		V PDIM	0		0.4	V	*1), OFF
PWM frequency		f pdim	100	500	1000	Hz	*3)
Dimming Ratio(PWM Duty)		DR	5		100	%	*3)
LED Life Time		LT	80,000	100,000		h	*4), *5)

\*1) Power and signals sequence:



- \*2) These data are for reference only. Power Supply Input Current (Rush Current) is influenced by power supply characteristics. (output impedance in particular) Therefore please evaluate LCD in advance.
- \*3) lower frequency causes the flicker or the image breaking of motion picture. Depending on the PDIM signal integrity (jitter etc.), the flicker may be visible. Please evaluate in advance.
- \*4) LED life time is defined as the time when the brightness becomes 50% of the initial value.
- \*5) The life time of the backlight depends on the ambient temperature. The life time will decrease under low/high temperature.

\*6) Fuse

Parameter	Fuse Type Name	Supplier	Remark
VL	FCC16162AB	Kamaya Electric Co., Ltd.	*)

\*) The power supply capacity should be designed to be more than the fusing current.

(3) Touch Panel

Electrical Characteri	stics				Ambient te	mperatur	e: Ta = 25°C
ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	Remarks
Touch Panel Voltage		VDD5	4.5	5.0	5.5	V	*1)
Touch Panel Current		ICCtp		50	200	mA	
Permissive Input Ripple Voltage		VRPtp			100	mVp-p	VDD5 = +5.0 V *2)
Logic Input Voltage	High	VIHtp	$0.8 \times VDD5$		VDD5	V	CKW, SC, DIN,
Logic Input Voltage	Low	VILtp	0		$0.2 \times VDD5$	V	RESET, *3)
Logic Output Current	High	IOH	-5.0		0	mA	DOUT
Logic Output Current	Low	IOL	0		5.0	mA	*4)
Multi-Touch Points				2		point	
Position Accuracy		$\Delta Ex$	-3.0		3.0	mm	Inner area*5)
Fosition Accuracy		$\Delta Ey$	-4.5		4.5	mm	Outer frame*5)
Position Coordinate				100		ana	Single touch *6)
Output Rate (standard	Output Rate (standard)			60		$\operatorname{sps}$	Dual touch
Dual Touch Detection Distance		∆dx ∆dy	35			mm	*5)
*1) Power and signals set $0.1 \text{ ms} \le t1 \le 10 \text{ ms}$ $0 < t2 \le 50 \text{ ms}$ $0 < t3 \le 50 \text{ ms}$	-	$\begin{array}{l} 200 \mbox{ ms} \leq \\ 2000 \mbox{ ms} \\ 0 \leq t6 \end{array}$					
$0.9 \times \text{VDD5} \qquad \qquad \qquad 0.9 \times \text{VDD5}$							
$0.1 \times \text{VDD5}$ $ 0.1 \times \text{VDD5}$ $-$							
Touch panel operating period							

Initialization of touch panel controller (calibration of touch panel) is carried out during period between power supply turning on and start of touch panel operation (t1+t2+t5), therefore please do not touch surface with finger, hold hands near touch surface, nor put conductive material like metal on touch panel.

t6

t3

If the calibration is not able to be carried out successfully at the initialization process, touch panel may not work properly for sometime.

- \*2) Ripple noise of touch panel power supply affects stability of touch detection and position accuracy. Therefore please use stabilized power supply to touch panel.
- \*3) Applied to CKW(2pin),SC(3pin),DIN(5pin),RESET(9pin). For, please input signal of USB2.0 compliance to D- (10pin) & D+(11pin).

t2

t1

t5

\*4) Applied to DOUT(6 pin).

Logic Signal

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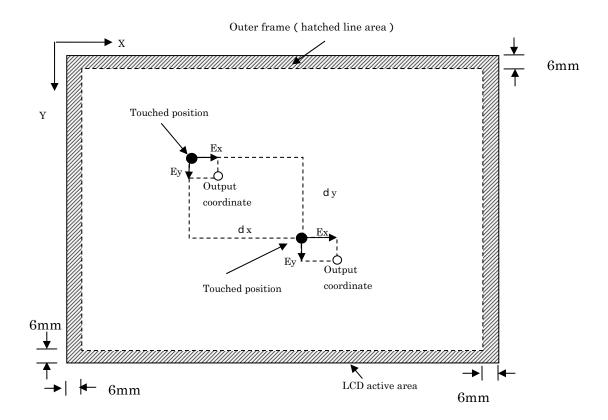
t4

\*5) Area of the finger touch is based on 10 mm in diameter.

Linearity is written as the difference of an actual touch position and the position coordinate which a touch controller outputs as an error ( $\Delta Ex$  and  $\Delta Ey$  stand for error length in the direction of X, Y, respectively). Dual-point touch detection distance is shown in following figure.

The coordinates accuracy of peripheral part is valid when one-point touched.

\* External noise may impact the coordinate accuracy significantly.



\*6) The time interval of touch position coordinate output under an initial parameter condition

*7)	Fuse			
	Parameter	Fuse Type Name	Supplier	Remark
	VDD5	FCC16501AB	Kamaya Electric Co., Ltd.	*)

\*) The power supply capacity should be designed to be more than the fusing current.

## **5. INTERFACE PIN CONNECTION**

(1) CN 1(Interface Signal)

Used connector: 20186-020E-11F (I-PEX) or FI-SEB20P-HFE (JAE)

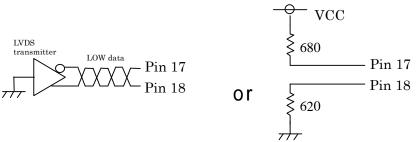
Corresponding connector: 20197-\*20U-F(I-PEX) or FI-S20S[for discrete Wire],

	FI-SE20ME [for FPC] (JAE)					
Pin	Growbal	Function (ISP 6 bit	compatibility mode)	Function (ISP 8 bit		
No.	Symbol	6 bit input 8 bit input		compatibility mode)		
1	VCC	+3.3 V Po	wer supply	<i>←</i>		
2	VCC	+3.3 V Po	wer supply	$\leftarrow$		
3	GND	Gl	ND	$\leftarrow$		
4	GND	Gl	ND	$\leftarrow$		
5	Link 0–	R0, R1, R2, R3, R4, R5, G0	R2, R3, R4, R5, R6, R7, G2	R0, R1, R2, R3, R4, R5, G0		
6	Link 0+	R0, R1, R2, R3, R4, R5, G0	R2, R3, R4, R5, R6, R7, G2	R0, R1, R2, R3, R4, R5, G0		
7	GND	Gl	ND	$\leftarrow$		
8	Link 1–	G1, G2, G3, G4, G5, B0, B1	G3, G4, G5, G6, G7, B2, B3	G1, G2, G3, G4, G5, B0, B1		
9	Link 1+	G1, G2, G3, G4, G5, B0, B1	G3, G4, G5, G6, G7, B2, B3	G1, G2, G3, G4, G5, B0, B1		
10	GND	Gl	$\leftarrow$			
11	Link 2–	B2, B3, B4, B5, DENA	B4, B5, B6, B7, DENA	B2, B3, B4, B5, DENA		
12	Link 2+	B2, B3, B4, B5, DENA	B4, B5, B6, B7, DENA	B2, B3, B4, B5, DENA		
13	GND	Gl	ND	←		
14	CLKIN-	Clo	ck –	$\leftarrow$		
15	CLKIN+	Clo	ck +	$\leftarrow$		
16	GND	Gl	ND	$\leftarrow$		
17	Link3–	See: *2)	R0, R1, G0, G1, B0, B1	R6, R7, G6, G7, B6, B7		
18	Link3+	See: *2)	R0, R1, G0, G1, B0, B1	R6, R7, G6, G7, B6, B7		
19	MODE	Low=ISP 6 bit c	ompatibility mode	High=ISP 8 bit compatibility mode		
20	SC	Scan direction control (Lo	w=Normal, High=Reverse)			

FI-SE20ME [for FPC] (JAE)

\*1) Metal frame is connected to signal GND.

\*2) Recommended wiring of Pin 17,18 (6 bit input)



(2) CN 2(Backlight)

Backlight-side connector: FI-S6P-HFE (JAE) Corresponding connector: FI-S6S (JAE)

Pin No.	Symbol	Function
1	VL	Power Supply Input Voltage
2	VL	Power Supply Input Voltage
3	GNDL	GND
4	GNDL	GND
5	BLEN	Backlight ON-OFF (High: ON, Low: OFF)
6	V pdim	Light Dimming Control (PWM) Input Voltage (High active)

\*1) GNDL is connected GND (of CN1) and the LCD frame internally.\*2) BLEN is NOT designed for dimming.

## (3) CN3 (Touch Panel Interface)

		/	
Used co	nnector: SM12	2B-SHLS-TF (I	LF)(SN) (JST)
Corresp	onding connec	tor: SHLP-12V	/-S-B (JST)

Pin	Symbol	Function	Connection to host equipment *4					
No.	Symbol	Function	UART	USB				
1	VDD5	Touch panel power supply(5V) *5)	Power supply 5V	NC				
2	CKW	Rotation of coordinate (Clockwise) *3)	CKW	CKW				
3	$\mathbf{SC}$	Reverse of coordinate *3)	$\mathbf{SC}$	SC				
4	GND	Touch panel controller GND	GND GND					
5	DIN	UARTreceive (H:5V, L:0V) *1)	DIN	NC				
6	DOUT	UART send (H:5V, L:0V) *1)	DOUT	NC				
7	TEST1	(Internal use) *2)	NC	NC				
8	TEST2	(Internal use) *2)	NC	NC				
9	RESET	Touch panel reset (H: Usually, L: Reset)	RESET	RESET				
10	D-	USB D– Terminal	NC	D-				
11	D+	USB D+ Terminal	NC	D+				
12	VUSB (VBUS)	USB power supply (5V) *5)	NC	Power supply 5V				

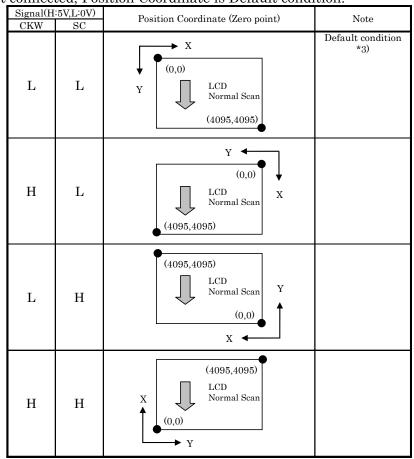
\*1) Direction of signal;

DIN (5pin): Host equipment  $\rightarrow$  Touch panel controller

DOUT (6pin): Controller  $\rightarrow$  Host equipment

\*2) Please don't use TEST1 (7pin) and TEST2 (8pin) because they are for internal use only.\*3) CKW and SC are signals to change zero point of touch panel position coordinate.

If they are not connected, Position Coordinate is Default condition.



\*4) UART and USB communication are exclusive and connection methods are different. NC should be open.

\*5) VDD5(1pin) and VUSB(12pin) are connected together on the touch-panel controller board.

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Specification of communication between the controller and host are shown below.

## • UART

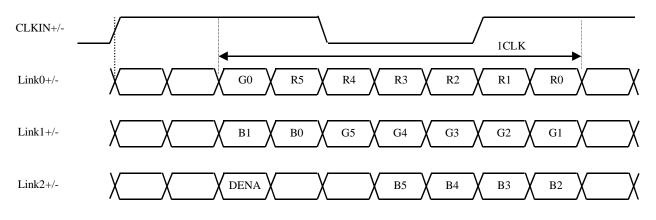
Item	Specifications
Communication method	UART
Communication speed	38400bps
Data length	8 bit
Stop bit	1 bit
parity	None

## • USB

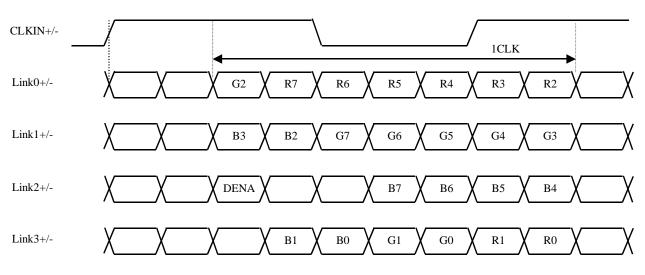
Please follow USB 2.0 standard.

### (4) ISP data mapping

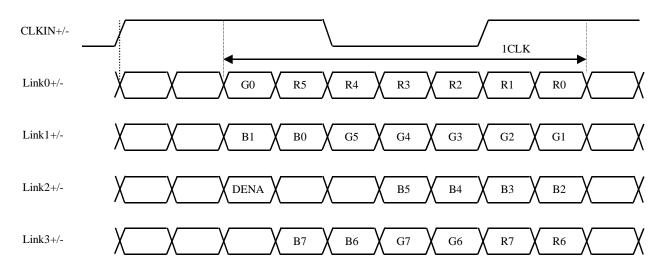
a. ISP 6 bit compatibility mode(6 bit input)



#### b. ISP 6 bit compatibility mode(8 bit input)



c. ISP 8 bit compatibility mode



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## 6. INTERFACE TIMING

LVDS transmitter input signal

(1) Timing Specifications	s
---------------------------	---

	ITEM		SYMBOL	MIN	TYP	MAX	UNIT
	Frequency		fclk	25	30.4	45	MHz
DCLK	Period		tclk	22.2	32.9	40	ns
		Active Time	$t_{ m HA}$	800	800	800	$\mathbf{t}_{\mathrm{CLK}}$
	Horizontal	Blanking Time	$t_{ m HB}$	20	160		tclk
	morizontai	Frequency	${ m f}_{ m H}$	26.4	31.7	45	kHz
		Period	$t_{ m H}$	22.2	31.6	37.9	$\mu s$
DENA		Active Time	tva	480	480	480	tн
	Vertical	Blanking Time	$t_{VB}$	3	48		$\mathbf{t}_{\mathrm{H}}$
	vertical	Frequency		55	60	75	Hz
		Period	$t_V$	13.3	16.7	18.2	ms

[Note]

1) DENA (Data Enable) should always be positive polarity as shown in the timing specification.

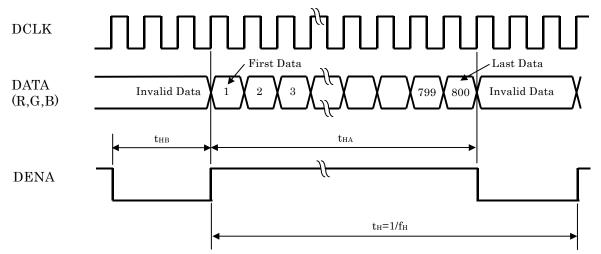
2) DCLK should appear during all invalid period.

3) LVDS timing follows the timing specifications of LVDS receiver IC: THC63LVDF84B(Thine).

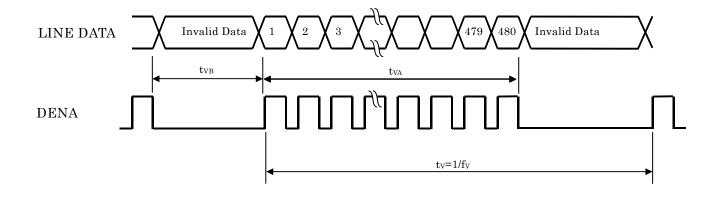
4) In case of blanking time fluctuation, please satisfy following condition.  $t_{VBn} > t_{VBn-1} - 3(t_H)$ 

## (2) Timing Chart

## a. Horizontal Timing Chart



## b. Vertical Timing Chart



## (3) Color Data Assignment

### <u>a. 6 bit input</u>

	<u>input</u>								IN	VPUT	' DA'I	Ά							
	R DATA								G D	ATA			B DATA						
С	OLOR	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
BASIC	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
COLOR	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
RED															-				
															-				
	RED(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
GREEN													-						
													-		-				
	GREEN(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE															-		-		
													-				-		
	BLUE(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	BLUE(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

[Note]

1) Definition of gray scale

Color(n) - n indicates gray scale level.

Higher n means brighter level.

2) Data

1:High, 0: Low

<u>b. 8 bit input</u>

												INI	PUT	' DA	TA										
C	OLOR			1	R DA	<b>ATA</b>						(	G D.	АТА	L					]	B D.	ATA	1		
	JLOK	R7	R6	R5	R4	R3	R2	$\mathbf{R}1$	R0	G7	G6	G5	$\mathbf{G4}$	G3	G2	G1	G0	B7	B6	B5	<b>B</b> 4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BASIC	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
COLOR	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
GREEN																									
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
BLUE																									
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

[Note]

1) Definition of gray scale

Color(n) - n indicates gray scale level.

Higher n means brighter level.

## 2) Data

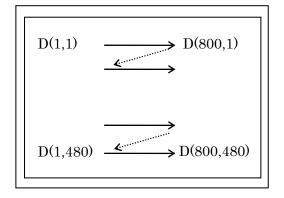
1:High, 0: Low

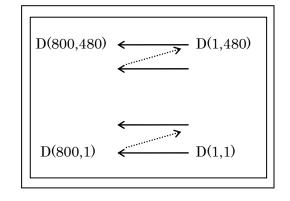
### (4) Display Position and Scan Direction

D(X,Y) shows the data number of input signal.

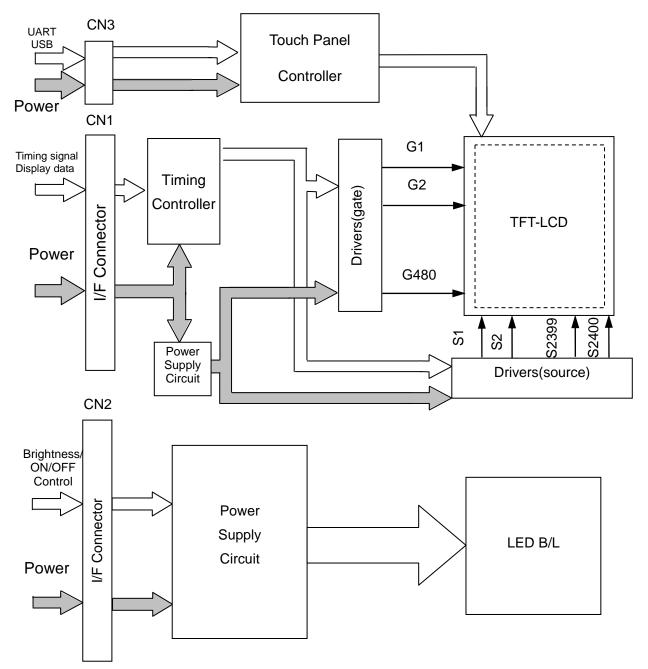
SC: Low

## SC: High



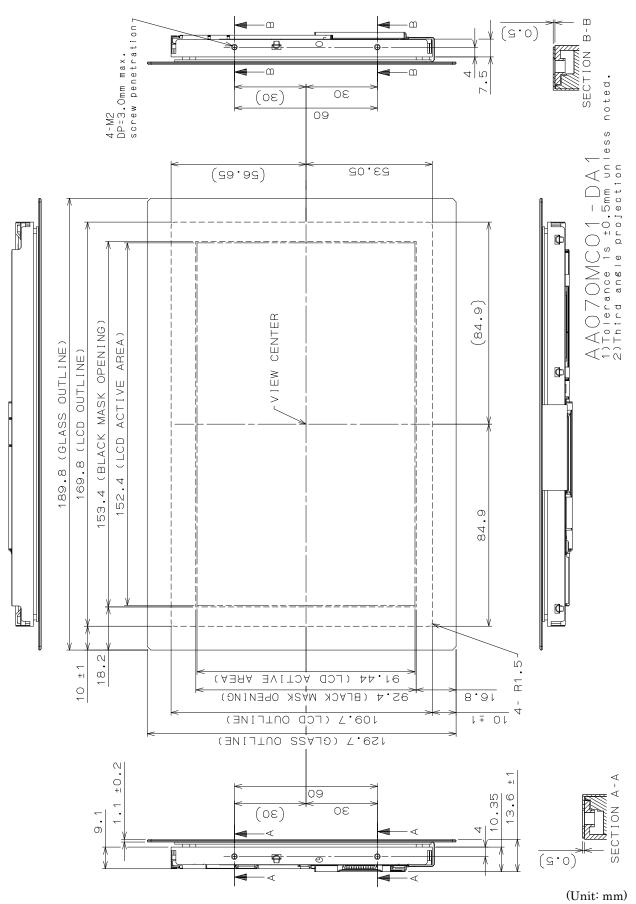


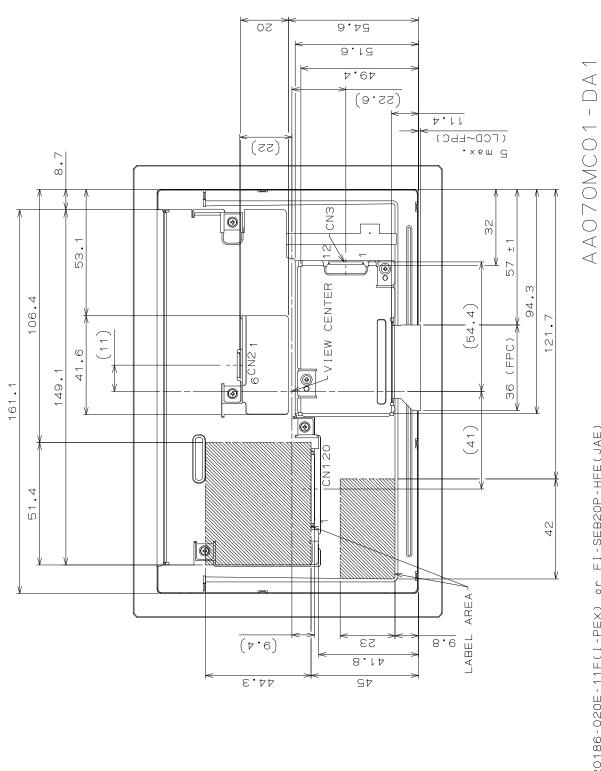
## 7. BLOCK DIAGRAM



## 8. MECHANICAL SPECIFICATIONS

(1) Front Side





(Unit:mm)

#### (3) Touch Panel Design Guide

- 1) Operating Precautions
- Please operate touch panel by finger. It does not sense by tip of nail.
- Sensing is affected by how strongly touched (touched finger area), glove thickness (distance) and material.

#### 2) Assembly Precautions

- Please connect touch panel controller GND to the earth ground. When there is no connection to the earth ground, please make bypass between touch panel controller GND and the earth ground to prevent noise.
- Please use non-conductive material for customer side housing around touch panel.
   When conductive material is used for the housing, please make space more than 2mm from touch panel surface, and also please design the housing strong enough not to change its distance. Please design the housing to prevent electrical noise. (Ex. to connect to GND)
- Please keep space between FPC and noise source like metal parts and signal cables. Please keep space more than 2mm from FPC and also design not to change its distance.
- Please do not make an impact on the cover glass edge.

3. OI 110A	_ •••		, VCC=3.3V, VL	= 12.0V, In	put Signals	: Typ. value	es shown	in Section 6
ITE	Μ	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	Remarks
Contrast Rat	io	CR	$\theta_{\rm V}=0^{\circ}, \theta_{\rm H}=0^{\circ}$	650	1000			*1)*2)*5)
Luminance		Lw	$\theta_{V}=0^{\circ}, \theta_{H}=0^{\circ}$	640	800		$cd/m^2$	*1)*5)
Luminance U	Jniformity	$\Delta Lw$	$\theta_{V}=0^{\circ}, \theta_{H}=0^{\circ}$			30	%	*1)*3)*5)
Deemonge Tim	• •	$\operatorname{tr}$	$\theta_V=0^\circ, \theta_H=0^\circ$		12		ms	*1)*4)*5)
Response Tin	ne	tf	$\theta_V=0^\circ, \theta_H=0^\circ$		12		ms	*1)*4)*5)
Viewing	Horizontal	$\theta_{\rm H}$	CD > 10	$-70 \sim 70$	$-85 \sim 85$		0	*1)*5)
Angle	Vertical	$\theta_{\rm V}$	$CR \ge 10$	$-70 \sim 70$	$-85 \sim 85$		o	*1)*5)
Image stickir	ıg	tis	2 h			2	s	*6)
	Red	Rx		0.541	0.581	0.621		
	nea	Ry		0.279	0.319	0.359		
Color	Green	Gx		0.294	0.334	0.374		
Coordinates	Green	Gy	$\theta_V=0^\circ, \theta_H=0^\circ$	0.542	0.582	0.622		*1)*5)
	Blue	Bx		0.110	0.150	0.190		
	Diue	By		0.091	0.131	0.171		
	White	Wx		0.273	0.313	0.353		
	white			0.289	0.329	0.369		

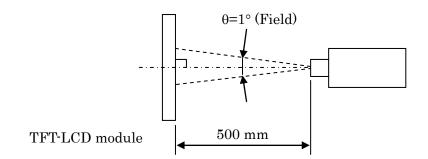
## 9. OPTICAL CHARACTERISTICS

[Note]

These items are measured using EZContrast XL88F(ELDIM) for viewing angle, RD-80SA(TOPCON) for response time and CS-2000 (KONICA MINOLTA) or equivalent equipment for others under the dark room condition (no ambient light) after more than 30 minutes from turning on the backlight unless noted.

Condition:  $V_{PDIM} = 2.5 \sim 5.5 \text{ VDC} \text{ (DUTY} = 100\%)$ 

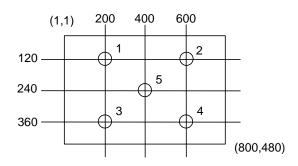
Measurement method for luminance and color coordinates is as follows.



The luminance is measured according to FLAT PANEL DISPLAY MEASUREMENTS STANDARD (VESA Standard).

#### \*1) Measurement Point

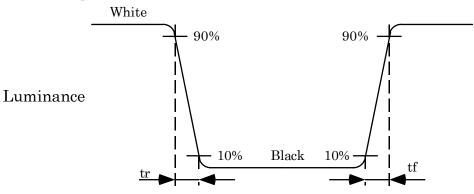
Contrast Ratio, Luminance, Response Time, Viewing Angle, Color Coordinates: Display Center Luminance Uniformity: point  $1\sim5$  shown in a figure below



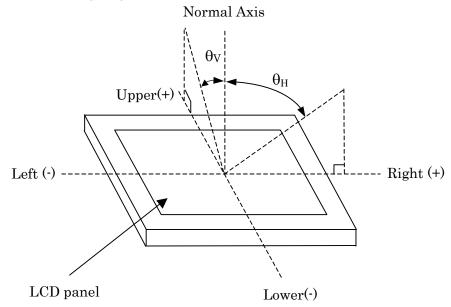
\*2) Definition of Contrast Ratio

CR= Luminance with all white pixels / Luminance with all black pixels

- \*3) Definition of Luminance Uniformity ΔLw=[Lw(MAX)/Lw(MIN)-1] × 100
- \*4) Definition of Response Time

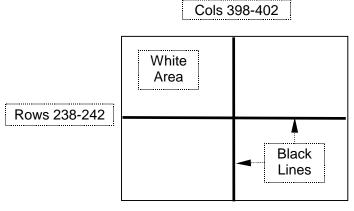


\*5) Definition of Viewing Angle ( $\theta_V$ ,  $\theta_H$ )



## \*6) Image sticking:

Continuously display the test pattern shown in the figure below for two-hours. Then display a completely white screen. The previous image shall not persist more than two seconds at 25°C.



TEST PATTERN FOR IMAGE STICKING TEST

## **10. RELIABILITY TEST CONDITION**

(1) Temperature and Humidity

ITEM	CONDITIONS
HIGH TEMPERATURE HIGH HUMIDITY OPERATION	40°C, 90%RH, 240 h (No condensation)
HIGH TEMPERATURE OPERATION	70°C, 240 h
LOW TEMPERATURE OPERATION	–30°C, 240 h
HIGH TEMPERATURE STORAGE	80°C, 240 h
LOW TEMPERATURE STORAGE	–30°C, 240 h
THERMAL SHOCK (NON-OPERATION)	-30°C (1h) ~ 80°C(1h), 100 cycles

## (2) Shock & Vibration

ITEM	CONDITIONS
	Shock level: 980 m/s <sup>2</sup> (100G)
SHOCK	Waveform: half sinusoidal wave, 2 ms
(NON-OPERATION)	Number of shocks: one shock input in each direction of three mutually
	perpendicular axes for a total of six shock inputs
VIBRATION (NON-OPERATION)	Vibration level: 9.8 m/s <sup>2</sup> (1.0G) Waveform: sinusoidal Frequency range: 5 to 500 Hz Frequency sweep rate: 0.5 octave /min Duration: one sweep from 5 to 500 Hz in each of three mutually perpendicular axis(each x,y,z axis: 1 hour, total 3 hours)

## (3) ESD Test

ITEM	CONDITIONS
CONTACT DISCHARGE (OPERATION)	150pF, 330 $\Omega$ , ±8kV, 10 times at 1 sec interval
SIGNAL PIN DISCHARGE (NON-OPERATION)	200pF, 0 $\Omega$ , ±200V, 10 times at 1 sec interval

## (4) Judgment standard

The judgment of the above tests should be made as follow:

## a. TFT-LCD

Pass: Normal display image, no damage of the display function. (ex. no line defect) Partial transformation of the module parts should be ignored.

Fail: No display image, damage of the display function. (ex. line defect)

#### b. Touch Panel

Pass: No damage of the touch function. (ex. touch detection cannot be performed.)

Fail: Touch panel is damaged. (ex. Touch panel does not work, or touch detection cannot be performed.)

## **11. OTHER FEATURE**

(1) Environmental Restriction /Law Compliance

This LCD module complies with  $\mathrm{RoHS}^{*)}$  directive.

\*) RoHS: Restriction of the use of certain hazardous substances in electrical and electronic equipment

(2) Safety Standard Authorization

UL1950 Recognized (UL File# E158720)

(3) Warranty Period

26 months after shipment from our factory

## **12. HANDLING PRECAUTIONS FOR TFT-LCD MODULE**

Please pay attention to the followings in handling TFT-LCD products;

### (1) ASSEMBLY PRECAUTION

- a. Please mount the LCD module by using mounting hole with a screw clamping torque less than 0.2 Nm. Please do not bend or wrench the LCD module in assembling. Please do not drop, bend or twist the LCD module in handling.
- b. Please design display housing in accordance with the following guide lines.
  - (a) Housing case must be designed carefully so as not to put stress on LCD and not to wrench module. If customer uses compression mounting, please evaluate housing case with LCD carefully to avoid image quality issue caused by mechanical stress.
  - (b) Under high temperature environment, performance and life time of LED may heavily shorten. When you design with our LCD product, please consider radiating heat and ventilation for good heat management.
  - (c) Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. Approximately 1.0mm of the clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
  - (d) When some parts, such as, FPC cable and ferrite plate, are installed underneath the LCD module, still sufficient clearance is required, such as 0.5mm. This clearance is, especially, to be reconsidered when the additional parts are implemented for EMI countermeasure.
  - (e) Keep sufficient clearance between LCD module and the others parts, such as inverter and speaker so as not to interfere the LCD module. Approximately 1.0 mm of the clearance in the design is recommended.
  - (f) To avoid local elevation/decrease of temperature, considering location of heating element, heat release, thermal design should be done.
- c. Please do not push or scratch touch panel surface with anything hard.
- d. Do not use or store the product under a condition where the product will be exposed to water, organic solution or acid.
- e. Please wipe off touch panel surface with absorbent cotton or soft cloth in case of it being soiled.
- f. Do not make an impact on the edge of the cover glass.
- g. Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.
- h. Please do not touch metal frames with bare hands and soiled gloves. A color change of the metal frames can happen during a long preservation of soiled LCD modules.
- i. Please handle metal frame carefully because edge of metal frame is very sharp.
- j. Please connect the metal frame of LCD module to GND in order to minimize the effect of external noise and EMI.
- k. Be sure to connect the cables and the connecters correctly.

#### MITSUBISHI ELECTRIC

#### (2) OPERATING PRECAUTIONS

- a. Please be sure to turn off the power supply before connecting and disconnecting signal input cable.
- b. Please do not change variable resistance settings in LCD module. They are adjusted to the most suitable value. If they are changed, it might happen LCD does not satisfy the characteristics specification.
- c. The interface signal speed is very high. Please pay attention to transmission line design and other high speed signal precautions to satisfy signal specification.
- d. Condensation might happen on the surface and inside of LCD module in case of sudden change of ambient temperature.
- e. Please pay attention not to display the same pattern for very long time. Image sticking might happen on LCD. Although image sticking may disappear as the operation time proceeds, screen saver function is recommended not to cause image sticking.
- f. Please obey the same safe instructions as ones being prepared for ordinary electronic products.

## (3) PRECAUTIONS WITH ELECTROSTATICS

- a. This LCD module use CMOS-IC on circuit board and TFT-LCD panel, and so it is easy to be affected by electrostatics. Please be careful with electrostatics by the way of your body connecting to the ground and so on.
- b. Please remove protection film very slowly from the surface of touch panel to prevent from electrostatics occurrence.

## (4) STORAGE PRECAUTIONS

LCD should be stored in the room temperature environment with normal humidity. The LCD inventory should be processed by first-in first-out method.

## (5) SAFETY PRECAUTIONS

- a. When you waste damaged or unnecessary LCDs, it is recommended to crush LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

## (6) OTHERS

- a. A strong incident light into LCD panel may cause deterioration to touch panel, polarizer film, color filter, and other materials, which will degrade the quality and performance of display.
  Please do not expose LCD module under strong Ultraviolet rays for a long time. If using under direct sunlight condition, please test the reliability and performance completely.
- b. For the packaging box handling, please see and obey with the packaging specification datasheet.

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