

MITSUBISHI IGBT MODULES
CM200DY-24NF

HIGH POWER SWITCHING USE

CM200DY-24NF



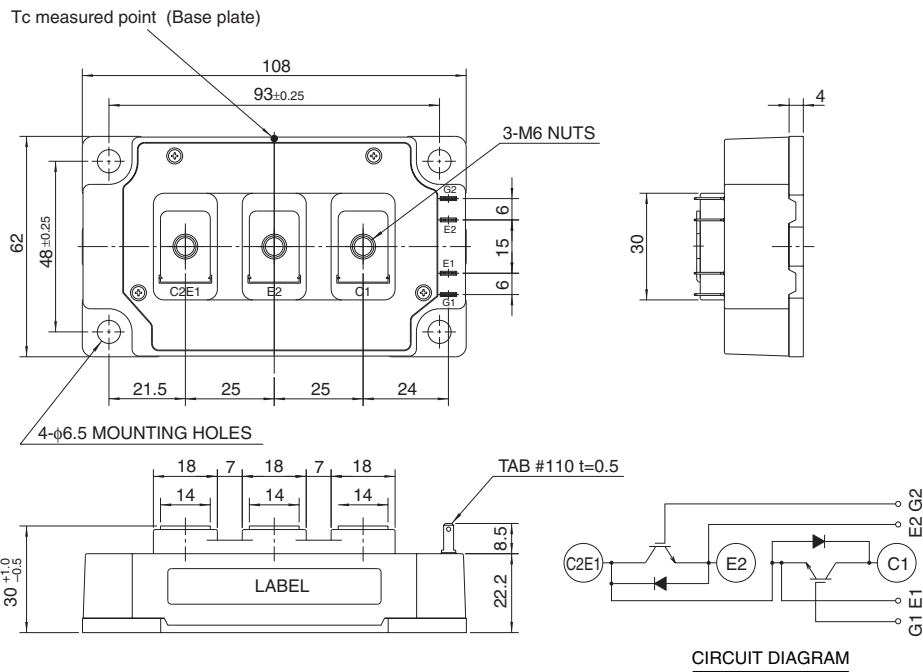
- IC200A
- VCES 1200V
- Insulated Type
- 2-elements in a pack

APPLICATION

General purpose inverters & Servo controls, etc

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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MAXIMUM RATINGS (T_j = 25°C, unless otherwise specified)

| Symbol | Parameter | Conditions | Ratings | Unit |
|--------------------------|-------------------------------|--|------------|------------------|
| V _{CE} S | Collector-emitter voltage | G-E Short | 1200 | V |
| V _{GE} S | Gate-emitter voltage | C-E Short | ±20 | V |
| I _C | Collector current | DC, T _c ' = 112°C ^{*3} | 200 | A |
| I _{CM} | | Pulse (Note 2) | 400 | A |
| I _E (Note 1) | Emitter current | | 200 | A |
| I _{EM} (Note 1) | | Pulse (Note 2) | 400 | A |
| P _C (Note 3) | Maximum collector dissipation | T _c = 25°C | 1130 | W |
| T _j | Junction temperature | | -40 ~ +150 | °C |
| T _{stg} | Storage temperature | | -40 ~ +125 | °C |
| V _{iso} | Isolation voltage | Terminals to base plate, f = 60Hz, AC 1 minute | 2500 | V _{rms} |
| — | Torque strength | Main terminals M6 screw | 3.5 ~ 4.5 | N • m |
| — | | Mounting M6 screw | 3.5 ~ 4.5 | N • m |
| — | Weight | Typical value | 400 | g |

ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise specified)

| Symbol | Parameter | Test conditions | Limits | | | Unit | | | | | | | |
|--------------------------|--------------------------------------|---|--|------|-----------------------|------|-----|-----|------------------------|---|-----|---|---|
| | | | Min. | Typ. | Max. | | | | | | | | |
| I _{CS} | Collector cutoff current | V _{CE} = V _{CE} S, V _{GE} = 0V | — | — | 1 | mA | | | | | | | |
| V _{GE(th)} | Gate-emitter threshold voltage | I _C = 20mA, V _{CE} = 10V | 6 | 7 | 8 | V | | | | | | | |
| I _{GES} | Gate leakage current | ±V _{GE} = V _{GES} , V _{CE} = 0V | — | — | 0.5 | µA | | | | | | | |
| V _{CE(sat)} | Collector-emitter saturation voltage | I _C = 200A, V _{GE} = 15V | <table border="1"> <tr> <td>T_j = 25°C</td> <td>—</td> <td>1.8</td> <td>2.5</td> </tr> <tr> <td>T_j = 125°C</td> <td>—</td> <td>2.0</td> <td>—</td> </tr> </table> | | T _j = 25°C | — | 1.8 | 2.5 | T _j = 125°C | — | 2.0 | — | V |
| T _j = 25°C | — | 1.8 | 2.5 | | | | | | | | | | |
| T _j = 125°C | — | 2.0 | — | | | | | | | | | | |
| C _{ies} | Input capacitance | V _{CE} = 10V V _{GE} = 0V | — | — | 47 | nF | | | | | | | |
| C _{oes} | Output capacitance | | — | — | 4 | nF | | | | | | | |
| C _{res} | Reverse transfer capacitance | | — | — | 0.9 | nF | | | | | | | |
| Q _G | Total gate charge | V _{CC} = 600V, I _C = 200A, V _{GE} = 15V | — | 1350 | — | nC | | | | | | | |
| t _{d(on)} | Turn-on delay time | V _{CC} = 600V, I _C = 200A V _{GE} = ±15V R _G = 1.6Ω, Inductive load I _E = 200A | — | — | 500 | ns | | | | | | | |
| t _r | Turn-on rise time | | — | — | 150 | ns | | | | | | | |
| t _{d(off)} | Turn-off delay time | | — | — | 600 | ns | | | | | | | |
| t _f | Turn-off fall time | | — | — | 350 | ns | | | | | | | |
| t _{rr} (Note 1) | Reverse recovery time | | — | — | 250 | ns | | | | | | | |
| Q _{rr} (Note 1) | Reverse recovery charge | | — | 7.5 | — | µC | | | | | | | |
| V _{EC} (Note 1) | Emitter-collector voltage | I _E = 200A, V _{GE} = 0V | — | — | 3.2 | V | | | | | | | |
| R _{th(j-c)Q} | Thermal resistance ^{*1} | IGBT part (1/2 module) | — | — | 0.11 | K/W | | | | | | | |
| R _{th(j-c)R} | | FWDi part (1/2 module) | — | — | 0.19 | K/W | | | | | | | |
| R _{th(c-f)} | Contact thermal resistance | Case to heat sink, Thermal compound Applied ^{*2} (1/2 module) | — | 0.04 | — | K/W | | | | | | | |
| R _{th(j-c')Q} | Thermal resistance | Case temperature measured point is just under the chips | — | — | 0.066 ^{*3} | K/W | | | | | | | |
| R _G | External gate resistance | | 1.6 | — | 16 | Ω | | | | | | | |

*1 : Case temperature (T_c) measured point is shown in page OUTLINE DRAWING.

*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

*3 : Case temperature (T_c) measured point is just under the chips.

If you use this value, R_{th(f-a)} should be measured just under the chips.

Note 1. I_E, V_{EC}, t_{rr} & Q_{rr} represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

2. Pulse width and repetition rate should be such that the device junction temperature (T_j) does not exceed T_{jmax} rating.

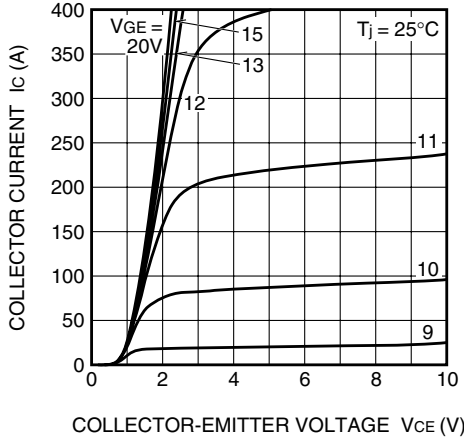
3. Junction temperature (T_j) should not increase beyond 150°C.

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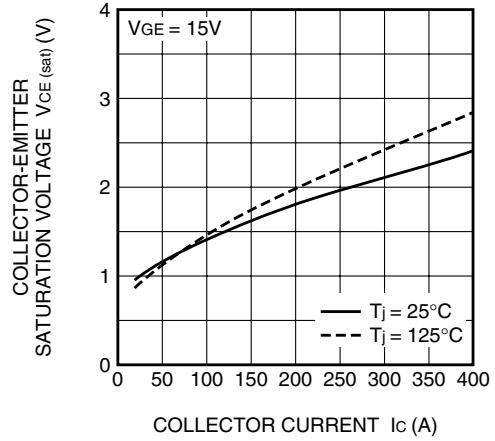
HIGH POWER SWITCHING USE

PERFORMANCE CURVES

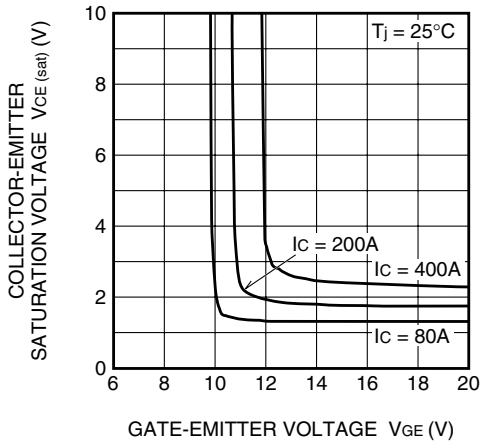
OUTPUT CHARACTERISTICS (TYPICAL)



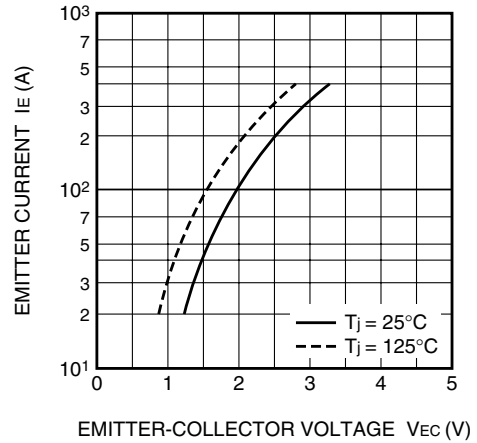
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



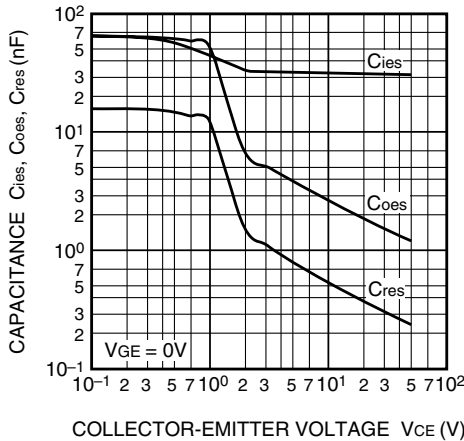
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



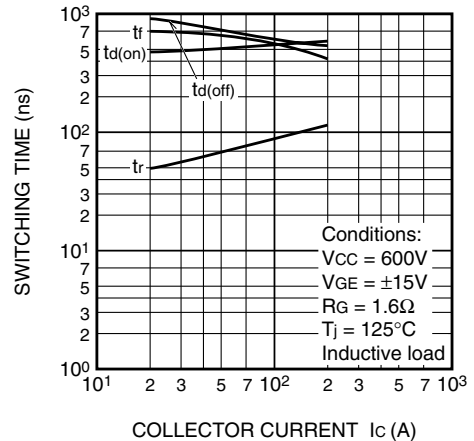
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



CAPACITANCE-VCE CHARACTERISTICS (TYPICAL)



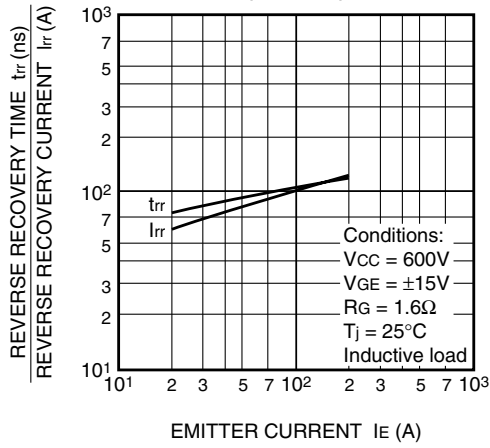
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



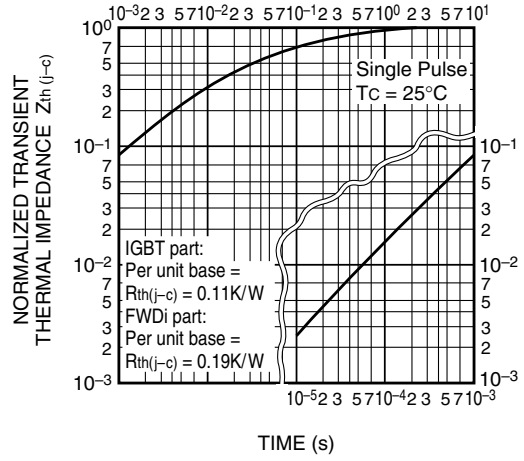
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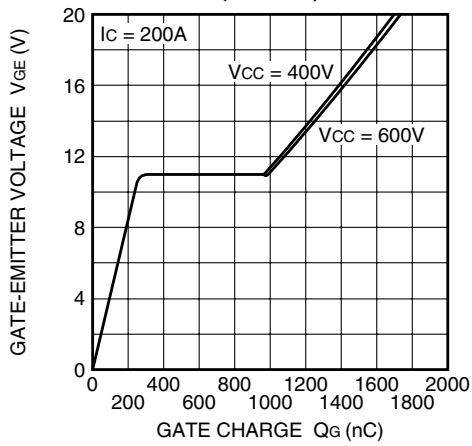
REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (IGBT part & FWDi part)



GATE CHARGE CHARACTERISTICS (TYPICAL)



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