

DESCRIPTION

The EVQ4487-U-00A is an evaluation board for MPQ4487, which integrates a monolithic step-down switch-mode converter with two USB current-limit switches and Type-C 5V@3A mode configure channel for each port. It achieves 6A max output current over a wide input-supply range with excellent load and line regulation.

The output of the USB switch is current limited. Both USB ports support TYPE-C 5V@3A DFP Mode eliminating outside user interaction.

Fault condition protection includes hiccup current limiting, output OVP, and thermal shutdown (TSD).

ELECTRICAL SPECIFICATION

Parameter	Symbol	Value	Units
Operating Input Voltage	V_{IN}	12	V
Switching Frequency	F_s	450	kHz
Output Voltage	V_{USB1}/V_{USB2}	5.17	V
Output Current	USB1_ I _{OUT}	3	A
	USB2_ I _{OUT}	3	A

FEATURES

- Wide 6V to 36V Operating Input-Voltage Range
- Selectable Output Voltage: 5.1V, 5.17V and 5.3V
- 90mV Line Drop Compensation
- Accurate USB1/USB2 Output-Current Limit
- 18mΩ/15mΩ Low $R_{DS(ON)}$ Internal Buck Power MOSFETs
- 18mΩ/18mΩ Low $R_{DS(ON)}$ Internal USB1/USB2 Power MOSFETs
- Load Shedding versus Temperature
- Hiccup Current Limit for both Buck and USB
- Supports USB TYPE-C 5V@3A Mode

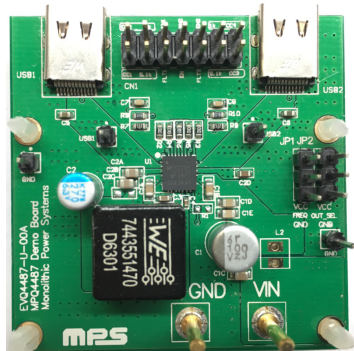
APPLICATIONS

- USB Hub
- Automotive Cigarette Lighter Adapters
- Power Supply for Linear Chargers

All MPS parts are lead-free, halogen free, and adhere to the RoHS directive. For MPS green status, please visit MPS website under Quality Assurance.

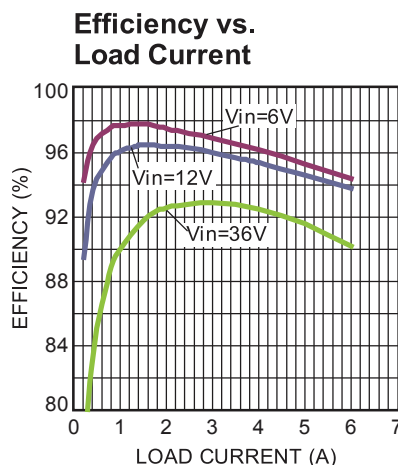
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EVQ4487-U-00A EVALUATION BOARD

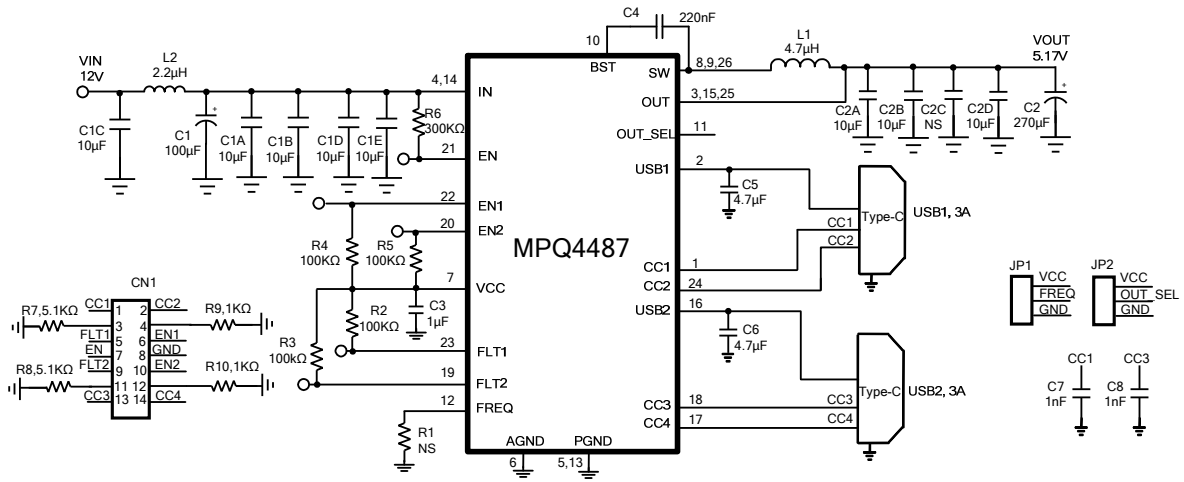


(L x W x H) 5cm x 5cm x 1.7cm
(Four Layer PCB/2oz per layer)

Board Number	MPS IC Number
EVQ4487-U-00A	MPQ4487



EVALUATION BOARD SCHEMATIC



EVQ4487-U-00A BILL OF MATERIALS

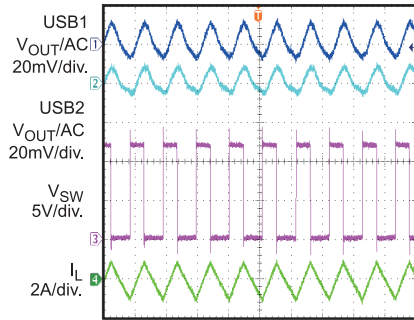
Qty	Ref	Value	Description	Package	Manufacturer	Part Number
5	C1A, C1B, C1C, C1D, C1E	10 μ F	Ceramic Capacitor, 35V, X6S	0805	Murata	GRM21BC8YA106KE11
1	C1	100 μ F	Aluminum Electrolytic Capacitor, 35V, 160m Ω ESR	SMD	Chemi-Con	EMZJ350ADA101MF80G
1	C2	270 μ F	Polymer Capacitor, 6.3V	DIP	Chemi-Con	APSK6R3ELL271ME08S
2	C2A, C2B	10 μ F	Ceramic Capacitor, 10V, X7R	0805	Murata	GRM21BR71A106KA73L
1	C2D	10 μ F	Ceramic Capacitor, 6.3V, X7R	0603	Murata	GRM219R60J106KE19D
1	C3	1 μ F	Ceramic Capacitor, 16V, X7R	0603	Murata	GRM186R71C105KA12D
1	C4	0.22 μ F	Ceramic Capacitor, 10V, X5R	0402	Murata	GRM155R61A224KE19
2	C5, C6	4.7 μ F	Ceramic Capacitor, 6.3V, X5R	0603	Murata	GRM188R60J475KE19D
2	C7, C8	1nF	Ceramic Capacitor, 16V, X7R	0603	Murata	GRM188R71C102KA01D
0	C2C, R1	NS				
4	R2, R3, R4, R5	100K Ω	Film Resistor, 1%	0603	Royal	RL0603FR-07100KL
1	R6	300K Ω	Film Resistor, 1%	0603	Royal	RL0603FR-07300KL
2	R7, R8	5.1K Ω	Film Resistor, 1%	0603	Royal	RL0603FR-075K1L
2	R9, R10	1K Ω	Film Resistor, 1%	0603	Royal	RL0603FR-071KL
1	L1	4.7 μ H	Inductor, DCR 7m Ω	SMD	Würth	7443551470
1	L2	2.2 μ H	Inductor, DCR 35m Ω	SMD	Würth	74438356022
2	USB1, USB2	USB	TYPE-C USB Port	DIP	Würth	632723300011
1	U1	MPQ448 7	Step Down Converter with Dual USB Charging Port	QFN26 (5mmx5m m)	MPS	MPQ4487GU
1	CN1	Header	2.54mm, 14pin, Dual pin header, default all pins open	DIP	Würth	61301421121
2	JP1, JP2	Header	2.54mm, 3pin header, default all pins open	DIP	Würth	61300311121

TYPICAL PERFORMANCE CHARACTERISTICS

$V_{IN} = 12V$, $V_{OUT} = 5.17V$, $L = 4.7\mu H$, $T_A = 25^\circ C$, unless otherwise noted.

Output Ripple

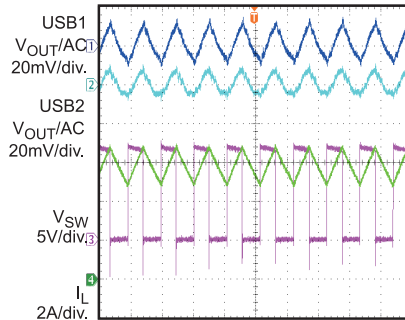
$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=0A$



2µs/div.

Output Ripple

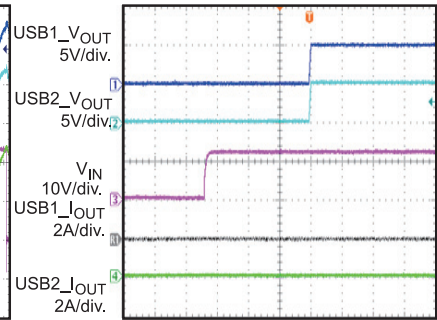
$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=3A$



2µs/div.

Power Start-Up

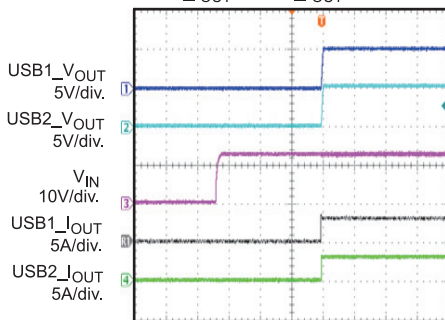
$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=0A$



40ms/div.

Power Start-Up

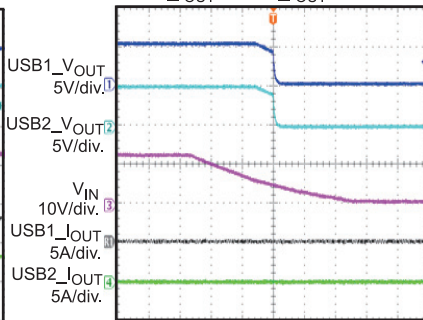
$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=3A$



40ms/div

Power Shutdown

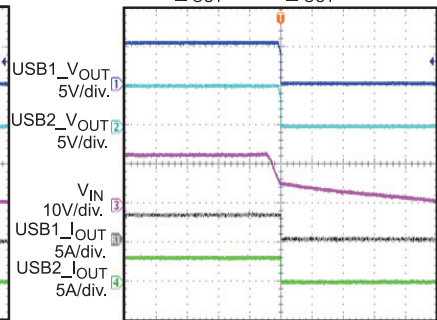
$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=0A$



20ms/div

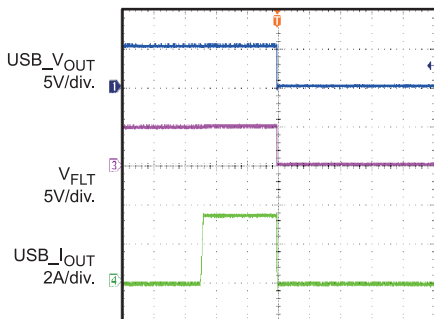
Power Shutdown

$V_{IN}=12V$, $V_{OUT}=5.17V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=3A$



10ms/div

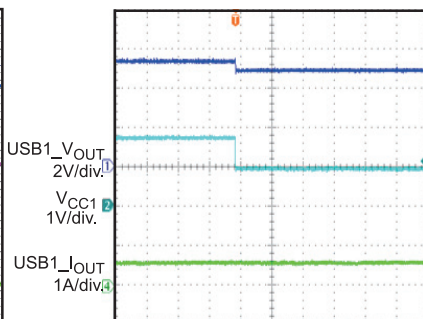
USB Over-Current Protection



2ms/div

Load Shedding Entry

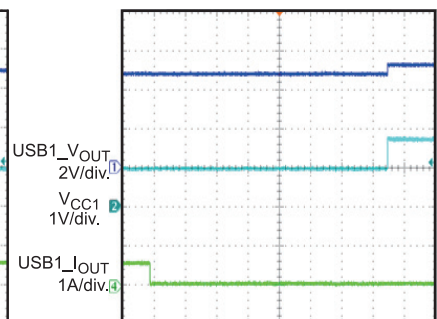
$V_{IN}=12V$, $V_{OUT}=5.17V$



10ms/div

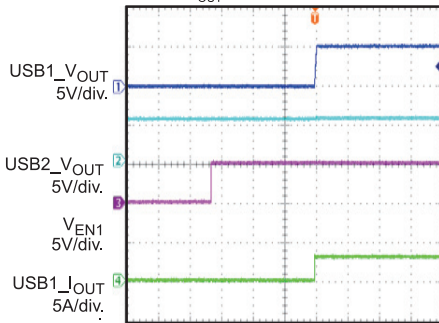
Load Shedding Recovery

$V_{IN}=12V$, $V_{OUT}=5.17V$

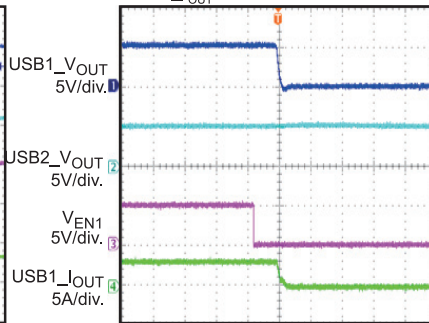


2s/div

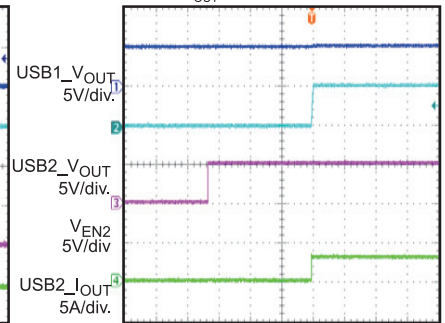
TYPICAL PERFORMANCE CHARACTERISTICS (continued)
 $V_{IN} = 12V$, $V_{OUT} = 5.17V$, $L = 4.7\mu H$, $T_A = 25^\circ C$, unless otherwise noted.

EN1 Start-Up
 $V_{IN} = 12V$, $V_{OUT} = 5.17V$,
 $USB1_I_{OUT} = 3A$


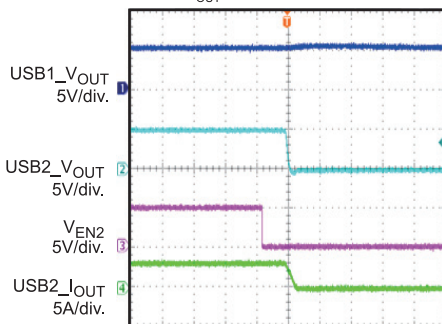
40ms/div.

EN1 Shutdown
 $V_{IN} = 12V$, $V_{OUT} = 5.17V$,
 $USB1_I_{OUT} = 3A$


20μs/div.

EN2 Start-Up
 $V_{IN} = 12V$, $V_{OUT} = 5.17V$,
 $USB2_I_{OUT} = 3A$


40ms/div.

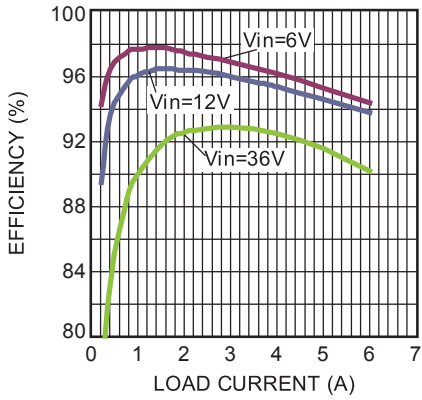
EN2 Shutdown
 $V_{IN} = 12V$, $V_{OUT} = 5.17V$,
 $USB2_I_{OUT} = 3A$


20μs/div

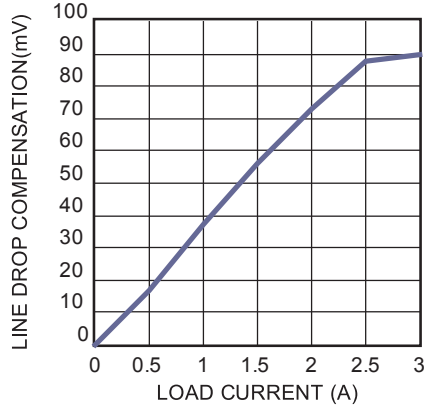
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

$V_{IN} = 12V$, $V_{OUT} = 5.17V$, $L = 4.7\mu H$, $T_A = 25^\circ C$, unless otherwise noted.

Efficiency vs. Load Current

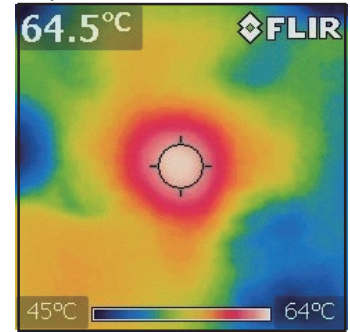


Line Drop Compensation vs. Load Current



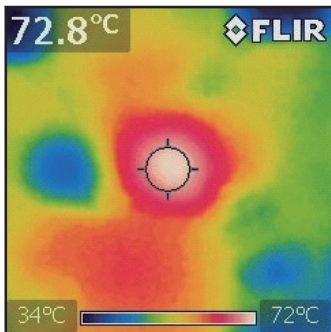
Thermal Image

$V_{IN}=12V$,
 $USB1_I_{OUT}=USB2_I_{OUT}=2.4A$
 4 layer PCB, 50mm x 50mm



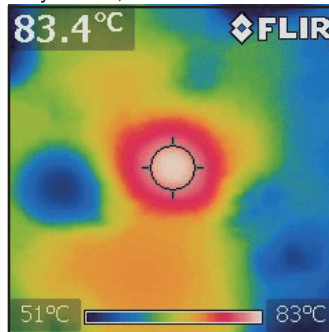
Thermal Image

$V_{in}=12V$,
 $USB1_I_{OUT}=2.4A$, $USB2_I_{OUT}=3A$
 4 layer PCB, 50mm x 50mm



Thermal Image

$V_{IN}=12V$,
 $USB1_I_{OUT}=3A$, $USB2_I_{OUT}=3A$
 4 layer PCB, 50mm x 50mm



PRINTED CIRCUIT BOARD LAYOUT

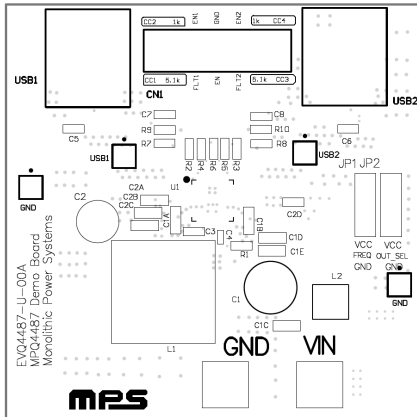


Figure 1—Top Silk Layer

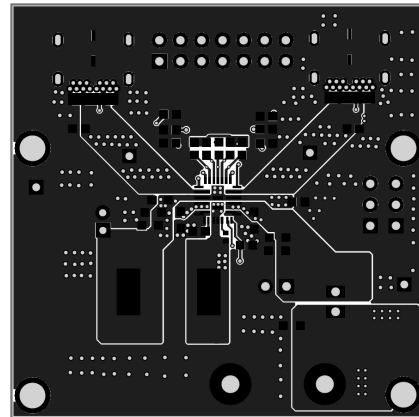


Figure 2—Top Layer

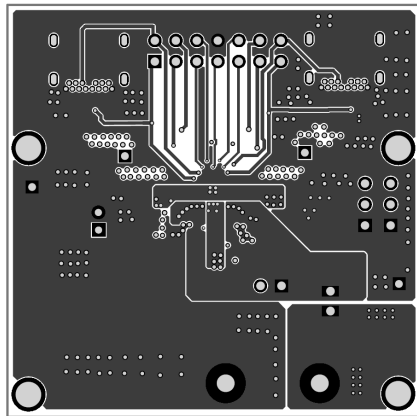


Figure 3—Middle1 Layer

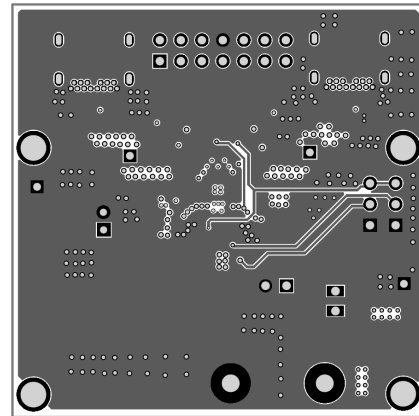


Figure 4—Middle2 Layer

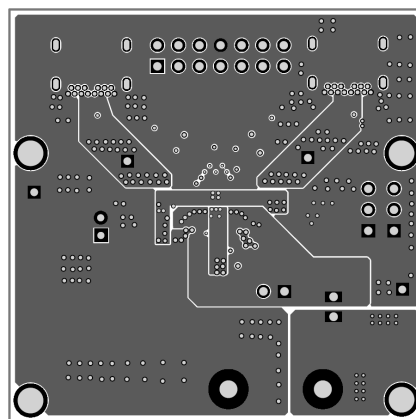


Figure 5—Bottom Layer

QUICK START GUIDE

1. Connect the positive and negative terminals of the load to the USB1, USB2 and GND pins, respectively.
2. Preset the power supply output between 6V and 36V, and then turn off the power supply.
3. Connect the positive and negative terminals of the power supply output to the VIN and GND pins, respectively.
4. Turn the power supply on, the board will automatically start up. But if no type-C device is attached, there is no Vbus output.
5. For USB Type-C 5V/3A DFP mode, if no type-C device is attached, short pin1 and pin 3 of CN1 with a jumper to enable USB1 output, short pin 11 and pin 13 of CN1 with a jumper to enable USB2 output; short pin 2 and pin 4 of CN1 with a jumper to enable VCONN1 output, short pin 12 and pin 14 of CN1 with a jumper to enable VCONN2 output;

If type-C device is attached, all CN1 pins should be float.

6. For USB Type-A 5V/2.4A mode, change R7 =80.6k Ω , remove C7 and short pin1 and pin 3 of CN1 with a jumper to enable USB1 output; change R8 =80.6k Ω , remove C8 and short pin 11 and pin 13 of CN1 with a jumper to enable USB2 output. Keep R9, R10 float.

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