

■ PRODUCT CHARACTERISTICS

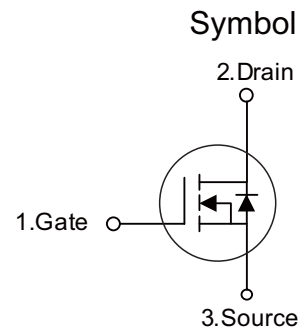
VDSS	500V
$R_{DS(on)typ}(V_{GS}=10V)$	0.21Ω
Qg@type	53nC
ID	25A

■ APPLICATIONS

- * High efficiency switch mode power supplies
- * Electronic lamp ballasts based on half bridge
- * LED power supplies

■ FEATURES

- * High Switching Speed
- * Improved dv/dt capability



■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT25N50HF	TO-220F	50 pieces/Tube
N/A	MOT25N50A	TO-220	50 pieces/Tube

■ ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	500	V
Gate-Source Voltage	V_{GSS}	±30	V
Drain Current	Continuous	I_D	25
	Pulsed (Note 2)	I_{DM}	100
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	1000
Peak Diode Recovery dv/dt		dv/dt	2.2
Power Dissipation	TO-220	P_D	250
	TO-220F		48
Junction Temperature	T_J	+150	°C
Storage Temperature	T_{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 10\text{mH}$, $I_{AS} = 14\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

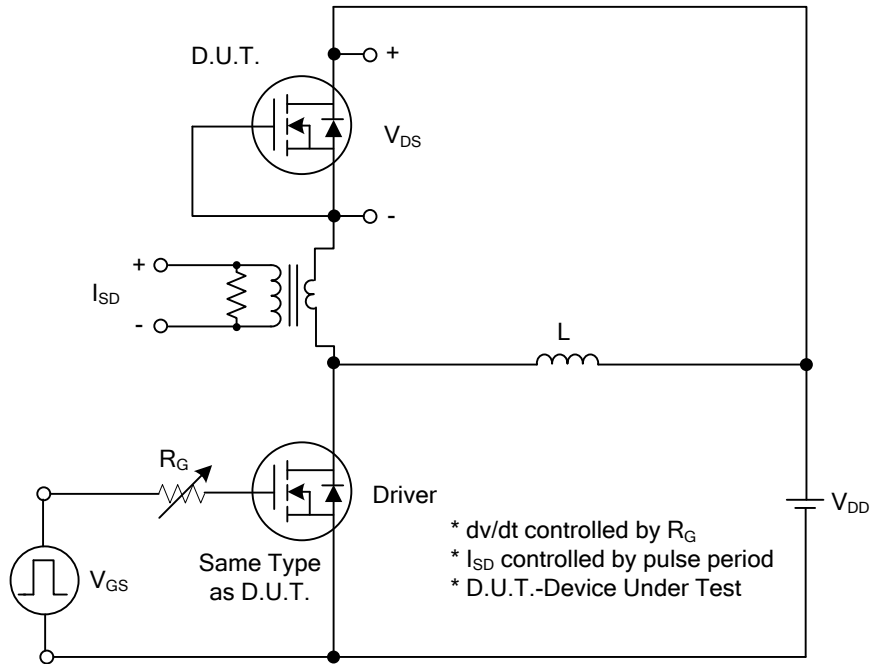
4. $I_{SD} \leq 20\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$, unless otherwise noted)

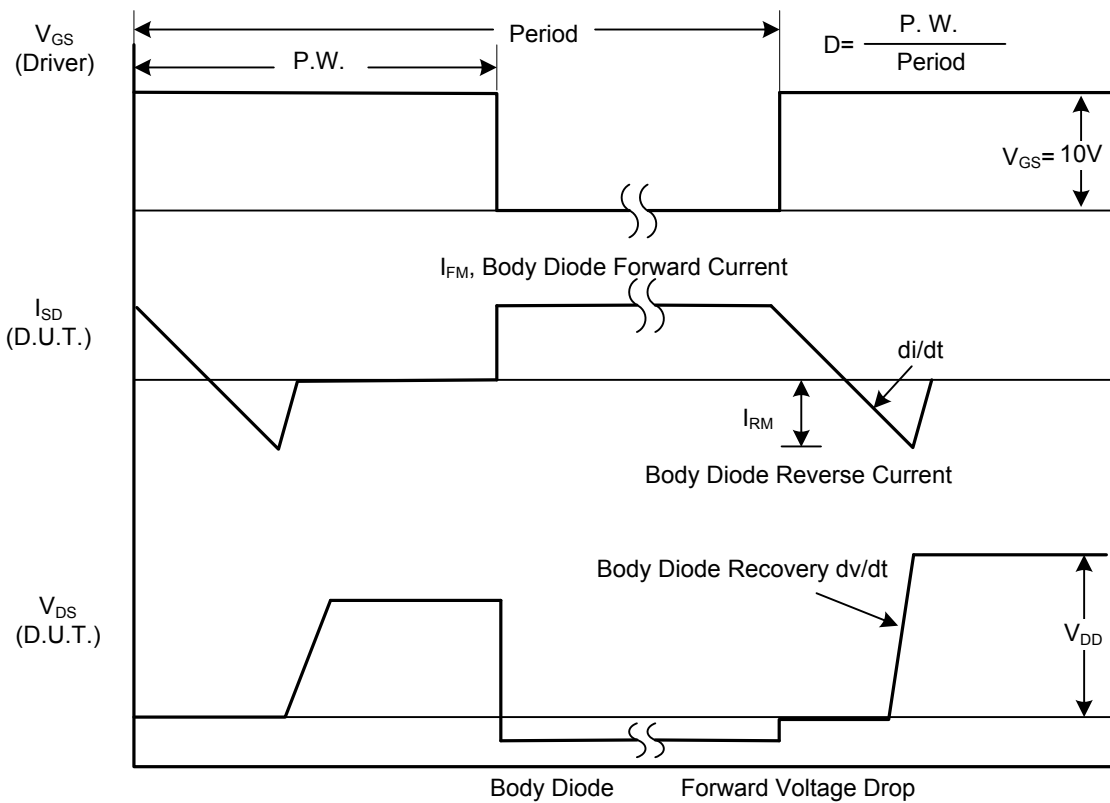
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Off characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	500	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=500\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
Gate- Source Leakage Current	Forward	I_{GSS}	-	-	+100	nA
	Reverse				-100	nA
On characteristics						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	2.0	-	4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}, I_D=12.5\text{A}$	-	0.21	0.24	Ω
Dynamic characteristics						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}, V_{DS}=25\text{V}, f=1.0\text{MHz}$	-	2450	-	pF
Output Capacitance	C_{OSS}		-	275	-	pF
Reverse Transfer Capacitance	C_{RSS}		-	16.5	-	pF
Switching characteristics						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=100\text{V}, V_{GS}=10\text{V}, I_D=25\text{A}$ $I_G=1\text{mA}$ (Note1, 2)	-	53	-	nC
Gate to Source Charge	Q_{GS}		-	11	-	nC
Gate to Drain Charge	Q_{GD}		-	13	-	nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DS}=100\text{V}, V_{GS}=10\text{V}, I_D=25\text{A},$ $R_G=25\Omega$ (Note1, 2)	-	32	-	ns
Rise Time	t_R		-	30	-	ns
Turn-OFF Delay Time	$t_{D(OFF)}$		-	162	-	ns
Fall-Time	t_F		-	70	-	ns
Source-drain diode ratings and characteristics						
Maximum Body-Diode Continuous Current	I_S		-	-	20	A
Maximum Body-Diode Pulsed Current	I_{SM}		-	-	80	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=25\text{A}, V_{GS}=0\text{V}$	-	-	1.4	V
Body Diode Reverse Recovery Time (Note 1)	t_{rr}	$I_S=25\text{A}, V_{GS}=0\text{V}, V_R=400\text{V}$ $dI_F/dt=100\text{A}/\mu\text{s}$ (Note 1)	-	412	-	ns
Reverse Recovery Charge	Q_{rr}		-	6.5	-	μC

Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.
 2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

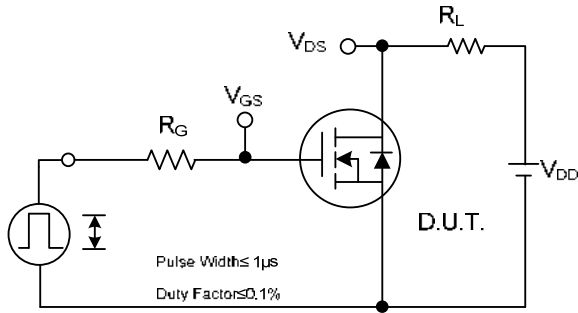


Peak Diode Recovery dv/dt Test Circuit

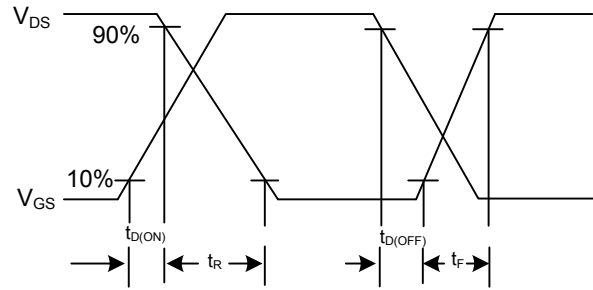


Peak Diode Recovery dv/dt Waveforms

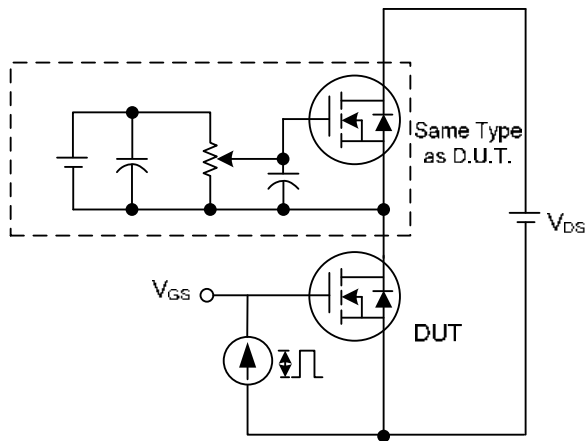
■ TEST CIRCUITS AND WAVEFORMS(Cont.)



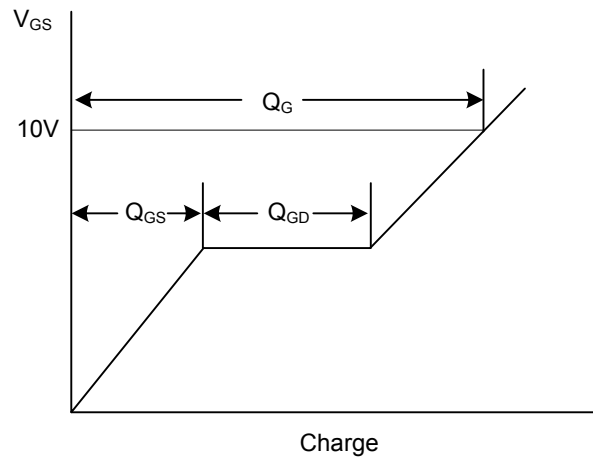
Switching Test Circuit



Switching Waveforms

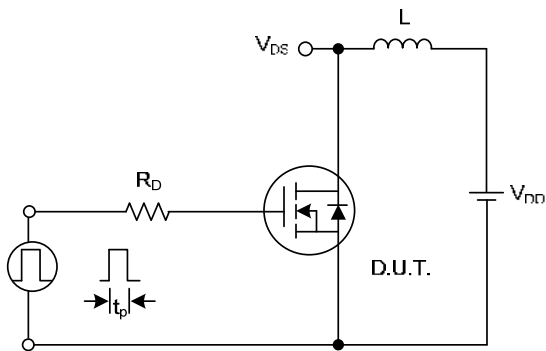


Gate Charge Test Circuit

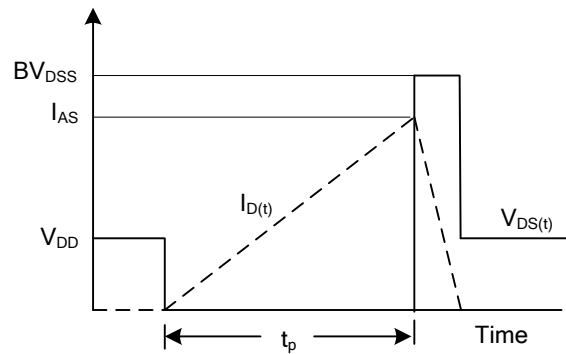


Charge

Gate Charge Waveform

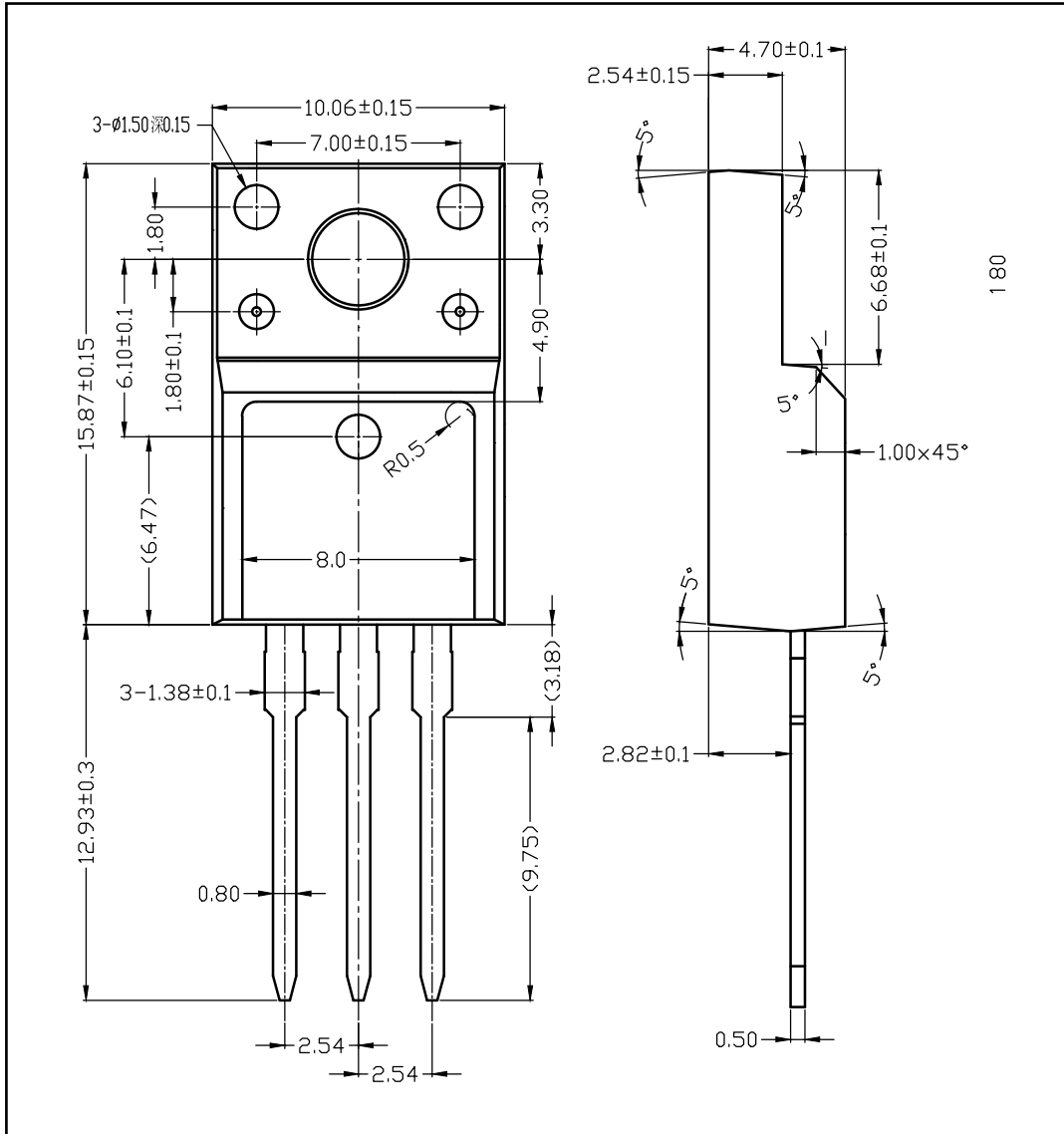


Unclamped Inductive Switching Test Circuit

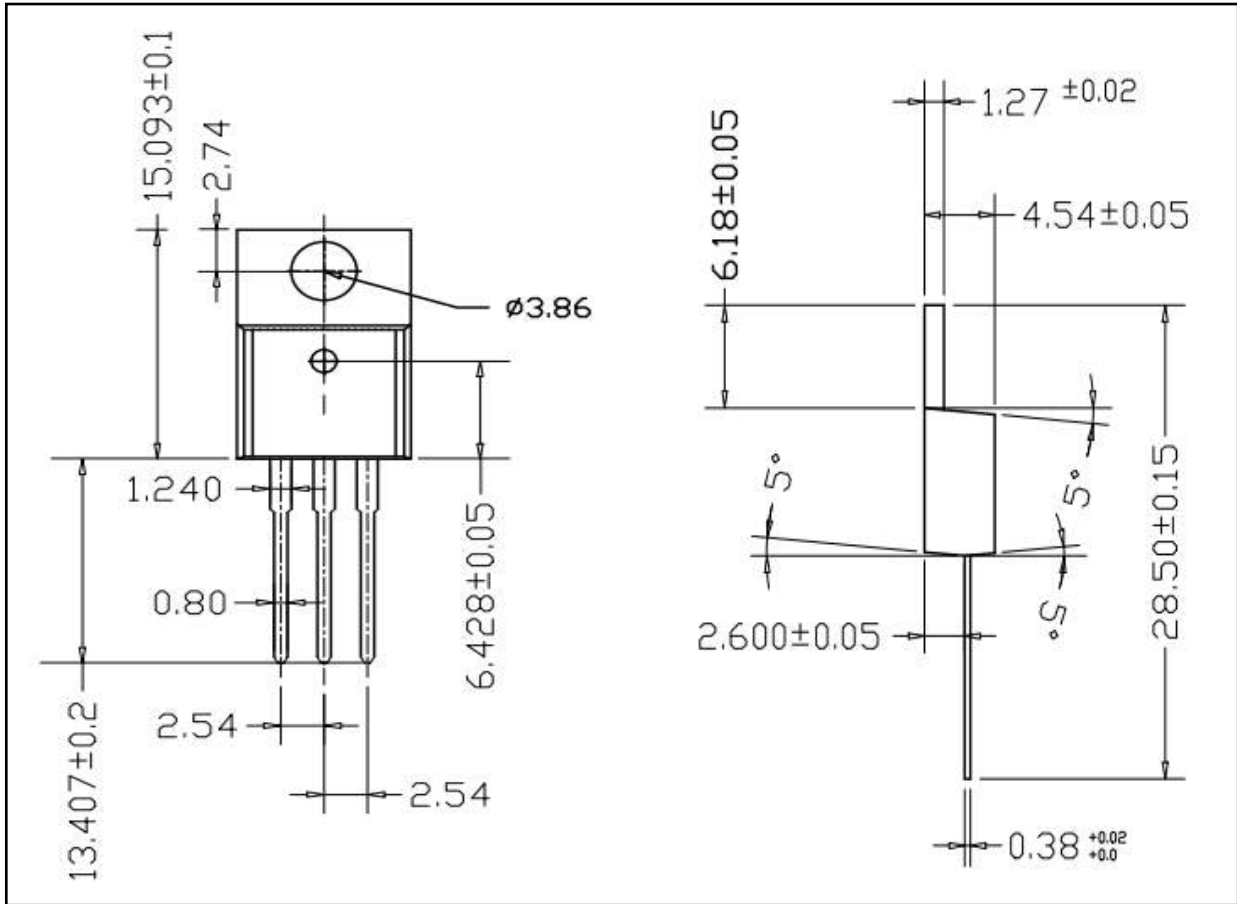


Unclamped Inductive Switching Waveforms

TO-220F PACKAGE OUTLINE DIMENSIONS



■ TO-220 PACKAGE OUTLINE DIMENSIONS



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