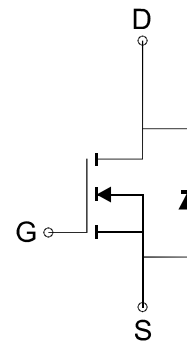
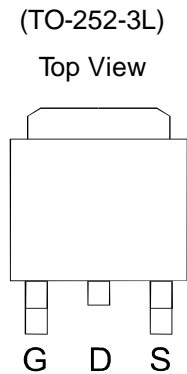


N-Channel 150V (D-S) MOSFET

GENERAL DESCRIPTION

The ME20N15 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits where high-side switching, and low in-line power loss are needed in a very small outline surface mount package.

PIN CONFIGURATION



N-Channel MOSFET

FEATURES

- $R_{DS(ON)} \leq 90m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} \leq 110m\Omega @ V_{GS}=7V$
- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability

APPLICATIONS

- Power Management
- DC/DC Converter
- Load Switch

Ordering Information: ME20N15 (Pb-free)

ME20N15-G (Green product-Halogen free)

Absolute Maximum Ratings (Tc=25°C Unless Otherwise Noted)

Parameter	Symbol	Maximum Ratings	Unit
Drain-Source Voltage	V _{DS}	150	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	T _c =25°C	20.8
		T _c =70°C	16.6
Pulsed Drain Current	I _{DM}	83	A
Maximum Power Dissipation	P _D	T _c =25°C	62.5
		T _c =70°C	40
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C
Thermal Resistance-Junction to Case*	R _{θJC}	2	°C/W

* The device mounted on 1in² FR4 board with 2 oz copper.

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N- Channel 150V (D-S) MOSFET
Electrical Characteristics (T_J=25°C Unless Otherwise Specified)

Symbol	Parameter	Limit	Min	Typ	Max	Unit
STATIC						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250 μA	150			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250 μA	3.4		4.6	V
I _{GSS}	Gate-Body Leakage	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =150V, V _{GS} =0V			1	μA
R _{DS(ON)}	Drain-Source On-Resistance*	V _{GS} =10V, I _D =10A		75.5	90	mΩ
		V _{GS} =7V, I _D =10A		84	110	
V _{SD}	Diode Forward Voltage *	I _S =1A, V _{GS} =0V		0.72	1	V
DYNAMIC						
Q _g	Total Gate Charge	V _{DD} =75V, V _{GS} =10V, I _D =10A		32.3		nC
Q _{gs}	Gate-Source Charge			15.7		
Q _{gd}	Gate-Drain Charge			6.62		
C _{iss}	Input Capacitance	V _{DS} =30V, V _{GS} =0V, f=1MHz		1039		pF
C _{oss}	Output Capacitance			136		
C _{rss}	Reverse Transfer Capacitance			14		
t _{d(on)}	Turn-On Delay Time	V _{GS} =10V, V _{DD} =75V, R _G =3Ω, I _D =1A, R _L =75Ω		24.9		ns
t _r	Turn-On Rise Time			6.02		
t _{d(off)}	Turn-Off Delay Time			49.9		
t _f	Turn-Off Fall Time			20		

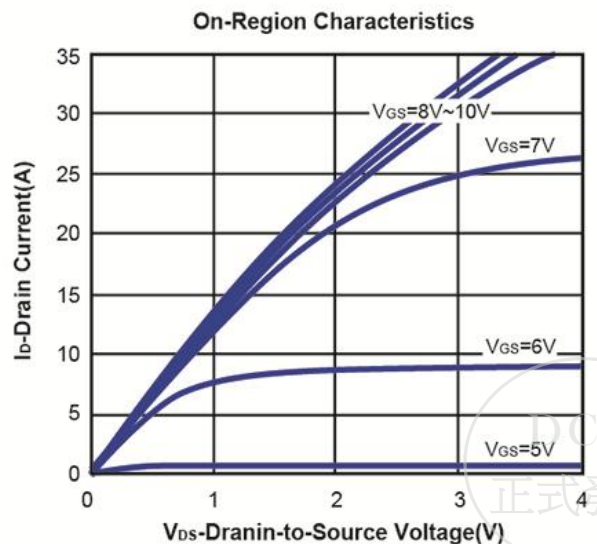
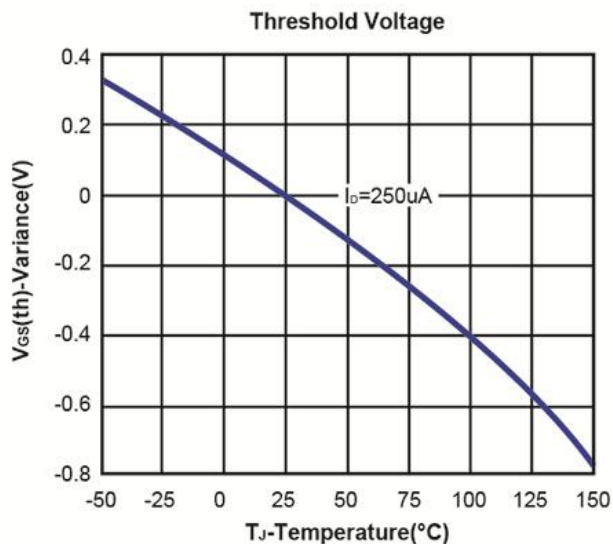
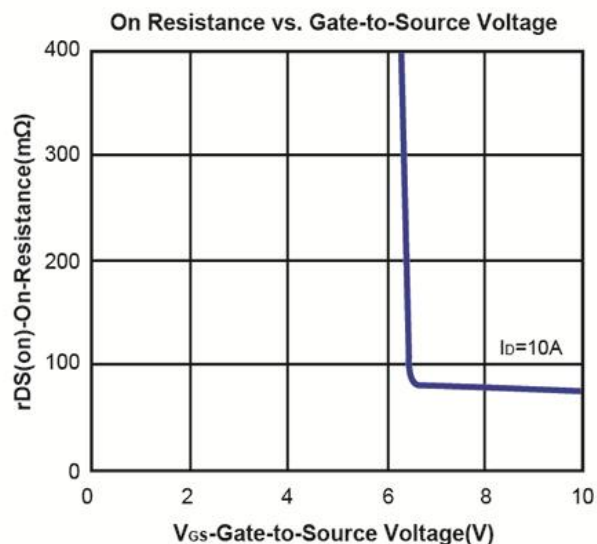
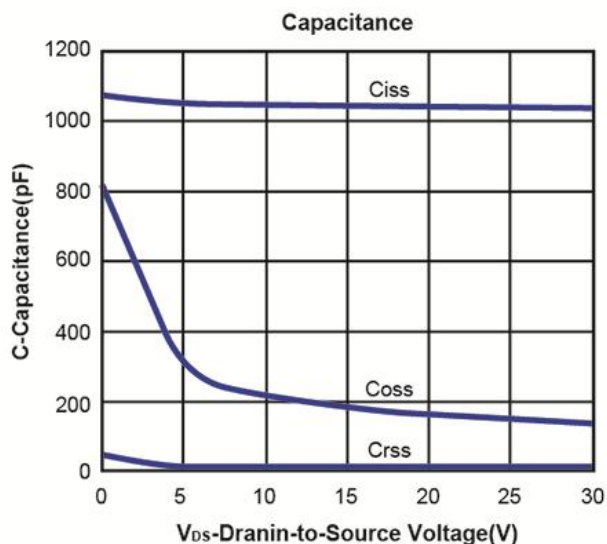
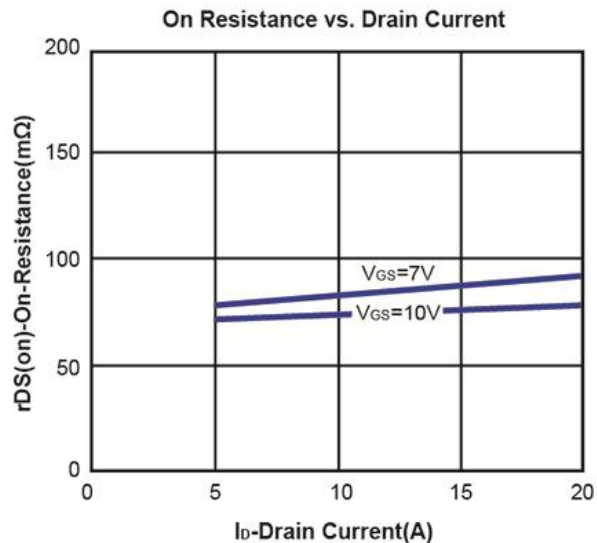
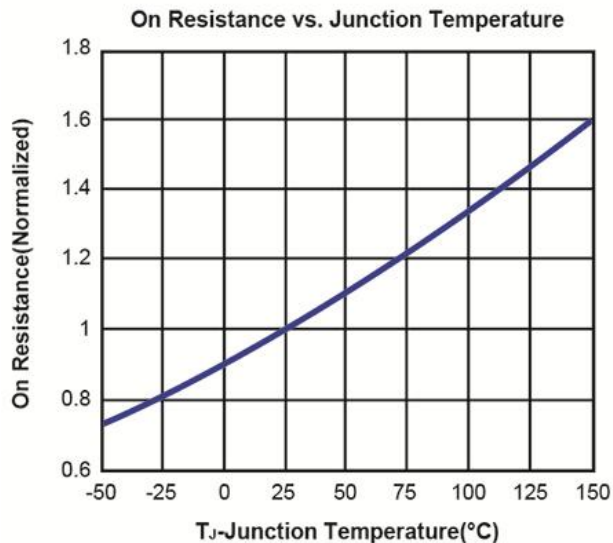
Notes: a. pulse test: pulse width ≤ 300us, duty cycle ≤ 2%, Guaranteed by design, not subject to production testing.

b. Matsuki Electric/ Force mos reserves the right to improve product design, functions and reliability without notice.

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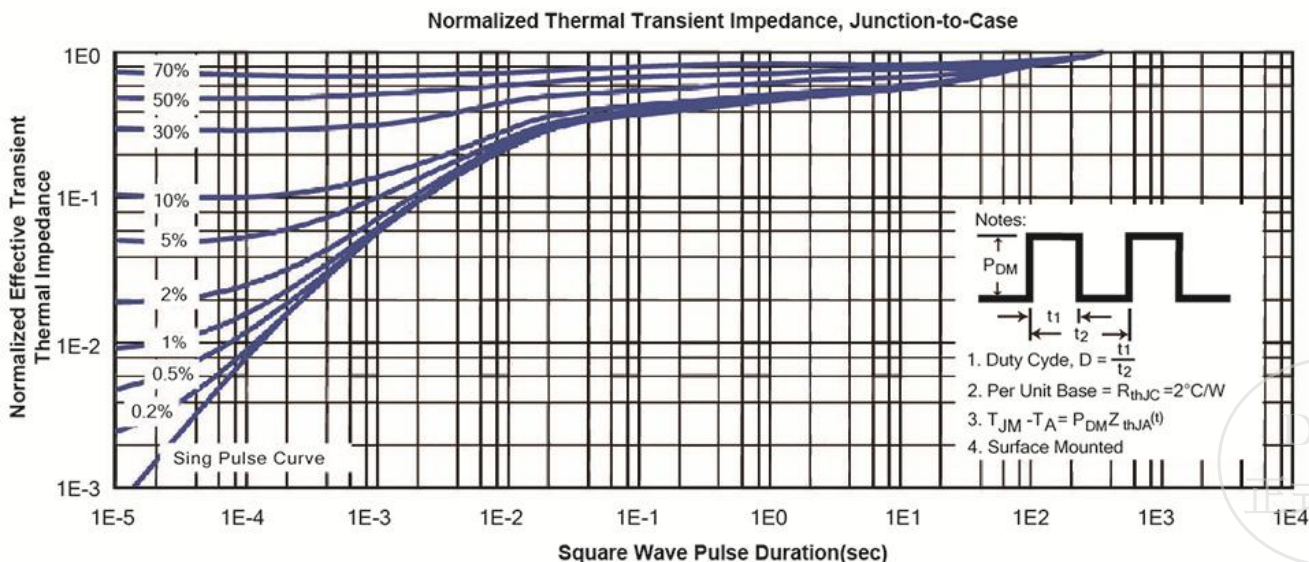
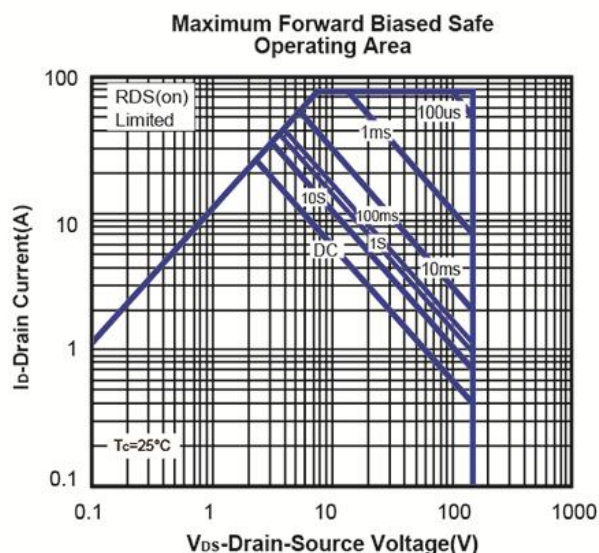
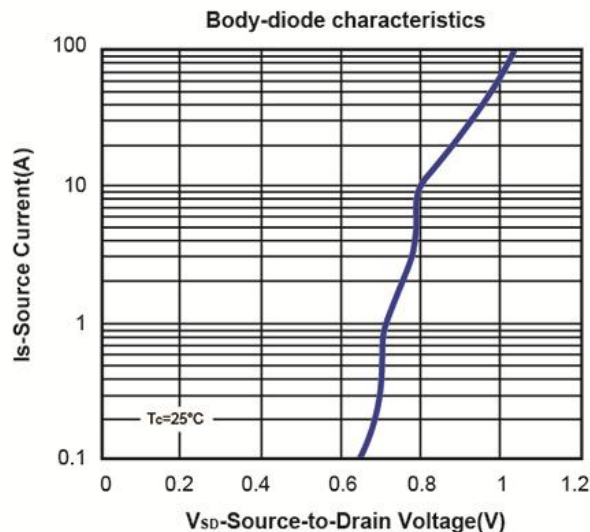
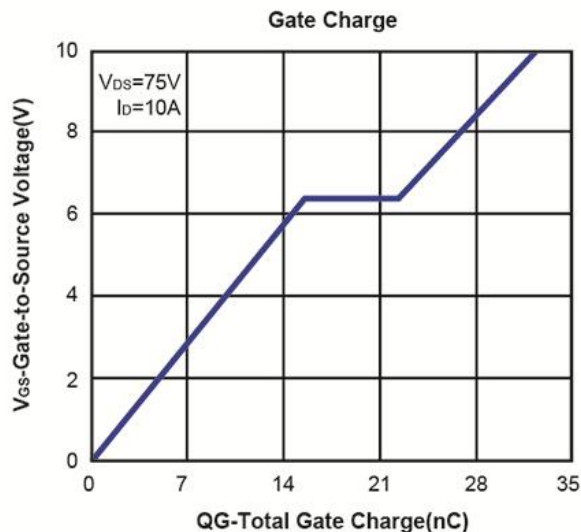
N- Channel 150V (D-S) MOSFET

Typical Characteristics (T_J =25°C Noted)

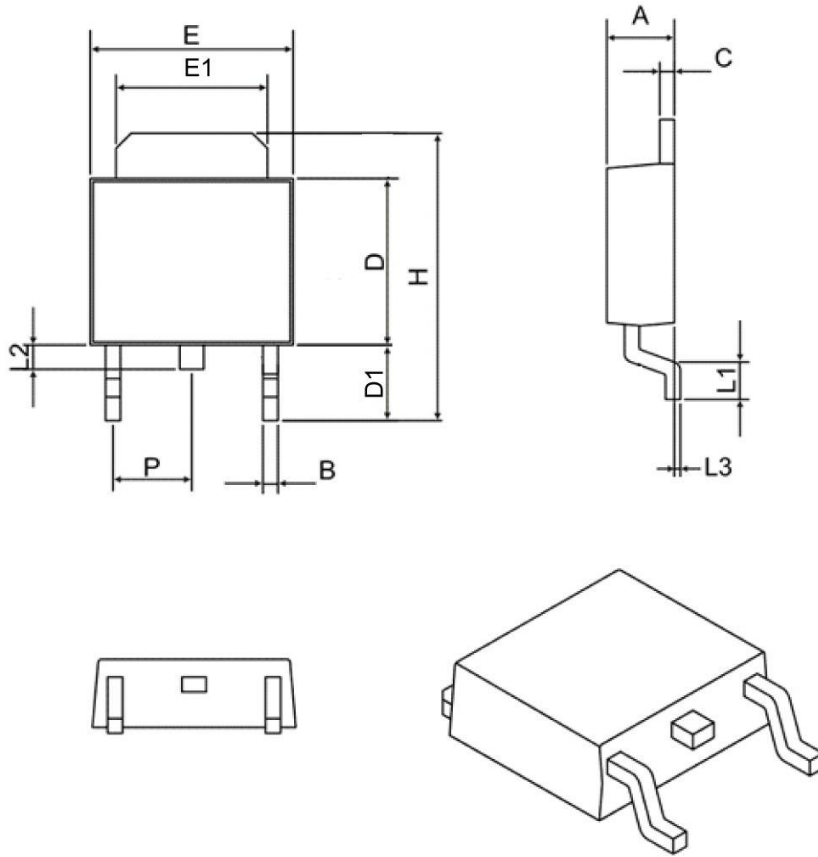


N- Channel 150V (D-S) MOSFET

Typical Characteristics (T_J = 25°C Noted)



TO-252 Package Outline



SYMBOL	MIN	MAX
A	2.10	2.50
B	0.40	0.90
C	0.40	0.90
D	5.30	6.30
D1	2.20	2.90
E	6.30	6.75
E1	4.80	5.50
L1	0.90	1.80
L2	0.50	1.10
L3	0.00	0.20
H	8.90	10.40
P	2.30 BSC	

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