MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

AON3419-MS

Product specification





Description

The AON3419-MS uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge .Thisdevice is well suited for high current load applications.

Features

V_{DS}=-30V,I_D=-32A

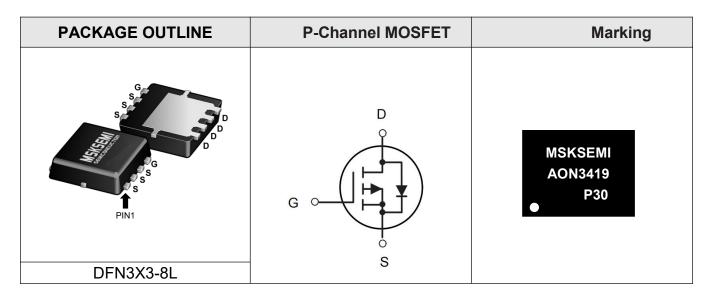
 $R_{\text{DS(ON)}}{<}12m\Omega \textcircled{O} V_{\text{GS}}{=}{-}10V$

 $R_{\text{DS(ON)}}$ < 18m Ω @ V_{GS}=-4.5V

Application

- High side switch for full bridge converter
- DC/DC converter for LCD display

Reference News



Absolute Maximum Ratings (TC=25°C unless otherwise specified)

Symbol	Parameter .	Rating	Units
VDS	Drain-Source Voltage	-30	V
VGS	Gate-Source Voltage	<u>+</u> 25	V
I□@T₄=25℃	Drain Current ³ , V _{GS} @ 10V	-32	A
ID@TA=70°C	Drain Current ³ , V _{GS} @ 10V	-9.8	A
IDM	Pulsed Drain Current ¹	-65	A
PD@TA=25°C	Total Power Dissipation	3.57	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range -55		°C
Rthj-c	Maximum Thermal Resistance, Junction-case	6 °C/W	
Rthj-a	Maximum Thermal Resistance, Junction- ambient ³	35 °C/W	



Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	Vgs =0V, Id =-250uA	-30	-	-	V
RDS(ON)	Static Drain-Source On- Resistance ²	V _{Gs} =- 10V, I _D =- 15A	-	10	12	mΩ
		V _{Gs} =-4.5V, I _D =- 10A	-	14	18	mΩ
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	- 1	1.95	-2.5	V
g fs	Forward Transconductance	V _{DS} =- 10V, I _D =-6A	-	19	-	S
IDSS	Drain-Source Leakage Current	V _{DS} =-24V, V _{GS} =0V	-	-	-30	uA
IGSS	Gate- Source Leakage	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Qg	Total Gate Charge	I ⊳=- 15 A	-	12.5	24	nC
Qgs	Gate- Source Charge	V _{DS} =- 15V	-	5.4	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	5	-	nC
td(on)	Turn-on Delay Time	V _{DS} =- 15V	-	4.4	-	ns
tr	Rise Time	l₀=- 15A	-	11.2	-	ns
td(off)	Turn-off Delay Time	R _G =3.3Ω	-	34	-	ns
t _f	Fall Time	Vgs=- 10V	-	18	-	ns
Ciss	Input Capacitance	V _{GS} =0V	-	1345	2000	pF
Coss	Output Capacitance	V _{DS} =- 15V	-	194	-	pF
Crss	Reverse Transfer Capacitance	f=1.0MHz.	-	158	_	pF
trr	Reverse Recovery Time	 Is=- 15Α, V _{GS} =0V, dI/dt=100Α/μs	-	12.4	-	ns
Qrr	Reverse Recovery Charge		-	5	-	nC

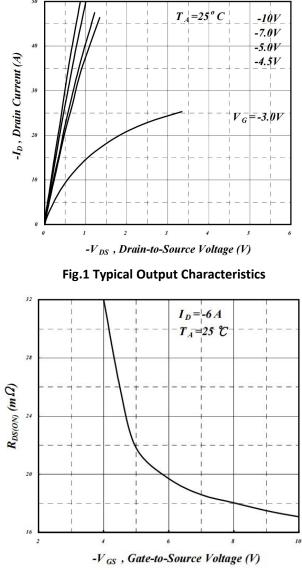
Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test



Typical Characteristics





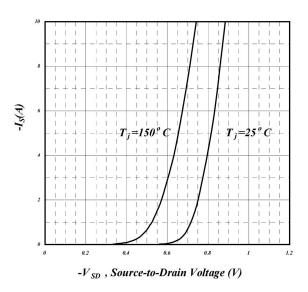


Fig 5. Forward Characteristic of Reverse Diode

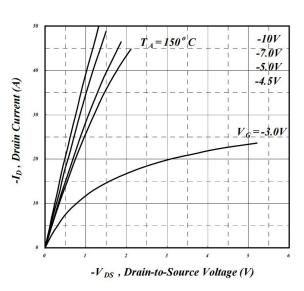


Fig 2. Typical Output Characteristics

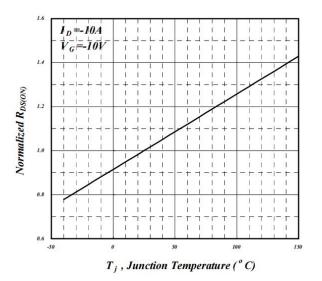


Fig 4. Normalized On-Resistance v.s. Junction Temperature

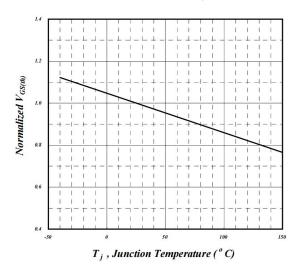


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



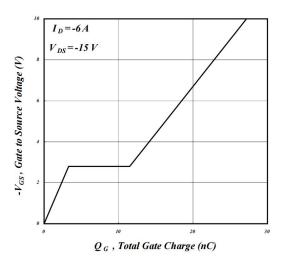


Fig 7. Gate Charge Characteristics

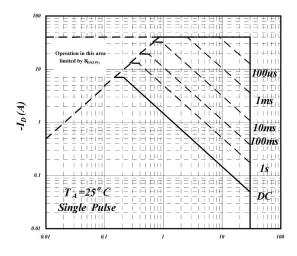


Fig 9. Maximum Safe Operating Area

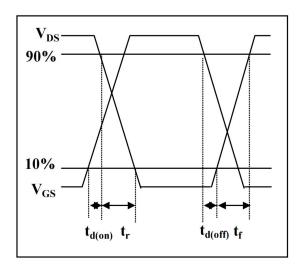


Fig 11. Switching Time Waveform

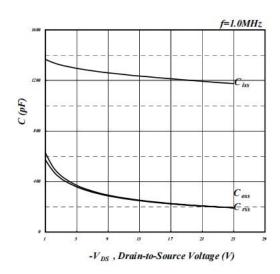


Fig 8. Typical Capacitance Characteristics

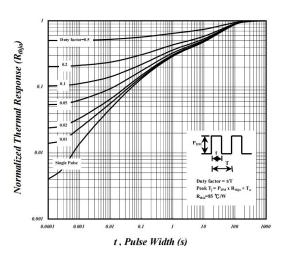


Fig 10. Effective Transient Thermal Impedance

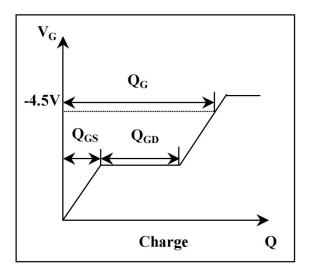
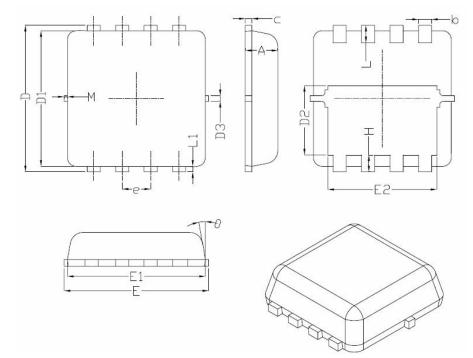


Fig 12. Gate Charge Waveform



DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
Α	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	_	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	_	0.13	-	
М	*	*	0.15	
θ		10 [°]	12 [°]	

REEL SPECIFICATION

P/N	PKG	QTY
AON3419-MS	DFN3X3-8L	5000

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