MSKSEMI 美森科













ESD

TVS

TSS

MOV

GDT

PLED

AON7430-MS

Product specification





Description

The AON7430-MS uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

Features

VDS = 30V ID = 30 A

 $RDS(ON) < 13m\Omega @ VGS=10V$

Application

- Battery protection
- Load switch
- Uninterruptible power supply

Reference News

PACKAGE OUTLINE	N-Channel MOSFET	Marking
PIN1	G	MSKSEMI AON7430 N30
DFN3X3-8L		

Absolute Maximum Ratings (TC=25 °C unless otherwise specified)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	30	V
VGS	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V ¹	30	A
Ib@Tc=100°C	Continuous Drain Current, V _{GS} @ 10V ¹	18	A
IDM	Pulsed Drain Current ²	55	A
EAS	Single Pulse Avalanche Energy ³	22.1	mJ
IAS	Avalanche Current	21	A
Pp@Tc=25°C	Total Power Dissipation ⁴	20	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient ¹	75	°C/ W
ReJC	Thermal Resistance Junction-Case ¹	6	°C/ W



Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	Vgs=0V , Id=250uA	30			V
∆BVbss/∆TJ	BVDSS Temperature Coefficient	Reference to 25°C , ID=1mA		0.022		V/°C
	Static Drain-Source On-Resistance ²	Vgs=10V , Id=10A		8	13	
Rds(ON)		V _{GS} =4.5V , I _D =5A		12	20	mΩ
VGS(th)	Gate Threshold Voltage		1.0		2.5	V
riangle VGS(th)	VGs(th) Temperature Coefficient	Vgs=Vds , Id =250uA		-5.1		Mv/°C
lano	Drain Source Lookage Current	VDS=24V , VGS=0V , TJ=25°C			1	•
ldss	Drain-Source Leakage Current	Vds=24V , Vgs=0V , Tj=55°C			5	uA
lgss	Gate-Source Leakage Current	$V_{GS}=\pm20V$, $V_{DS}=0V$			± 100	nA
gfs	Forward Transconductance	Vos=5V , Io=1A		4.5		S
Rg	Gate Resistance	Vos=0V , Vgs=0V , f=1MHz		2.5		Ω
Qg	Total Gate Charge (4.5V)	V _{DS} =20V , V _{GS} =4.5V , I _D =10A		7.2		
Qgs	Gate-Source Charge			1.4		nC
\mathbf{Q}_{gd}	Gate-Drain Charge			2.2		
Td(on)	Turn-On Delay Time			4.1		
Tr	Rise Time			9.8		
Td(off)	Turn-Off Delay Time			15.5		ns
Tf	Fall Time			6.0		
Ciss	Input Capacitance			572		
Coss	Output Capacitance	Vos=15V , Vos=0V , f=1MHz		81		pF
Crss	Reverse Transfer Capacitance			65		
ls	Continuous Source Current ^{1,5}				28	А
lsм	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			55	А
Vsd	Diode Forward Voltage ²	Vgs=0V , Is=1A , Tj=25°C			1.2	V

Note :

1. The data tested by surface mounted on a 1 inch $_2$ FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width $\,\leq\,$ 300us , duty cycle $\,\leq\,$ 2%

3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS}=21A

4 .The power dissipation is limited by 150 °C junction temperature 5.The data is theoretically the same as I_D and

 I_DM , in real applications , should be limited by total power dissipation.



10

10

Typical Characteristics

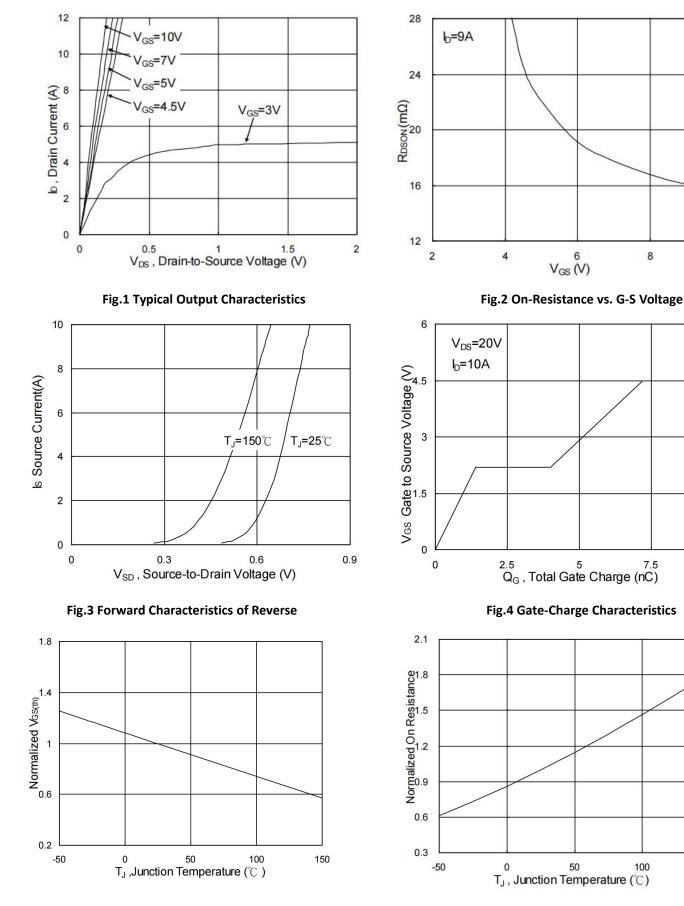


Fig.5 Normalized VGS(th) vs. TJ

Fig.6 Normalized RDSON vs. TJ

150



AON7430-MS

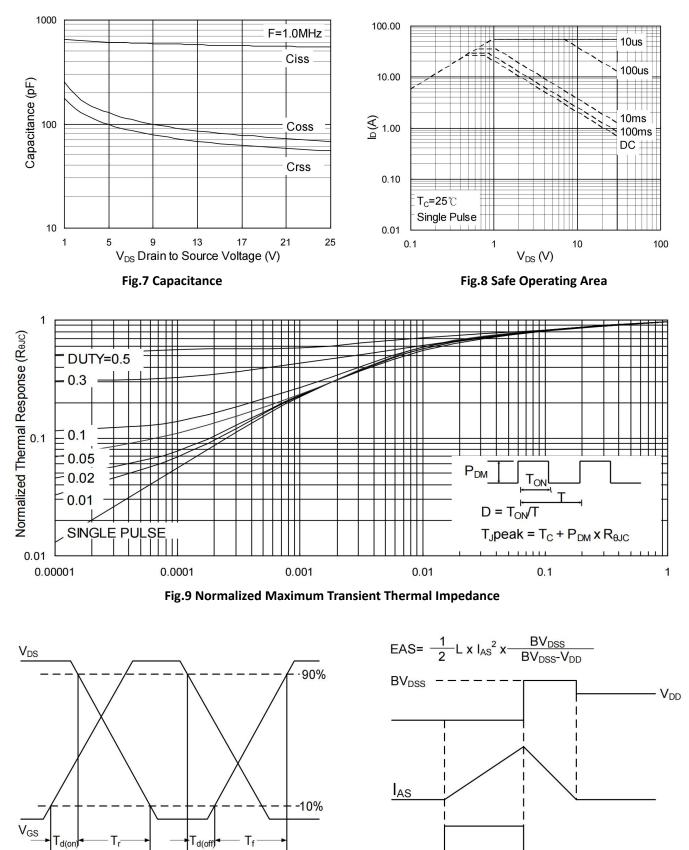


Fig.10 Switching Time Waveform

Toff

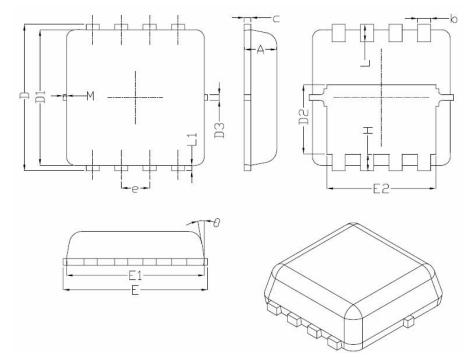
Ton

Fig.11 Unclamped Inductive Switching Waveform

 V_{GS}



DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
Α	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
e	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	_	
М	*	*	0.15	
θ		10 [°]	12 [°]	

REEL SPECIFICATION

P/N	PKG	QTY
AON7430-MS	DFN3X3-8L	5000



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