# MSKSEMI 美森科







TSS



MOV



GDT



DIED

**AONR36368-MS** 

Product specification





## **Description**

The AONR36368-MSuses advanced trench technology to provide excellent RDS(ON),

low gate charge and operation with gate voltages as low as 4.5V.

This device is suitable for use as a Battery protection or in other Switching application.

#### **Features**

V<sub>DS</sub> = 30V I<sub>D</sub> =60 A

 $R_{DS(ON)}$  <  $8m\Omega$  @  $V_{GS}$ =-10V

# **Application**

- Battery protection
- Load switch
- Uninterruptible power supply

# **Reference News**

PACKAGE OUTLINE	N-Channel MOSFET	Marking
DFN3X3-8L		60N03 • xxxx



# **Absolute Maximum Ratings** (TC=25 $^{\circ}$ C unless otherwise specified)

Symbol	Parameter	Rating	Units
Vps	Drain-Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
Ib@Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V¹	60	Α
In@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V¹	20	А
In@Ta=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	А
ID@Ta=70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	12	А
Ірм	Pulsed Drain Current <sup>2</sup>	140	А
EAS	Single Pulse Avalanche Energy <sup>3</sup>	115.2	mJ
las	Avalanche Current	48	Α
P <b></b>	Total Power Dissipation <sup>4</sup>	59	W
Pd@Ta=25°C	Total Power Dissipation <sup>4</sup>	2	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-ambient <sup>1</sup>	62	°C/W
Reuc	Thermal Resistance Junction-Case <sup>1</sup>	2.1	°C/W



# Electrical Characteristics (TJ=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVpss	Drain- Source Breakdown Voltage	Vgs=0V , ID=250uA	30			V
△BVɒss/△Tɹ	BVDSS Temperature Coefficient	Reference to 25 °C , ID=1mA		0.027		V/℃
		Vgs=10V , ID=20A		6	8	
Rds(on)	Static Drain-Source On- Resistance <sup>2</sup>	Vgs=4.5V , Ip=10A		7.5	10	mΩ
$V_{GS(th)}$	Gate Threshold Voltage		1.2		2.5	V
$\triangle V$ GS(th)	V <sub>GS(th)</sub> Temperature Coefficien			-5.8		Mv/℃
	Drain-Source Leakage	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25C			1	
loss	Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55 ℃			5	uA
lgss	Gate- Source Leakage Currer	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$			± 100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		43		S
R <sub>9</sub>	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7		Ω
$Q_g$	Total Gate Charge (4.5V)			20		
Qgs	Gate- Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V		7.6		nC
$Q_{gd}$	Gate- Drain Charge	, l₀=15A		7.2		IIC
T <sub>d</sub> (on)	Turn- On Delay Time			7.8		
Tr	Rise Time	VDD=15V , VGS=10V		15		
T <sub>d(off)</sub>	Turn- Off Delay Time	, Rg=3.3 Ω		37.3		ns
Tf	Fall Time	ID=15A		10.6		
Ciss	Input Capacitance			2295		
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V ,		267		
Crss	Reverse Transfer Capacitano	f=1MHz		210		pF
ls	Continuous Source Current <sup>1, 6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force			40	Α
lsм	Pulsed Source Current <sup>2,6</sup>	Current			140	Α
VsD	Diode Forward Voltage <sup>2</sup>	V <sub>G</sub> s=0V , I <sub>S</sub> =1A , T <sub>J</sub> =25 ℃			1	V

#### **Diode Characteristics**

#### Note:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\, \leq \, 2\%$
- 3 .The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}$ =25V, $V_{\text{GS}}$ =10V,L=0.1mH,Ias=34A
- 4.The power dissipation is limited by 150  $\!\!\!\!\!^{\,\mathrm{C}}$  junction temperature
- 5 .The data is theoretically the same as  $l_D$  and  $l_{DM}$ , in real applications , should be limited by total power dissipation.



# **Typical Characteristics**

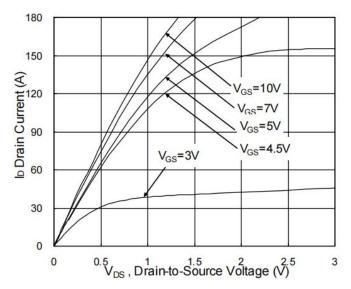


Fig.1 Typical Output Characteristics

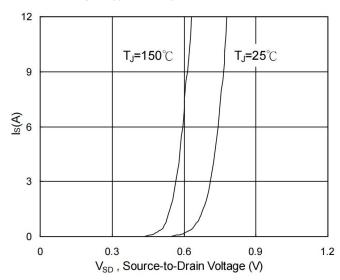


Fig.3 Forward Characteristics of Reverse

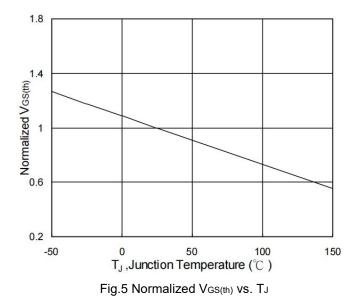


Fig.2 On-Resistance vs. G-S Voltage

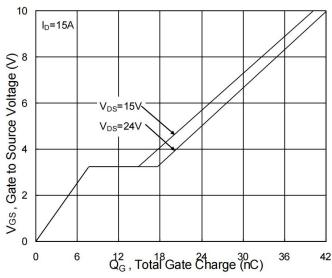


Fig.4 Gate-Charge Characteristics

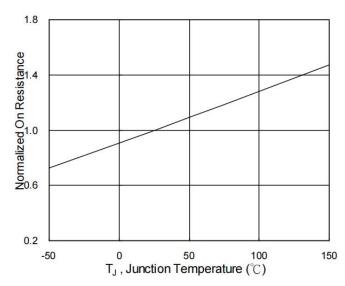
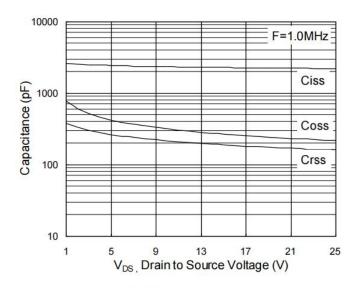


Fig.6 Normalized RDSON vs. TJ



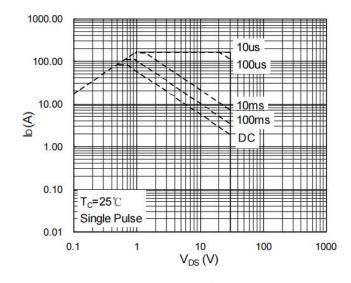


Fig.7 Capacitance

Fig.8 Safe Operating Area

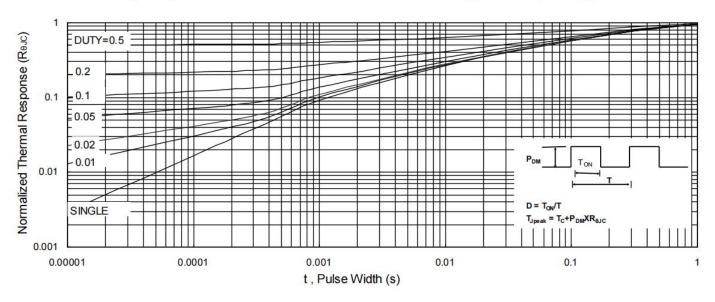


Fig.9 Normalized Maximum Transient Thermal Impedance

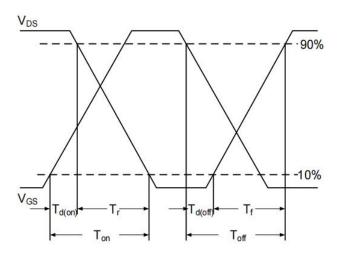


Fig.10 Switching Time Waveform

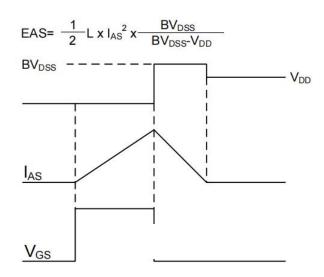
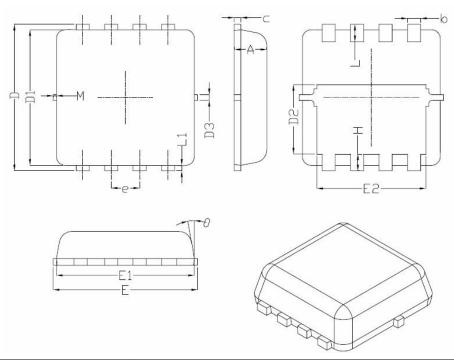


Fig.11 Unclamped Inductive Switching Waveform



# DFN3X3-8L Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
Α	0.70	0.75	0.80
b	0.25	0.30	0.35
С	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	_	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
е	0.65BSC		
Н	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12 <sup>°</sup>

# **REEL SPECIFICATION**

P/N	PKG	QTY
AONR36368-MS	DFN3X3-8L	5000



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DMN1017UCP3-7 EFC2J004NUZTDG P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 DMC2700UDMQ-7 DMN2080UCB4-7
DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 IPS60R360PFD7SAKMA1
DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
MCQ7328-TP SSM3J143TU,LXHF DMN12M3UCA6-7 PJMF280N65E1\_T0\_00201 PJMF380N65E1\_T0\_00201
PJMF280N60E1\_T0\_00201 PJMF600N65E1\_T0\_00201 PJMF900N65E1\_T0\_00201