# MSKSEMI















**ESD** 

TVS

TSS

MOV

GDT

**PLED** 

# Broduct data sheet













### **FEATURES**

- **Fast Switching Speed**
- For General Purpose Switching Applications
- **High Conductance**

Marking: A6



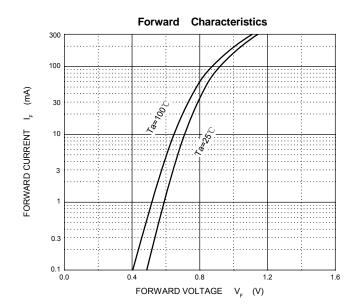
#### Maximum Ratings and Electrical Characteristics, Single Diode @Ta=25℃

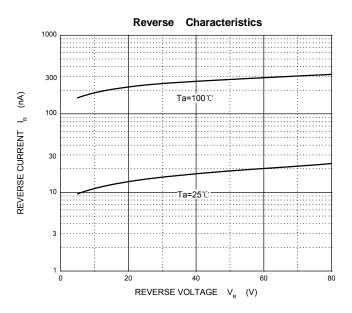
Parameter	Symbol	Limit	Unit
Non-Repetitive Peak Reverse Voltage	$V_{RM}$	100	V
Peak Repetitive Peak Reverse Voltage	$V_{RRM}$		
Working Peak Reverse Voltage	$V_{RWM}$	75	V
DC Blocking Voltage	$V_{R}$		
RMS Reverse Voltage	$V_{R(RMS)}$	53	V
Forward Continuous Current	I <sub>FM</sub>	300	mA
Average Rectified Output Current	lo	150	mA
Non-Repetitive Peak Forward Surge Current @t=8.3ms	I <sub>FSM</sub>	2.0	Α
Power Dissipation	Pd	225	mW
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	556	°C/W
Junction Temperature	Tj	150	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C

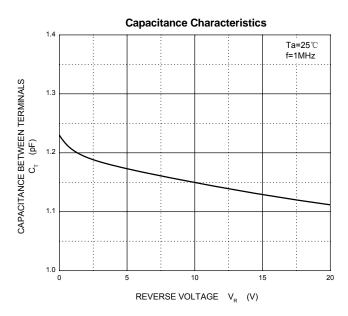
# **ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)**

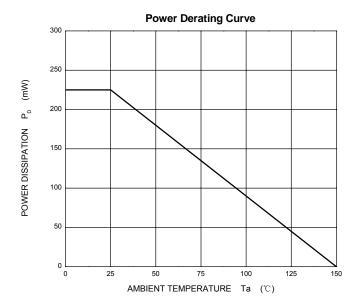
Parameter	Symbol	Test conditions	Min	Max	Unit
Reverse breakdown voltage	$V_{(BR)}$	I <sub>R</sub> = 100μA	75		V
Reverse voltage leakage current	I <sub>R</sub>	V <sub>R</sub> =75V		1	μΑ
Forward voltage	V <sub>F</sub>	I <sub>F</sub> =1mA I <sub>F</sub> =10mA I <sub>F</sub> =50mA I <sub>F</sub> =150mA		0.715 0.855 1 1.25	V
Diode capacitance	C <sub>D</sub>	V <sub>R</sub> =0, f=1MHz		2	pF
Reveres recovery time	t <sub>rr</sub>	$I_F=I_R=10\text{mA},I_{rr}=0.1\times I_R,$ $R_L=100\Omega$		6	ns







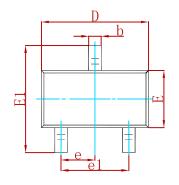


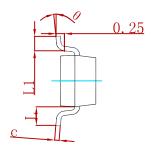


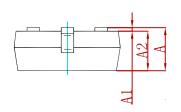




### **PACKAGE MECHANICAL DATA**

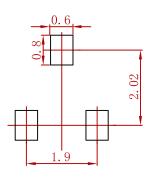






Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

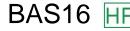
# **Suggested Pad Layout**



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

# **REEL SPECIFICATION**

P/N	PKG	QTY
BAS16	SOT-23	3000





Semiconductor Compiance



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